



SPEC[®] CFP2006 Result

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Sugon

SPECfp[®]_rate2006 = 1520

Sugon I840-G20 (Intel Xeon E7-8890 v3)

SPECfp_rate_base2006 = 1480

CPU2006 license: 9046

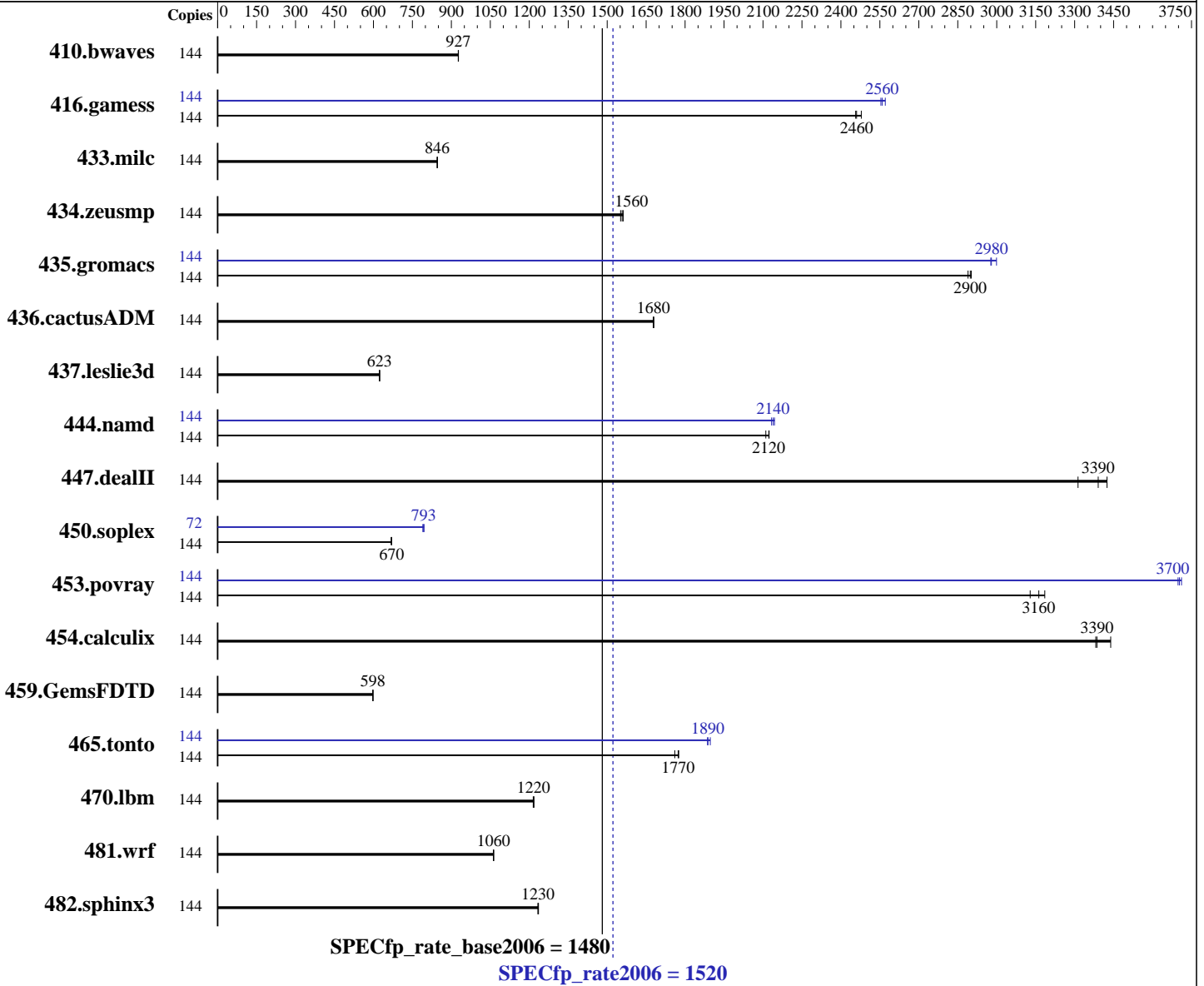
Test date: Nov-2015

Test sponsor: Sugon

Hardware Availability: Sep-2015

Tested by: Sugon

Software Availability: Sep-2015



Hardware

CPU Name: Intel Xeon E7-8890 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 72 cores, 4 chips, 18 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64)
 3.12.28-4-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 5 (Multi-user)

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L3 Cache: 45 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
 Disk Subsystem: 4 x 250 GB SATA, RAID10
 Other Hardware: None

Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	144	2112	927	2112	927	2110	927	144	2112	927	2112	927	2110	927		
416.gamess	144	1148	2460	1138	2480	1146	2460	144	1102	2560	1104	2550	1097	2570		
433.milc	144	1563	846	1564	845	1561	847	144	1563	846	1564	845	1561	847		
434.zeusmp	144	845	1550	839	1560	841	1560	144	845	1550	839	1560	841	1560		
435.gromacs	144	355	2900	354	2900	356	2890	144	343	3000	345	2980	345	2980		
436.cactusADM	144	1025	1680	1026	1680	1024	1680	144	1025	1680	1026	1680	1024	1680		
437.leslie3d	144	2169	624	2173	623	2171	623	144	2169	624	2173	623	2171	623		
444.namd	144	547	2110	544	2120	544	2120	144	539	2140	540	2140	541	2130		
447.dealII	144	481	3420	497	3310	486	3390	144	481	3420	497	3310	486	3390		
450.soplex	144	1793	670	1798	668	1791	670	72	757	793	760	790	755	795		
453.povray	144	245	3130	242	3160	241	3190	144	206	3710	207	3700	207	3700		
454.calculix	144	345	3440	351	3390	351	3380	144	345	3440	351	3390	351	3380		
459.GemsFDTD	144	2557	598	2555	598	2554	598	144	2557	598	2555	598	2554	598		
465.tonto	144	798	1780	805	1760	799	1770	144	747	1900	750	1890	751	1890		
470.lbm	144	1628	1220	1624	1220	1626	1220	144	1628	1220	1624	1220	1626	1220		
481.wrf	144	1514	1060	1515	1060	1513	1060	144	1514	1060	1515	1060	1513	1060		
482.sphinx3	144	2272	1240	2275	1230	2275	1230	144	2272	1240	2275	1230	2275	1230		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Set Power Technology to Custom
 Set Package C State limit C0/C1 state
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Platform Notes (Continued)

Set DDR speed to 1600 MHz
 Set Hyper-Threading to Enabled
 Baseboard Management Controller used to adjust the fan speed to performance mode
 Sysinfo program /home/cpu2006/config/sysinfo.rev6914
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
 running on localhost Sat Nov 28 07:56:42 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) CPU E7-8890 v3 @ 2.50GHz
 4 "physical id"s (chips)
 144 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores     : 18
  siblings      : 36
  physical 0:   cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
  physical 1:   cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
  physical 2:   cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
  physical 3:   cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size     : 46080 KB

```

From /proc/meminfo

```

MemTotal:      529304188 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

/usr/bin/lsb_release -d

SUSE Linux Enterprise Server 12

From /etc/*release* /etc/*version*

```

SuSE-release:
 SUSE Linux Enterprise Server 12 (x86_64)
 VERSION = 12
 PATCHLEVEL = 0
 # This file is deprecated and will be removed in a future service pack or
 release.
 # Please check /etc/os-release for details about this release.

os-release:
 NAME="SLES"
 VERSION="12"
 VERSION_ID="12"
 PRETTY_NAME="SUSE Linux Enterprise Server 12"
 ID="sles"
 ANSI_COLOR="0;32"
 CPE_NAME="cpe:/o:suse:sles:12"

```

uname -a:

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Platform Notes (Continued)

```
Linux localhost 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 5 Nov 27 17:41
```

```
SPEC is set to: /home/cpu2006
```

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3        ext4  875G  535G  340G  62% /home
```

```
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS American Megatrends Inc. 1.01 08/26/2015
```

```
Memory:
```

```
32x Hynix HMA42GR7MFR4N-TFTD 16 GB 2 rank 2133 MHz, configured at 1600 MHz
16x NO DIMM NO DIMM
```

```
(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
```

```
Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
memory using RedHat EL 7.1
```

```
Transparent Huge Pages enabled with:
```

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

```
Filesystem page cache cleared with:
```

```
echo 1 > /proc/sys/vm/drop_caches
```

```
runspec command invoked through numactl i.e.:
```

```
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

```

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Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -D_FILE_OFFSET_BITS=64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

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Peak Optimization Flags (Continued)

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-HSW-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-HSW-revB.xml>



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Tested with SPEC CPU2006 v1.2.
Report generated on Tue Dec 15 16:46:15 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 15 December 2015.