



SPEC® CFP2006 Result

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Supermicro

Supermicro C7Z170-M motherboard
(C7Z170-M, Intel Core i5-6500T)

SPECfp[®]_rate2006 = 161

SPECfp_rate_base2006 = 157

CPU2006 license: 001176

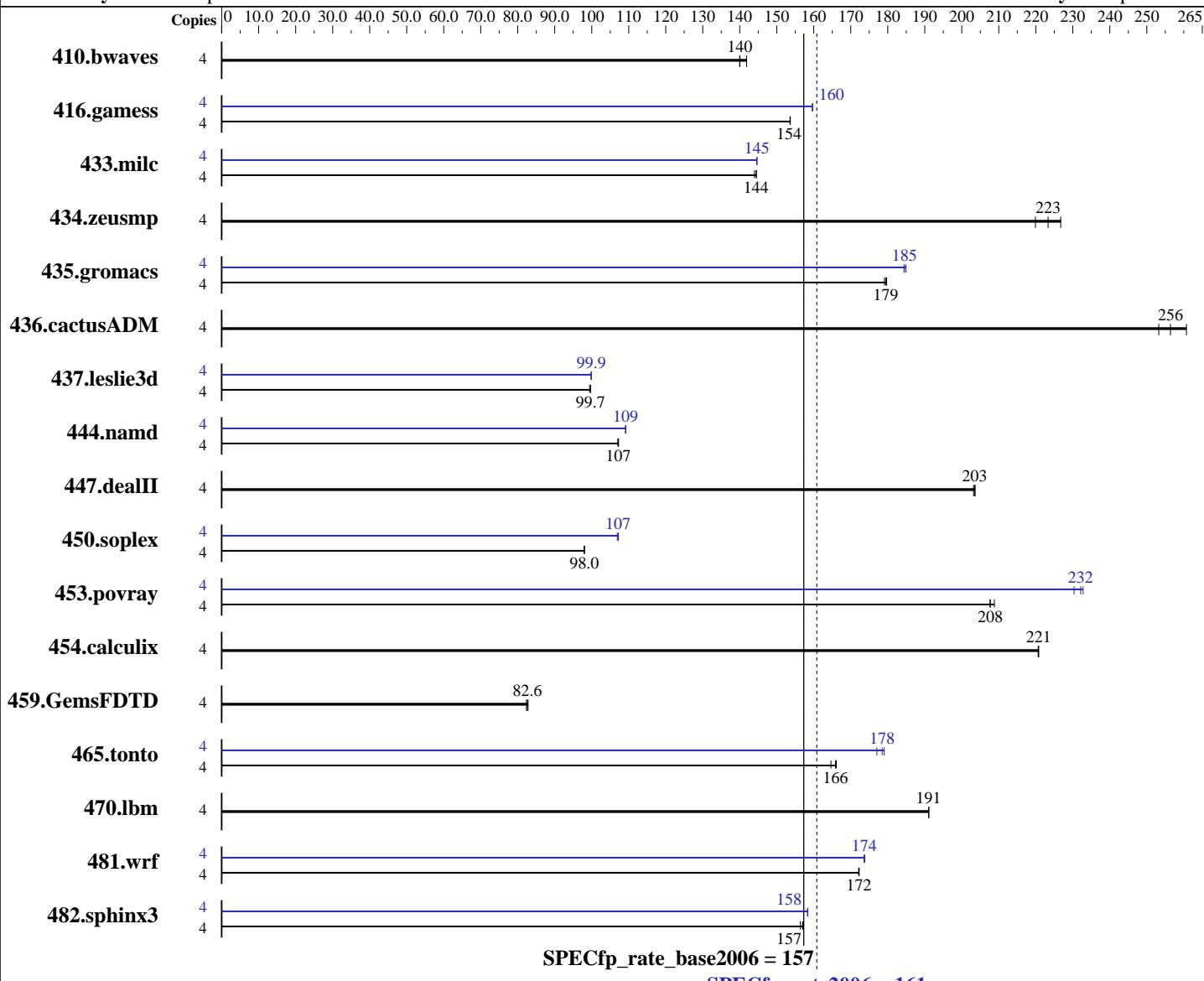
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Nov-2015

Hardware Availability: Sep-2015

Software Availability: Sep-2014



Hardware

CPU Name: Intel Core i5-6500T
CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
CPU MHz: 2500
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 7.1, Kernel 3.10.0-229.el7.x86_64
Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: xfs
System State: Run level 3 (multi-user)

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L3 Cache:	6 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (4 x 4 GB 1Rx8 PC4-2666P-U, running at 2133 MHz)
Disk Subsystem:	1 x 200 GB SATA III SSD
Other Hardware:	None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	383	142	388	140	<u>388</u>	<u>140</u>	4	383	142	388	140	<u>388</u>	<u>140</u>
416.gamess	4	<u>510</u>	<u>154</u>	510	154	510	154	4	491	160	<u>491</u>	<u>160</u>	491	160
433.milc	4	255	144	<u>254</u>	<u>144</u>	254	144	4	254	145	<u>254</u>	<u>145</u>	254	145
434.zeusmp	4	166	220	161	227	<u>163</u>	<u>223</u>	4	166	220	161	227	<u>163</u>	<u>223</u>
435.gromacs	4	159	180	<u>159</u>	<u>179</u>	159	179	4	154	185	<u>155</u>	<u>185</u>	155	184
436.cactusADM	4	183	261	189	253	<u>186</u>	<u>256</u>	4	183	261	189	253	<u>186</u>	<u>256</u>
437.leslie3d	4	<u>377</u>	<u>99.7</u>	378	99.4	377	99.7	4	376	99.9	377	99.8	<u>376</u>	<u>99.9</u>
444.namd	4	300	107	299	107	<u>299</u>	<u>107</u>	4	294	109	<u>294</u>	<u>109</u>	294	109
447.dealII	4	<u>225</u>	<u>203</u>	225	204	225	203	4	<u>225</u>	<u>203</u>	225	204	225	203
450.soplex	4	341	97.9	340	98.0	<u>340</u>	<u>98.0</u>	4	312	107	<u>311</u>	<u>107</u>	311	107
453.povray	4	102	208	102	209	<u>102</u>	<u>208</u>	4	91.4	233	92.4	230	<u>91.7</u>	<u>232</u>
454.calculix	4	<u>149</u>	<u>221</u>	149	221	150	221	4	<u>149</u>	<u>221</u>	149	221	150	221
459.GemsFDTD	4	515	82.4	<u>514</u>	<u>82.6</u>	513	82.8	4	515	82.4	<u>514</u>	<u>82.6</u>	513	82.8
465.tonto	4	239	165	237	166	<u>237</u>	<u>166</u>	4	220	179	222	177	<u>221</u>	<u>178</u>
470.lbm	4	288	191	288	191	<u>288</u>	<u>191</u>	4	288	191	288	191	<u>288</u>	<u>191</u>
481.wrf	4	259	172	260	172	<u>259</u>	<u>172</u>	4	257	174	<u>257</u>	<u>174</u>	257	174
482.sphinx3	4	<u>497</u>	<u>157</u>	496	157	499	156	4	492	158	492	158	<u>492</u>	<u>158</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"



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Platform Notes

As tested, the system used a Supermicro CSE-731i-300B chassis.

The chassis is configured with a PWS-305-PQ power supply, 1 SNK-P0046A4 heatsink, as well as 1 FAN-0108L4 rear cooling fan.

Sysinfo program /usr/cpu2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on C7Z170-01 Sat Nov 14 15:30:41 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Core(TM) i5-6500T CPU @ 2.50GHz
        1 "physical id"s (chips)
        4 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The
    following excerpts from /proc/cpuinfo might not be reliable. Use with
    caution.)
        cpu cores : 4
        siblings : 4
        physical 0: cores 0 1 2 3
    cache size : 6144 KB
```

```
From /proc/meminfo
    MemTotal:       16334764 kB
    HugePages_Total:      0
    Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.1 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.1"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server
```

```
uname -a:
Linux C7Z170-01 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38 EST 2015
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Nov 11 03:07

```
SPEC is set to: /usr/cpu2006
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sda2        xfs   183G   31G  152G  17% /
Additional information from dmidecode:
```

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Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. T20151015150001 10/15/2015

Memory:

4x 0420 F4-2666C15-4GRR 4 GB 1 rank 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
    -auto-ilp32

```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: -xCORE-AVX2 -prof-gen(pass 1) -ipo -O3 -no-prec-div
    -prof-use(pass 2) -unroll2
```

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
    -fno-alias -auto-ilp32

```

```
447.dealII: basepeak = yes
```

```
450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
    -opt-malloc-options=3
```

```
453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
    -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
    -ansi-alias
```

Fortran benchmarks:

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Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revG.20141230.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revG.20141230.00.xml>

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