



SPEC® CINT2006 Result

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Supermicro

Supermicro C7H170-M motherboard
(C7H170-M, Intel Core i5-6400T)

SPECint®2006 = 52.8

SPECint_base2006 = 51.6

CPU2006 license: 001176

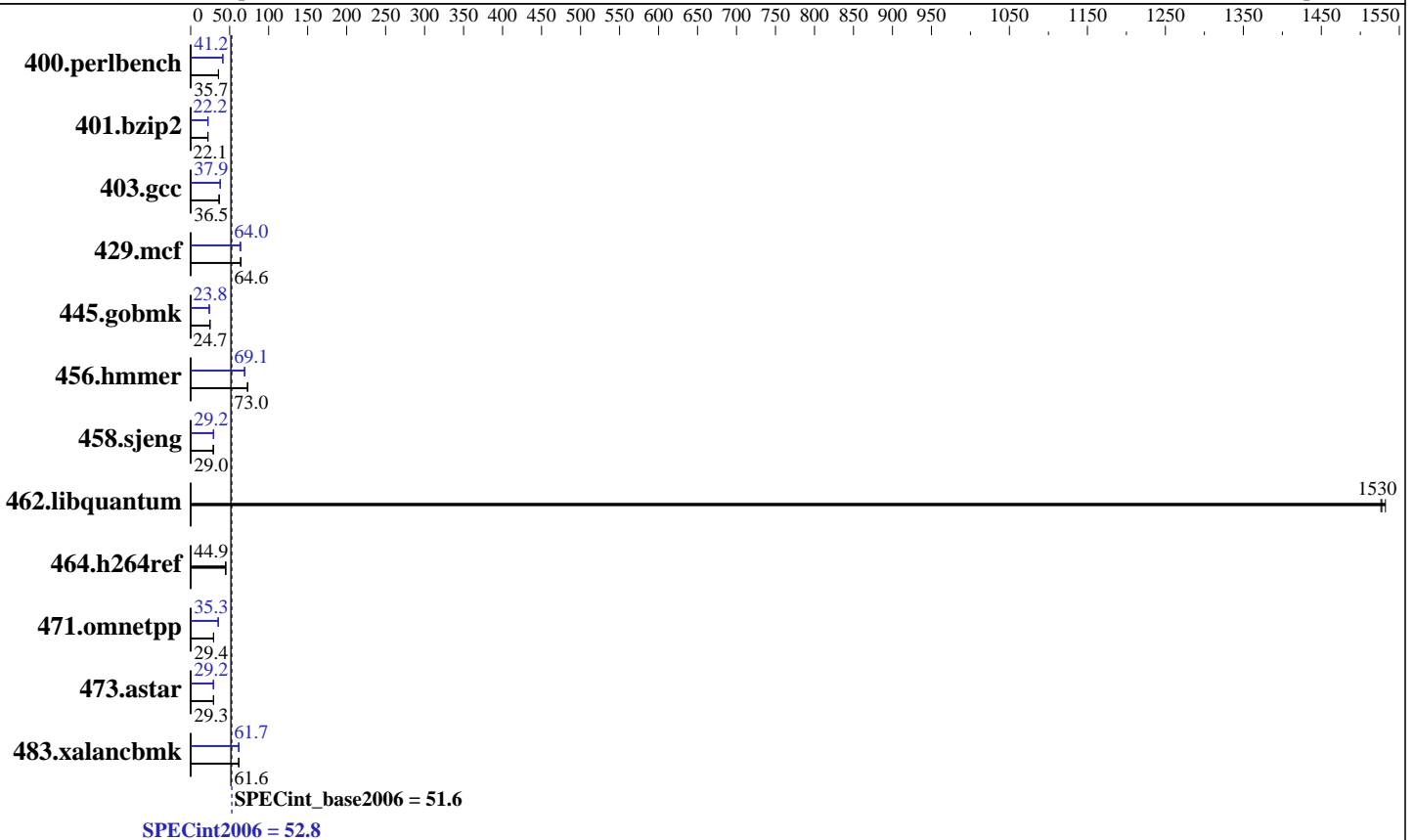
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Oct-2015

Hardware Availability: Sep-2015

Software Availability: Sep-2014



Hardware

CPU Name: Intel Core i5-6400T
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 6 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4 GB 1Rx8 PC4-2800R-U, running at 2133 MHz)
 Disk Subsystem: 1 x 200 GB SATA III SSD
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.1, Kernel 3.10.0-229.el7.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|----------------|-------------------|--------------------|--------------------|--------------------|-------------------|--------------------|-------------------|--------------------|--------------------|--------------------|-------------------|--------------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 274 | 35.6 | 274 | 35.7 | <u>274</u> | <u>35.7</u> | <u>237</u> | <u>41.2</u> | 237 | 41.2 | 237 | 41.2 |
| 401.bzip2 | <u>437</u> | <u>22.1</u> | 437 | 22.1 | 435 | 22.2 | 436 | 22.1 | 435 | 22.2 | <u>435</u> | <u>22.2</u> |
| 403.gcc | 220 | 36.6 | <u>220</u> | <u>36.5</u> | 221 | 36.5 | 213 | 37.8 | 212 | 38.0 | <u>213</u> | <u>37.9</u> |
| 429.mcf | 141 | 64.6 | <u>141</u> | <u>64.6</u> | 143 | 63.7 | 145 | 63.1 | 142 | 64.1 | <u>143</u> | <u>64.0</u> |
| 445.gobmk | 424 | 24.7 | <u>424</u> | <u>24.7</u> | 424 | 24.7 | 440 | 23.8 | 440 | 23.8 | <u>440</u> | <u>23.8</u> |
| 456.hammer | 128 | 72.9 | 128 | 73.1 | <u>128</u> | <u>73.0</u> | <u>135</u> | <u>69.1</u> | 135 | 69.3 | 135 | 69.1 |
| 458.sjeng | 417 | 29.0 | 417 | 29.0 | <u>417</u> | <u>29.0</u> | 414 | 29.2 | 414 | 29.2 | <u>414</u> | <u>29.2</u> |
| 462.libquantum | 13.5 | 1530 | <u>13.6</u> | <u>1530</u> | 13.6 | 1530 | 13.5 | 1530 | <u>13.6</u> | <u>1530</u> | 13.6 | 1530 |
| 464.h264ref | 492 | 45.0 | 493 | 44.9 | <u>493</u> | <u>44.9</u> | 492 | 45.0 | 493 | 44.9 | <u>493</u> | <u>44.9</u> |
| 471.omnetpp | <u>212</u> | <u>29.4</u> | 214 | 29.3 | 212 | 29.5 | 177 | 35.3 | <u>177</u> | <u>35.3</u> | 177 | 35.3 |
| 473.astar | 239 | 29.4 | <u>240</u> | <u>29.3</u> | 240 | 29.2 | <u>240</u> | <u>29.2</u> | 240 | 29.3 | 242 | 29.0 |
| 483.xalancbmk | 112 | 61.8 | 112 | 61.6 | <u>112</u> | <u>61.6</u> | 112 | 61.4 | 112 | 61.8 | <u>112</u> | <u>61.7</u> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

As tested, the system used a Supermicro CSE-743TQ-865B-SQ chassis.

The chassis is configured with a PWS-865-PQ power supply, 1 SNK-P0046A4 heatsink, as well as 1 FAN-0103L4 rear fan and 2 FAN-0104L4 chassis fan.

Sysinfo program /usr/cpu2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on C7H170-01 Thu Oct 22 14:48:50 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Core(TM) i5-6400T CPU @ 2.20GHz

1 "physical id"s (chips)

4 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 4

siblings : 4

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info@spec.org

<http://www.spec.org/>

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Platform Notes (Continued)

physical 0: cores 0 1 2 3
cache size : 6144 KB

From /proc/meminfo
MemTotal: 16334556 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.1 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.1"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server

uname -a:
Linux C7H170-01 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38 EST 2015
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 21 23:03

SPEC is set to: /usr/cpu2006
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 183G 24G 159G 13% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. T20151015150001 10/15/2015

Memory:
4x 0420 F4-2800C16-4GRK 4 GB 1 rank 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"
OMP_NUM_THREADS = "4"

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General Notes (Continued)

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

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Peak Optimization Flags (Continued)

456.hmmr: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revG.20141230.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revG.20141230.00.xml>



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