



SPEC® CINT2006 Result

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Hewlett-Packard Company

ProLiant DL580 Gen9
(2.80 GHz, Intel Xeon E7-8891 v3)

SPECint®2006 = 66.4

SPECint_base2006 = 64.5

CPU2006 license: 3

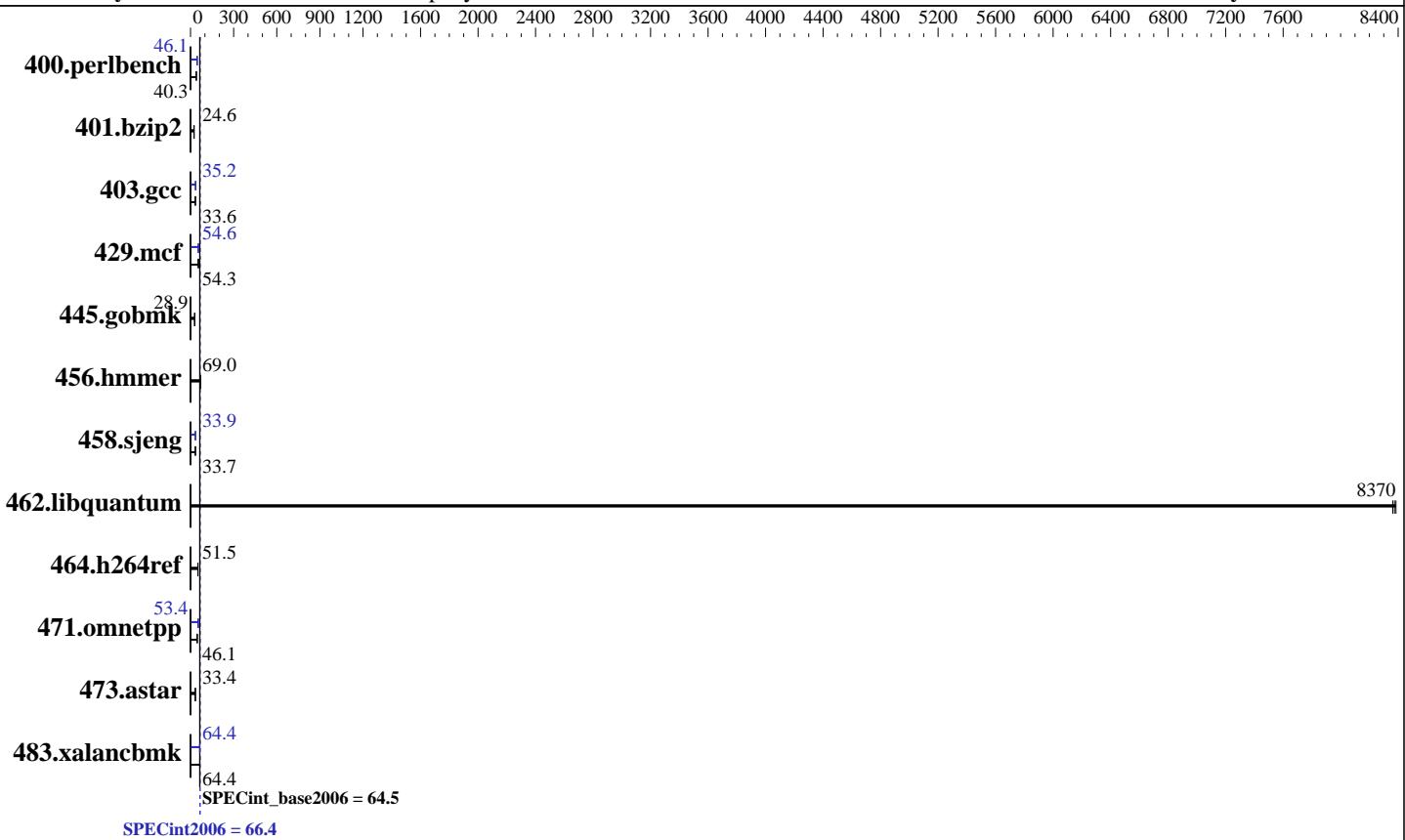
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Apr-2015

Hardware Availability: May-2015

Software Availability: Oct-2014



Hardware		Software	
CPU Name:	Intel Xeon E7-8891 v3	Operating System:	SUSE Linux Enterprise Server 12 (x86_64)
CPU Characteristics:	Intel Turbo Boost Technology up to 3.50 GHz	Compiler:	Kernel 3.12.28-4-default
CPU MHz:	2800	Auto Parallel:	C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
FPU:	Integrated	File System:	xfs
CPU(s) enabled:	40 cores, 4 chips, 10 cores/chip	System State:	Run level 3 (multi-user)
CPU(s) orderable:	2,4 chip	Base Pointers:	32/64-bit
Primary Cache:	32 KB I + 32 KB D on chip per core	Peak Pointers:	32/64-bit
Secondary Cache:	256 KB I+D on chip per core	Other Software:	Microquill SmartHeap V10.0
L3 Cache:	45 MB I+D on chip per chip		
Other Cache:	None		
Memory:	512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)		
Disk Subsystem:	1 x 400 GB SAS SSD, RAID 0		
Other Hardware:	None		



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	243	40.3	243	40.3	244	40.1	212	46.1	212	46.1	212	46.1
401.bzip2	393	24.6	392	24.6	392	24.6	393	24.6	392	24.6	392	24.6
403.gcc	240	33.6	240	33.5	239	33.6	229	35.2	229	35.2	233	34.5
429.mcf	164	55.6	170	53.5	168	54.3	168	54.2	167	54.6	167	54.6
445.gobmk	362	28.9	363	28.9	363	28.9	362	28.9	363	28.9	363	28.9
456.hmmer	135	69.0	136	68.8	135	69.0	135	69.0	136	68.8	135	69.0
458.sjeng	359	33.7	359	33.7	358	33.8	357	33.9	357	33.9	356	34.0
462.libquantum	2.48	8360	2.47	8380	2.47	8370	2.48	8360	2.47	8380	2.47	8370
464.h264ref	432	51.2	430	51.5	430	51.5	432	51.2	430	51.5	430	51.5
471.omnetpp	136	46.1	132	47.2	136	45.9	115	54.3	117	53.4	119	52.7
473.astar	209	33.6	210	33.4	211	33.3	209	33.6	210	33.4	211	33.3
483.xalancbmk	107	64.3	107	64.4	107	64.8	107	64.4	108	63.9	107	64.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

BIOS Configuration

Intel Hypthreading Options set to Disabled
Power Profile set to Custom
Power Regulator set to Static High Performance Mode
Minimum Processor Idle Power Core C-State set to C6 State
Minimum Processor Idle Power Package C-State set to Package C6 (retention) State
Energy/Performance Bias set to Maximum Performance
Collaborative Power Control set to Enabled
Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh

Sysinfo program /cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\\$ e3ffb8667b5a285932ceab81e28219e1
running on linux-l6gu Sat Apr 18 03:29:39 2015

This section contains SUT (System Under Test) info as seen by
Continued on next page



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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8891 v3 @ 2.80GHz
        4 "physical id"s (chips)
        40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 10
siblings : 10
physical 0: cores 0 1 2 4 6 8 17 19 20 23
physical 1: cores 0 1 2 4 6 8 17 19 20 23
physical 2: cores 0 1 2 4 6 8 17 19 20 23
physical 3: cores 0 1 2 4 6 8 17 19 20 23
cache size : 46080 KB
```

```
From /proc/meminfo
MemTotal:      529321520 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 0
    # This file is deprecated and will be removed in a future service pack or
    release.
    # Please check /etc/os-release for details about this release.
os-release:
    NAME="SLES"
    VERSION="12"
    VERSION_ID="12"
    PRETTY_NAME="SUSE Linux Enterprise Server 12"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12"
```

```
uname -a:
Linux linux-16gu 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Apr 17 22:17
```

```
SPEC is set to: /cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3        xfs   371G  8.3G  363G   3%  /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
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Platform Notes (Continued)

reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP U17 03/13/2015

Memory:

11x HP 752369-081 16 GB 2 rank 2133 MHz, configured at 1600 MHz

64x UNKNOWN NOT AVAILABLE

21x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2133 MHz, configured at 1600 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have two lines reading as:

11x HP 752369-081 16 GB 2 rank 2133 MHz, configured at 1600 MHz

21x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2133 MHz, configured at 1600 MHz

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/cpu2006/lib32:/cpu2006/libs/32:/cpu2006/lib64:/cpu2006/sh"

OMP_NUM_THREADS = "40"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

445.gobmk: -DSPEC_CPU_LP64

456.hmmr: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

464.h264ref: -DSPEC_CPU_LP64

471.omnetpp: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: basepeak = yes

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-opt-prefetch -auto-p32

445.gobmk: basepeak = yes

456.hammer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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