



# SPEC® CINT2006 Result

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## Huawei

SPECint®2006 = 47.1

Huawei XH628 V3 (Intel Xeon E5-2650L v3)

SPECint\_base2006 = 45.4

CPU2006 license: 3175

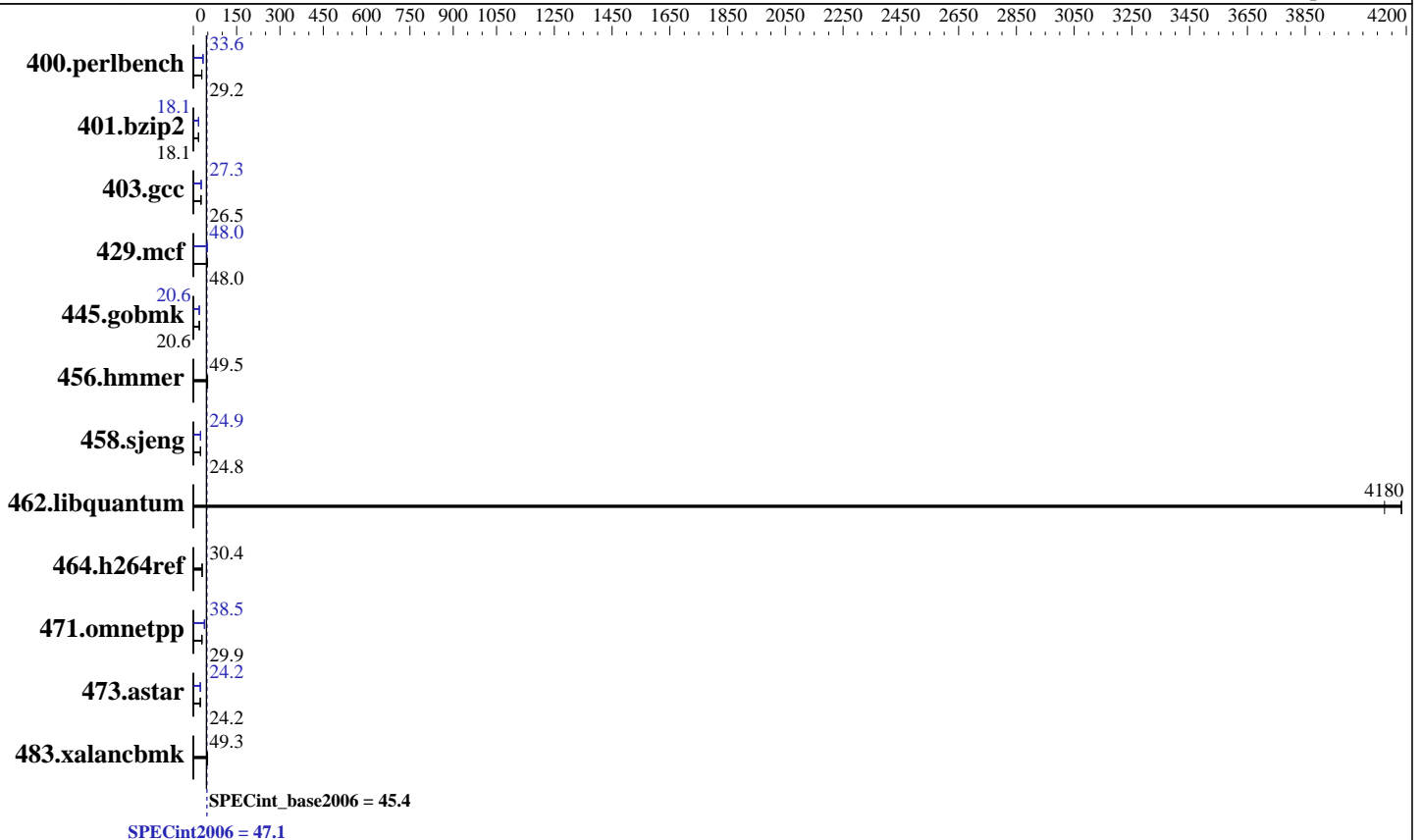
Test sponsor: Huawei

Tested by: Huawei

Test date: Mar-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E5-2650L v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 3.10.0-123.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

| Benchmark      | Base               |                    |                   |                    |                   |                    | Peak               |                    |                   |                    |                   |                    |
|----------------|--------------------|--------------------|-------------------|--------------------|-------------------|--------------------|--------------------|--------------------|-------------------|--------------------|-------------------|--------------------|
|                | Seconds            | Ratio              | Seconds           | Ratio              | Seconds           | Ratio              | Seconds            | Ratio              | Seconds           | Ratio              | Seconds           | Ratio              |
| 400.perlbench  | <b><u>335</u></b>  | <b><u>29.2</u></b> | 336               | 29.1               | 334               | 29.3               | 292                | 33.5               | <b><u>291</u></b> | <b><u>33.6</u></b> | 291               | 33.6               |
| 401.bzip2      | 535                | 18.0               | <b><u>534</u></b> | <b><u>18.1</u></b> | 534               | 18.1               | 532                | 18.1               | 532               | 18.2               | <b><u>532</u></b> | <b><u>18.1</u></b> |
| 403.gcc        | 304                | 26.5               | 303               | 26.6               | <b><u>303</u></b> | <b><u>26.5</u></b> | <b><u>295</u></b>  | <b><u>27.3</u></b> | 295               | 27.3               | 295               | 27.3               |
| 429.mcf        | 190                | 48.1               | <b><u>190</u></b> | <b><u>48.0</u></b> | 193               | 47.3               | 191                | 47.7               | <b><u>190</u></b> | <b><u>48.0</u></b> | 190               | 48.1               |
| 445.gobmk      | 508                | 20.6               | <b><u>508</u></b> | <b><u>20.6</u></b> | 509               | 20.6               | <b><u>508</u></b>  | <b><u>20.6</u></b> | 508               | 20.6               | 508               | 20.7               |
| 456.hammer     | 189                | 49.5               | 189               | 49.3               | <b><u>189</u></b> | <b><u>49.5</u></b> | 189                | 49.5               | 189               | 49.3               | <b><u>189</u></b> | <b><u>49.5</u></b> |
| 458.sjeng      | 488                | 24.8               | 487               | 24.8               | <b><u>488</u></b> | <b><u>24.8</u></b> | 485                | 24.9               | <b><u>485</u></b> | <b><u>24.9</u></b> | 485               | 24.9               |
| 462.libquantum | <b><u>4.95</u></b> | <b><u>4180</u></b> | 5.02              | 4120               | 4.95              | 4180               | <b><u>4.95</u></b> | <b><u>4180</u></b> | 5.02              | 4120               | 4.95              | 4180               |
| 464.h264ref    | 725                | 30.5               | <b><u>727</u></b> | <b><u>30.4</u></b> | 729               | 30.4               | 725                | 30.5               | <b><u>727</u></b> | <b><u>30.4</u></b> | 729               | 30.4               |
| 471.omnetpp    | 208                | 30.1               | 217               | 28.8               | <b><u>209</u></b> | <b><u>29.9</u></b> | 162                | 38.5               | 163               | 38.4               | <b><u>162</u></b> | <b><u>38.5</u></b> |
| 473.astar      | 289                | 24.2               | 290               | 24.2               | <b><u>290</u></b> | <b><u>24.2</u></b> | <b><u>290</u></b>  | <b><u>24.2</u></b> | 287               | 24.4               | 292               | 24.1               |
| 483.xalancbmk  | 141                | 49.0               | <b><u>140</u></b> | <b><u>49.3</u></b> | 140               | 49.4               | 141                | 49.0               | <b><u>140</u></b> | <b><u>49.3</u></b> | 140               | 49.4               |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Custom

Set Snoop Mode to ES mode

Set Intel Hyper-threading Technology to Disable

Sysinfo program /spec15/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1

running on localhost.localdomain Thu Mar 19 05:06:23 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2650L v3 @ 1.80GHz

2 "physical id"s (chips)

24 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

Continued on next page



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## Platform Notes (Continued)

```

cpu cores : 12
siblings  : 12
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB

```

From /proc/meminfo

```

MemTotal:      263720560 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

From /etc/\*release\* /etc/\*version\*

os-release:

```

NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"

```

redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)

system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:7.0:ga:server

uname -a:

```

Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Mar 19 04:58

SPEC is set to: /spec15

```

Filesystem      Type      Size      Used      Avail      Use%      Mounted on
/dev/sdal        ext4     443G     165G     256G     40%      /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 1.17 09/03/2014

Memory:

```

8x Micron 36ASF2G72PZ-2G1A2 16 GB 1 rank 2133 MHz
8x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz

```

(End of data from sysinfo program)



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**Test sponsor:** Huawei

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**Test date:** Mar-2015

**Hardware Availability:** Sep-2014

**Software Availability:** Sep-2014

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/spec15/libs/32:/spec15/libs/64:/spec15/sh"

OMP\_NUM\_THREADS = "24"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

The Huawei XH622 V3 and Huawei XH628 V3

are electronically equivalent.

The results have been measured on a Huawei XH628 V3 model.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -DSPEC\_CPU\_LP64

456.hmmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -DSPEC\_CPU\_LP64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32

-Wl,-z,muldefs -L/sh -lsmartheap64



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|   |                           |             |
|---|---------------------------|-------------|
| <b>Huawei</b>                                   | <b>SPECint2006 =</b>      | <b>47.1</b> |
| <b>Huawei XH628 V3 (Intel Xeon E5-2650L v3)</b> | <b>SPECint_base2006 =</b> | <b>45.4</b> |

|                              |  |
|------------------------------|--|
| <b>CPU2006 license:</b> 3175 | <b>Test date:</b> Mar-2015             |
| <b>Test sponsor:</b> Huawei  | <b>Hardware Availability:</b> Sep-2014 |
| <b>Tested by:</b> Huawei     | <b>Software Availability:</b> Sep-2014 |

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

C++ benchmarks (except as noted below):

icpc -m64

471.omnetpp: icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)

-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32

-opt-prefetch -ansi-alias

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## Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

456.hmmcr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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