



# SPEC® CINT2006 Result

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## Lenovo Group Limited

Lenovo ThinkServer TS140 (Intel Xeon E3-1225 v3,  
3.20 GHz)

**SPECint®2006 = 58.7**

**SPECint\_base2006 = 57.2**

**CPU2006 license:** 9017

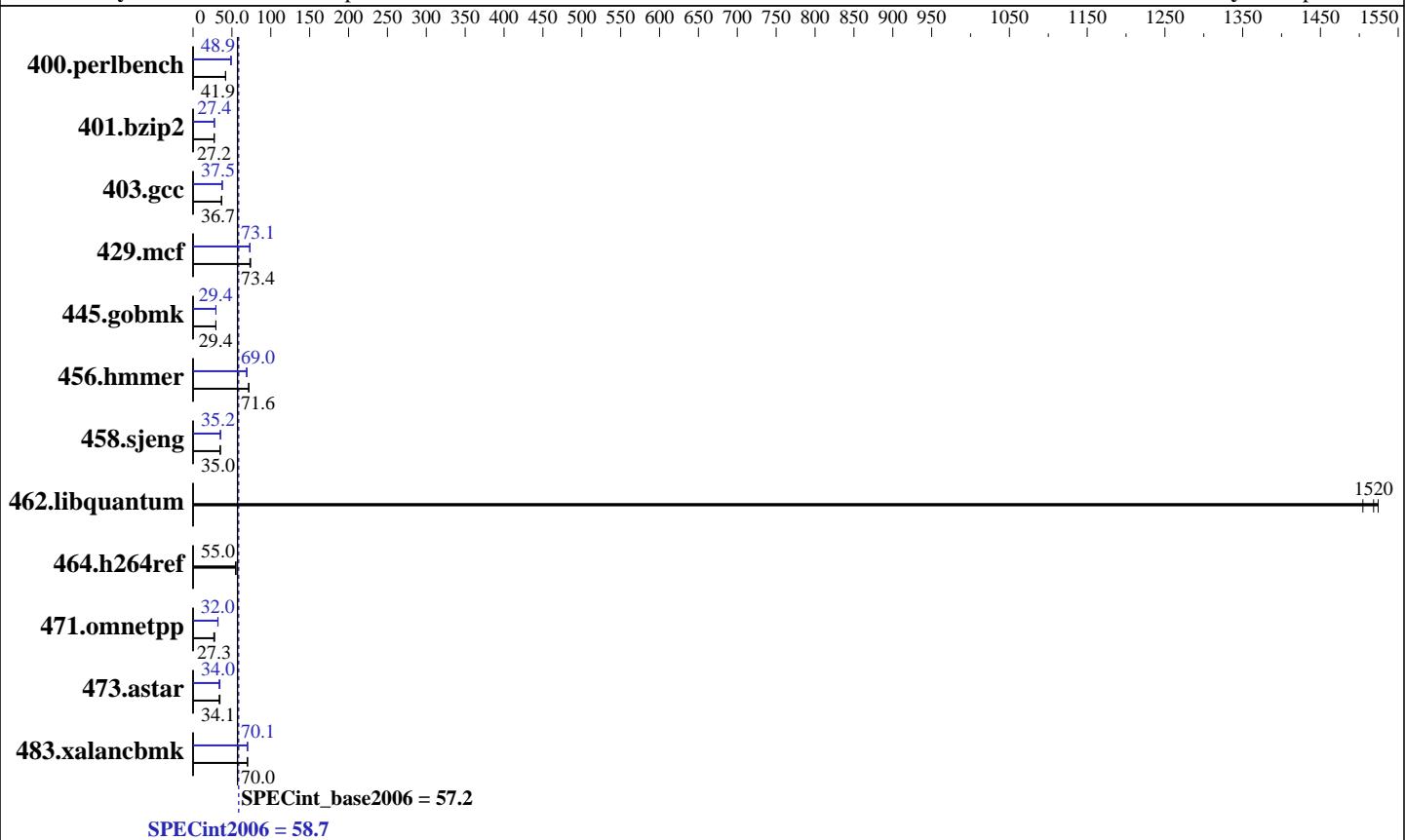
**Test date:** Dec-2014

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Sep-2013

**Tested by:** Lenovo Group Limited

**Software Availability:** Sep-2014



### Hardware

CPU Name:	Intel Xeon E3-1225 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (2 x 8 GB 2Rx8 PC3L-12800E-11,ECC)
Disk Subsystem:	1 x 800 GB SATA SSD
Other Hardware:	None

### Software

Operating System:	Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64
Compiler:	C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	232	42.0	234	41.7	<b><u>233</u></b>	<b><u>41.9</u></b>	200	48.9	200	48.8	<b><u>200</u></b>	<b><u>48.9</u></b>
401.bzip2	357	27.1	354	27.3	<b><u>354</u></b>	<b><u>27.2</u></b>	<b><u>352</u></b>	<b><u>27.4</u></b>	353	27.4	<b><u>352</u></b>	<b><u>27.4</u></b>
403.gcc	219	36.7	<b><u>219</u></b>	<b><u>36.7</u></b>	220	36.6	215	37.4	214	37.5	<b><u>214</u></b>	<b><u>37.5</u></b>
429.mcf	123	74.1	<b><u>124</u></b>	<b><u>73.4</u></b>	125	73.1	<b><u>125</u></b>	<b><u>73.1</u></b>	124	73.4	125	72.8
445.gobmk	357	29.4	<b><u>357</u></b>	<b><u>29.4</u></b>	357	29.4	<b><u>357</u></b>	<b><u>29.4</u></b>	357	29.4	357	29.4
456.hmmer	<b><u>130</u></b>	<b><u>71.6</u></b>	130	71.5	130	71.7	<b><u>135</u></b>	<b><u>69.0</u></b>	135	69.1	135	68.9
458.sjeng	347	34.9	345	35.1	<b><u>346</u></b>	<b><u>35.0</u></b>	<b><u>343</u></b>	<b><u>35.2</u></b>	343	35.2	343	35.2
462.libquantum	13.8	1500	<b><u>13.6</u></b>	<b><u>1520</u></b>	13.6	1520	13.8	1500	<b><u>13.6</u></b>	<b><u>1520</u></b>	13.6	1520
464.h264ref	<b><u>402</u></b>	<b><u>55.0</u></b>	402	55.1	404	54.8	<b><u>402</u></b>	<b><u>55.0</u></b>	402	55.1	404	54.8
471.omnetpp	234	26.7	<b><u>229</u></b>	<b><u>27.3</u></b>	226	27.7	<b><u>196</u></b>	<b><u>32.0</u></b>	194	32.2	196	31.9
473.astar	<b><u>206</u></b>	<b><u>34.1</u></b>	203	34.7	208	33.7	<b><u>203</u></b>	<b><u>34.6</u></b>	<b><u>207</u></b>	<b><u>34.0</u></b>	209	33.6
483.xalancbmk	<b><u>98.5</u></b>	<b><u>70.0</u></b>	98.9	69.8	98.5	70.1	<b><u>98.5</u></b>	<b><u>70.1</u></b>	98.5	70.1	98.7	69.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration

ICE Performance Modes set to Full Speed

C1E Support set to Disabled

C State Support set to C1C3C6

Sysinfo program /usr/cpu2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date::: 2014-06-25 #\\$ e3fbb8667b5a285932ceab81e28219e1

running on TS140 Wed Dec 24 04:42:46 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E3-1225 v3 @ 3.20GHz  
1 "physical id"s (chips)  
4 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

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## Platform Notes (Continued)

```
cpu cores : 4
siblings : 4
physical 0: cores 0 1 2 3
cache size : 8192 KB

From /proc/meminfo
MemTotal:      16155272 kB
HugePages_Total:      0
Hugepagesize:     2048 kB

From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.0 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.0"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux TS140 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64
x86_64 x86_64 GNU/Linux

run-level 3 Dec 24 04:41

SPEC is set to: /usr/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        xfs   741G   27G  715G   4% /
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO FBKT99AUS 09/19/2014
Memory:
2x Samsung M391B1G73QH0-YK0 8 GB 2 rank 1600 MHz
2x [Empty] [Empty]

(End of data from sysinfo program)
TS140 support 2 channels and 2 DIMMs per cha
2 DIMM slots installed with 8 GB DIMM for th
```



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## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

OMP\_NUM\_THREADS = "4"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB  
memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

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## Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

456.hmmr: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-TS140-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-TS140-revC.xml>



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