SPEC® CINT2006 Result
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Fujitsu

PRIMERGY CX2550 M1, Intel Xeon E5-2609 v3, 1.9 GHz

$SPECint®2006 =$ 35.1

$SPECint\_base2006 =$ 33.7

CPU2006 license: 19	**Test date:** Nov-2014
Test sponsor: Fujitsu	**Hardware Availability:** Sep-2014
Tested by: Fujitsu	**Software Availability:** Sep-2014

0 100 200 300 400 500 600 700 800 900 1000 1150 1300 1450 1600 1750 1900 2050 2200 2350 2500 2650 2850

- 400.perlbench: 25.4 / 22.0
- 401.bzip2: 13.8 / 13.7
- 403.gcc: 20.1 / 19.5
- 429.mcf: 38.1
- 445.gobmk: 15.7 / 15.7
- 456.hmmer: 37.6
- 458.sjeng: 19.0 / 18.9
- 462.libquantum: 2820
- 464.h264ref: 30.8
- 471.omnetpp: 19.8 / 15.1
- 473.astar: 18.1
- 483.xalancbmk: 36.8

SPECint_base2006 = 33.7

SPECint2006 = 35.1

Hardware

CPU Name:	Intel Xeon E5-2609 v3
CPU Characteristics:	
CPU MHz:	1900
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	15 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem:	1 x SATA, 500 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 7.0 (Maipo) Kernel 3.10.0-123.8.1.el7.x86_64
Compiler:	C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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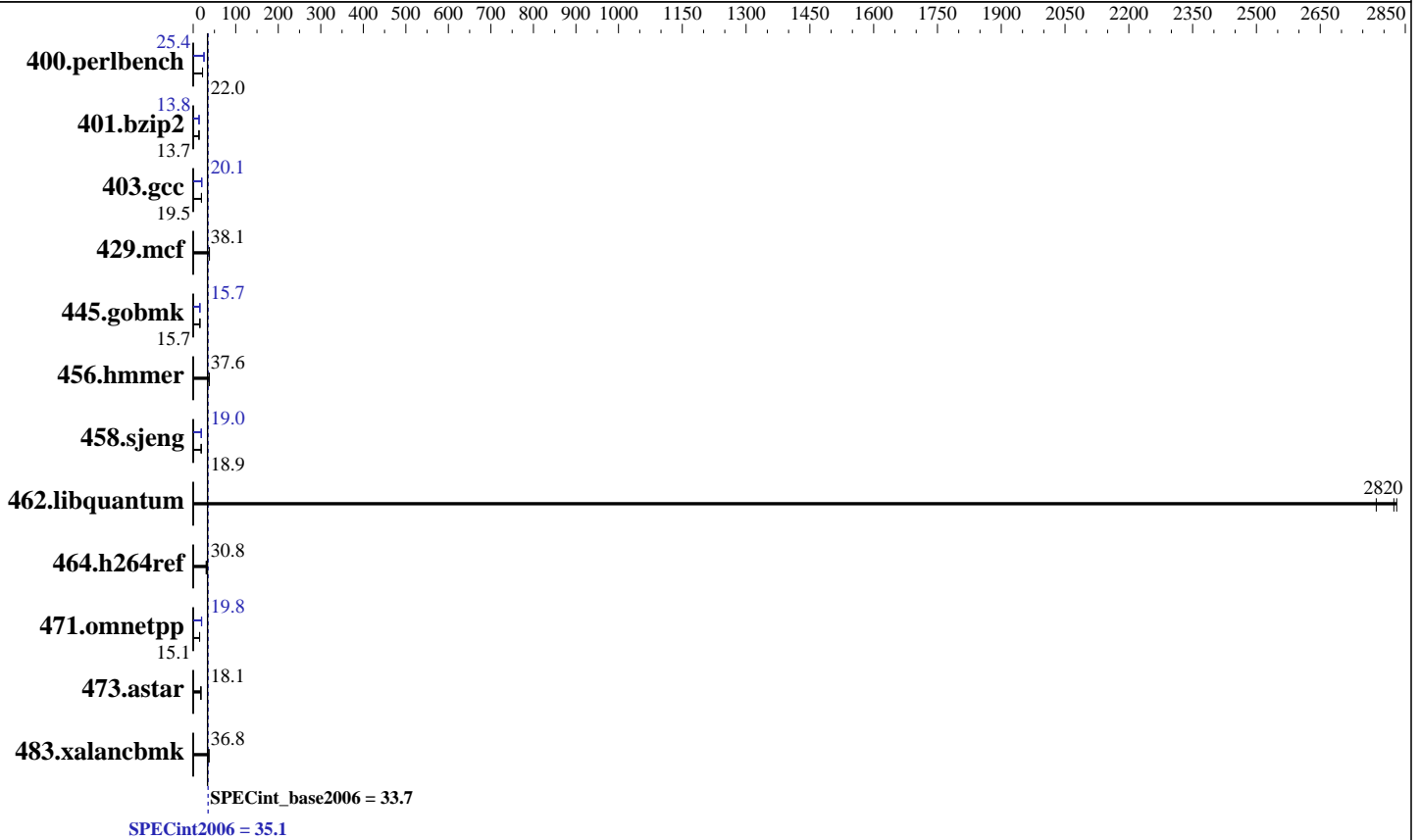
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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	445	21.9	**444**	**22.0**	444	22.0	386	25.3	385	25.4	**385**	**25.4**
401.bzip2	704	13.7	708	13.6	**705**	**13.7**	700	13.8	**699**	**13.8**	699	13.8
403.gcc	**414**	**19.5**	413	19.5	414	19.4	401	20.1	**401**	**20.1**	400	20.1
429.mcf	238	38.3	**240**	**38.1**	242	37.8	238	38.3	**240**	**38.1**	242	37.8
445.gobmk	666	15.7	**667**	**15.7**	669	15.7	**668**	**15.7**	668	15.7	667	15.7
456.hmmer	**248**	**37.6**	248	37.6	248	37.6	**248**	**37.6**	248	37.6	248	37.6
458.sjeng	643	18.8	639	18.9	**640**	**18.9**	637	19.0	**637**	**19.0**	636	19.0
462.libquantum	**7.34**	**2820**	7.45	2780	7.32	2830	**7.34**	**2820**	7.45	2780	7.32	2830
464.h264ref	719	30.8	716	30.9	**718**	**30.8**	719	30.8	716	30.9	**718**	**30.8**
471.omnetpp	**415**	**15.1**	418	15.0	412	15.2	315	19.9	323	19.4	**315**	**19.8**
473.astar	**388**	**18.1**	387	18.2	389	18.1	**388**	**18.1**	387	18.2	389	18.1
483.xalancbmk	187	36.9	187	36.8	**187**	**36.8**	187	36.9	187	36.8	**187**	**36.8**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

```
BIOS configuration:
Energy Performance = Performance
Utilization Profile = Unbalanced
QPI snoop mode: Early Snoop
    COD Enable = Disabled, Early Snoop = Enabled
CPU C1E Support = Disabled
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"
OMP_NUM_THREADS = "12"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0

 This result was measured on the PRIMERGY CX2550 M1. The PRIMERGY CX2550 M1
```

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General Notes (Continued)

and the PRIMERGY CX2570 M1 are electronically equivalent.
For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
    icc -m64

C++ benchmarks:
    icpc -m64

Base Portability Flags

        400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
          401.bzip2: -DSPEC_CPU_LP64
            403.gcc: -DSPEC_CPU_LP64
            429.mcf: -DSPEC_CPU_LP64
         445.gobmk: -DSPEC_CPU_LP64
         456.hmmer: -DSPEC_CPU_LP64
          458.sjeng: -DSPEC_CPU_LP64
     462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
        464.h264ref: -DSPEC_CPU_LP64
        471.omnetpp: -DSPEC_CPU_LP64
           473.astar: -DSPEC_CPU_LP64
      483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
    -Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

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CPU2006 license: 19

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Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Sep-2014

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc -m64
```

400.perlbench: `icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

445.gobmk: `icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

C++ benchmarks (except as noted below):
```
icpc -m64
```

471.omnetpp: `icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

Peak Portability Flags

```
        400.perlbench: -DSPEC_CPU_LINUX_IA32
            401.bzip2: -DSPEC_CPU_LP64
              403.gcc: -DSPEC_CPU_LP64
              429.mcf: -DSPEC_CPU_LP64
           456.hmmer: -DSPEC_CPU_LP64
           458.sjeng: -DSPEC_CPU_LP64
       462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
          464.h264ref: -DSPEC_CPU_LP64
            473.astar: -DSPEC_CPU_LP64
       483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
        400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                       -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                       -opt-prefetch -ansi-alias

            401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                       -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
                       -opt-prefetch -ansi-alias

              403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
                       -opt-malloc-options=3 -auto-ilp32

              429.mcf: basepeak = yes

            445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
                       -ansi-alias
```

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Peak Optimization Flags (Continued)

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block   -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Jan 27 13:33:25 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 27 January 2015.