



SPEC® CINT2006 Result

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Huawei

SPECint®2006 = 52.1

Huawei RH1288A V2 (Intel Xeon E5-2658 v2)

SPECint_base2006 = 48.0

CPU2006 license: 3175

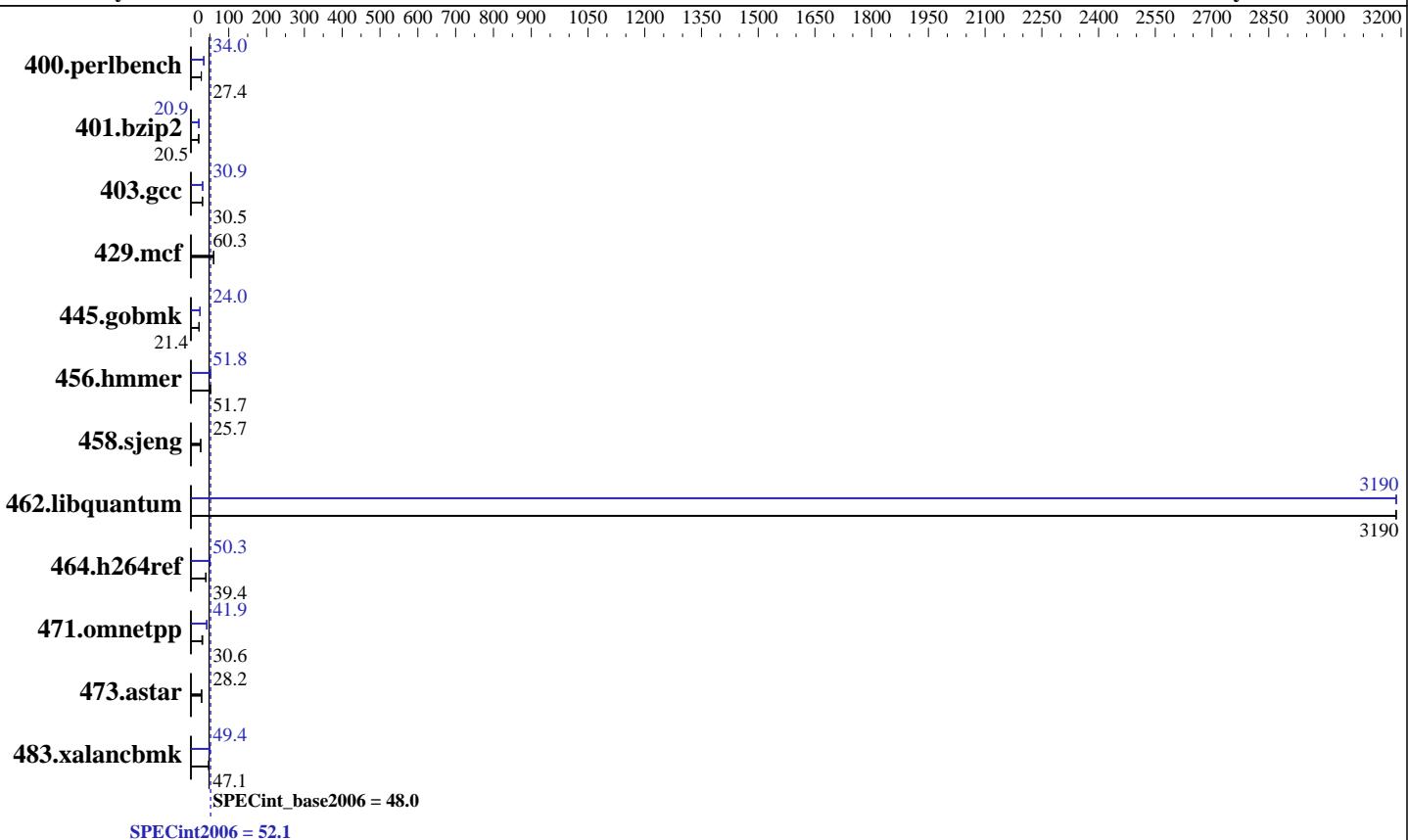
Test date: Sep-2014

Test sponsor: Huawei

Hardware Availability: Sep-2013

Tested by: Huawei

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E5-2658 v2
CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (8 x 16 GB 2Rx4 PC3-14900R-11, ECC)
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
Compiler: 2.6.32-431.el6.x86_64
C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel: Yes
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	357	27.3	356	27.4	<u>357</u>	<u>27.4</u>	<u>288</u>	<u>34.0</u>	288	34.0	288	34.0
401.bzip2	<u>470</u>	<u>20.5</u>	470	20.5	470	20.5	<u>462</u>	<u>20.9</u>	<u>462</u>	<u>20.9</u>	<u>462</u>	<u>20.9</u>
403.gcc	<u>264</u>	<u>30.5</u>	263	30.6	265	30.4	<u>261</u>	<u>30.9</u>	261	30.9	261	30.9
429.mcf	<u>151</u>	<u>60.3</u>	153	59.7	151	60.4	<u>151</u>	<u>60.3</u>	153	59.7	151	60.4
445.gobmk	490	21.4	<u>489</u>	<u>21.4</u>	489	21.5	<u>438</u>	<u>24.0</u>	<u>438</u>	<u>24.0</u>	438	24.0
456.hmmer	<u>181</u>	<u>51.7</u>	181	51.7	181	51.6	<u>180</u>	<u>51.8</u>	181	51.7	179	52.1
458.sjeng	470	25.7	470	25.8	<u>470</u>	<u>25.7</u>	<u>470</u>	<u>25.7</u>	470	25.8	<u>470</u>	<u>25.7</u>
462.libquantum	<u>6.50</u>	<u>3190</u>	6.50	3190	6.50	3190	<u>6.50</u>	<u>3190</u>	<u>6.50</u>	<u>3190</u>	6.50	3190
464.h264ref	<u>561</u>	<u>39.4</u>	560	39.5	561	39.4	<u>440</u>	<u>50.3</u>	439	50.4	<u>440</u>	<u>50.3</u>
471.omnetpp	203	30.7	215	29.0	<u>204</u>	<u>30.6</u>	<u>150</u>	<u>41.7</u>	149	42.0	<u>149</u>	<u>41.9</u>
473.astar	249	28.2	<u>249</u>	<u>28.2</u>	249	28.2	<u>249</u>	<u>28.2</u>	<u>249</u>	<u>28.2</u>	249	28.2
483.xalancbmk	147	47.0	<u>147</u>	<u>47.1</u>	146	47.1	<u>140</u>	<u>49.3</u>	140	49.4	<u>140</u>	<u>49.4</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Custom

Baseboard Management Controller used to adjust the fan speed to 100%

Sysinfo program /spec/config/sysinfo.rev6800

\$Rev: 6800 \$ \$Date:: 2011-10-11 ## 6f2ebdff5032aaa42e583f96b07f99d3

running on localhost Wed Sep 17 00:53:44 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2658 v2 @ 2.40GHz

2 "physical id"s (chips)

20 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 10

siblings : 10

physical 0: cores 0 1 2 3 4 8 9 10 11 12

physical 1: cores 0 1 2 3 4 8 9 10 11 12

cache size : 25600 KB

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Platform Notes (Continued)

```
From /proc/meminfo
MemTotal:       132103760 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Sep 17 00:52
```

```
SPEC is set to: /spec
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sdal      ext4  439G   88G  329G  22%  /
```

```
Additional information from dmidecode:
```

```
Memory:
 8x Samsung M393B2G70QH0-CMA 16 GB 1867 MHz 2 rank
```

```
(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,0,1"

LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"

OMP_NUM_THREADS = "20"

Binaries compiled on a system with 2x Xeon X5645 CPU + 16GB memory
using RHEL 6.1

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

The Huawei RH2288A V2 and Huawei RH1288A V2

are electronically equivalent.

The results have been measured on a Huawei RH2288A V2 model

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

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Base Compiler Invocation (Continued)

C++ benchmarks:

icpc -m64

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hammer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/smartheap -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

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Peak Compiler Invocation (Continued)

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4_2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xSSE4_2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4_2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmer: -xSSE4_2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32
-ansi-alias

458.sjeng: basepeak = yes

462.libquantum: -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch
-auto-p32

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Peak Optimization Flags (Continued)

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll12 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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