



# SPEC® CINT2006 Result

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## Hewlett-Packard Company

ProLiant BL460c Gen9  
(3.40 GHz, Intel Xeon E5-2643 v3)

**SPECint®2006 = 66.0**

**SPECint\_base2006 = 62.6**

**CPU2006 license:** 3

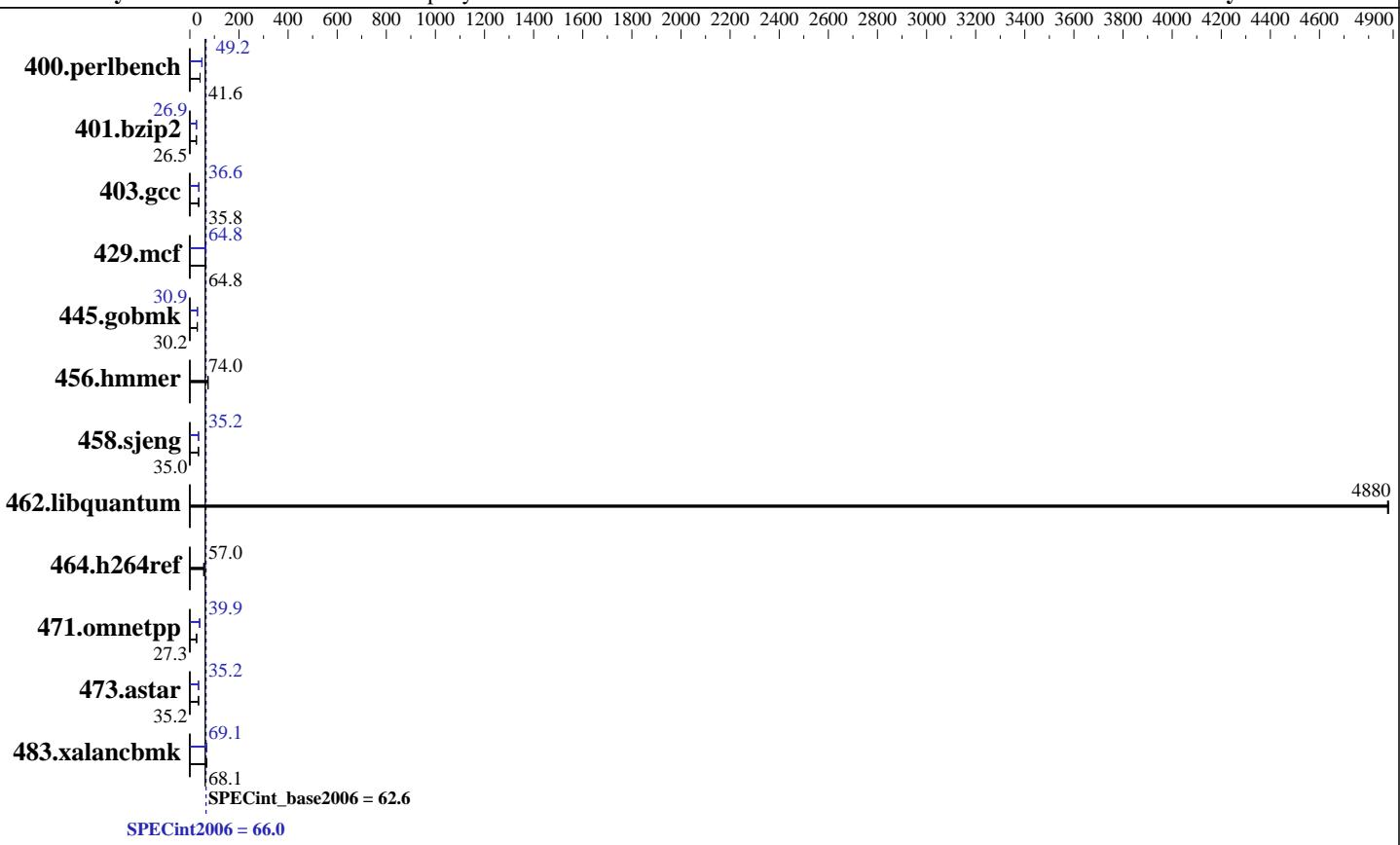
**Test sponsor:** Hewlett-Packard Company

**Tested by:** Hewlett-Packard Company

**Test date:** Aug-2014

**Hardware Availability:** Sep-2014

**Software Availability:** Jun-2014



### Hardware

CPU Name: Intel Xeon E5-2643 v3  
CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
CPU MHz: 3400  
FPU: Integrated  
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core  
L3 Cache: 20 MB I+D on chip per chip  
Other Cache: None  
Memory: 128 GB (8 x 16 GB 2Rx4 PC4-2133P-R)  
Disk Subsystem: 2 x 400 GB SAS SSD, RAID 1  
Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
Compiler: Kernel 3.10.0-123.el7.x86\_64  
Auto Parallel: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
File System: ext4  
System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	234	41.7	<b>235</b>	<b>41.6</b>	236	41.5	198	49.2	199	49.1	<b>199</b>	<b>49.2</b>
401.bzip2	<b>364</b>	<b>26.5</b>	363	26.6	364	26.5	359	26.9	<b>359</b>	<b>26.9</b>	360	26.8
403.gcc	224	35.9	<b>225</b>	<b>35.8</b>	225	35.8	220	36.6	220	36.5	<b>220</b>	<b>36.6</b>
429.mcf	<b>141</b>	<b>64.8</b>	141	64.8	141	64.6	140	65.0	<b>141</b>	<b>64.8</b>	141	64.5
445.gobmk	<b>347</b>	<b>30.2</b>	347	30.3	347	30.2	340	30.9	<b>340</b>	<b>30.9</b>	340	30.9
456.hmmer	<b>126</b>	<b>74.0</b>	126	74.1	126	74.0	<b>126</b>	<b>74.0</b>	126	74.1	126	74.0
458.sjeng	346	35.0	346	34.9	<b>346</b>	<b>35.0</b>	343	35.2	<b>344</b>	<b>35.2</b>	344	35.2
462.libquantum	<b>4.25</b>	<b>4880</b>	4.25	4880	4.24	4880	<b>4.25</b>	<b>4880</b>	4.25	4880	4.24	4880
464.h264ref	<b>388</b>	<b>57.0</b>	390	56.7	388	57.0	<b>388</b>	<b>57.0</b>	390	56.7	388	57.0
471.omnetpp	228	27.4	230	27.2	<b>229</b>	<b>27.3</b>	<b>157</b>	<b>39.9</b>	158	39.6	156	40.2
473.astar	200	35.1	<b>200</b>	<b>35.2</b>	199	35.2	200	35.1	199	35.2	<b>199</b>	<b>35.2</b>
483.xalancbmk	101	68.1	101	68.2	<b>101</b>	<b>68.1</b>	101	68.3	<b>99.8</b>	<b>69.1</b>	99.8	69.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1 > /proc/sys/vm/drop_caches
```

## Platform Notes

BIOS Configuration:

Intel Hyperthreading Options set to Disabled

HP Power Profile set to Custom

HP Power Regulator set to HP Static High Performance Mode

Minimum Processor Idle Power Package C-State set to No Package State

Energy/Performance Bias set to Maximum Performance

QPI Snoop Configuration set to Early Snoop

Thermal Configuration set to Maximum Cooling

Processor Power and Utilization Monitoring set to Disabled

Memory Refresh Rate set to 1x Refresh

Sysinfo program /cpu2006/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-07-17 ## e86d102572650a6e4d596a3cee98f191

running on BL460cGen9-VP2 Wed Aug 13 12:42:12 2014

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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2643 v3 @ 3.40GHz
        2 "physical id"s (chips)
        12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 6
    siblings   : 6
    physical 0: cores 0 1 2 3 4 5
    physical 1: cores 0 1 2 3 4 5
    cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      131734376 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.0 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.0"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
```

```
uname -a:
Linux BL460cGen9-VP2 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Aug 13 12:40
```

```
SPEC is set to: /cpu2006
Filesystem           Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-root ext4  310G  140G  154G  48%  /
```

Additional information from dmidecode:

```
BIOS HP I36 07/11/2014
Memory:
8x HP NOT AVAILABLE 16 GB 2133 MHz 2 rank
8x UNKNOWN NOT AVAILABLE
```

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## Platform Notes (Continued)

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 128 GB and the dmidecode description should have one line reading as:  
8x HP NOT AVAILABLE 16 GB 2133 MHz 2 rank

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/cpu2006/lib32/32:/cpu2006/lib32/64:/cpu2006/sh"

OMP\_NUM\_THREADS = "12"

memory using RedHat EL 6.4

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

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## Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

456.hmmr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revA.xml>



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