



SPEC® CFP2006 Result

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Hewlett-Packard Company

SPECfp®2006 = 66.3

ProLiant DL380e Gen8
(1.70 GHz, Intel Xeon E5-2450L v2)

SPECfp_base2006 = 63.7

CPU2006 license: 3

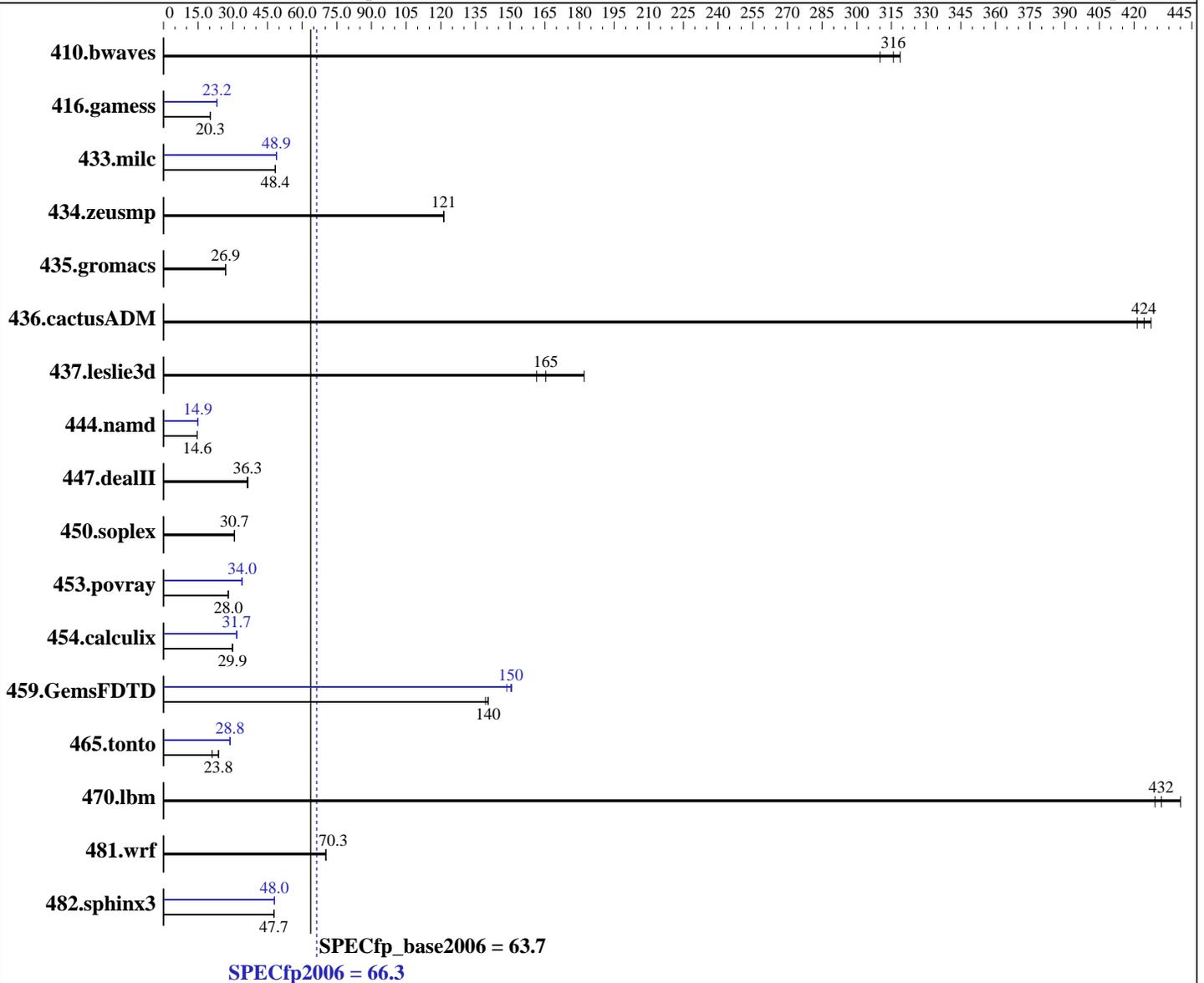
Test date: Jan-2014

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2014

Tested by: Hewlett-Packard Company

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2450L v2
 CPU Characteristics: Intel Turbo Boost Technology up to 2.10 GHz
 CPU MHz: 1700
 FPU: Integrated
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP3
 Kernel 3.0.76-0.11-default
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)

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L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 300 GB 15 K SAS, RAID 0
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|-------------|-------------|------------|-------------|-------------|-------------|-------------|-------------|------------|-------------|-------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 43.0 | 316 | 43.8 | 310 | 42.6 | 319 | 43.0 | 316 | 43.8 | 310 | 42.6 | 319 |
| 416.gamess | 965 | 20.3 | 967 | 20.3 | 964 | 20.3 | 845 | 23.2 | 845 | 23.2 | 845 | 23.2 |
| 433.milc | 190 | 48.4 | 190 | 48.4 | 190 | 48.4 | 188 | 48.9 | 188 | 48.9 | 188 | 48.9 |
| 434.zeusmp | 75.0 | 121 | 75.0 | 121 | 75.0 | 121 | 75.0 | 121 | 75.0 | 121 | 75.0 | 121 |
| 435.gromacs | 265 | 26.9 | 266 | 26.9 | 266 | 26.9 | 265 | 26.9 | 266 | 26.9 | 266 | 26.9 |
| 436.cactusADM | 28.0 | 427 | 28.4 | 421 | 28.2 | 424 | 28.0 | 427 | 28.4 | 421 | 28.2 | 424 |
| 437.leslie3d | 56.8 | 165 | 51.6 | 182 | 58.2 | 161 | 56.8 | 165 | 51.6 | 182 | 58.2 | 161 |
| 444.namd | 549 | 14.6 | 551 | 14.6 | 549 | 14.6 | 539 | 14.9 | 539 | 14.9 | 540 | 14.9 |
| 447.dealII | 315 | 36.3 | 315 | 36.3 | 313 | 36.6 | 315 | 36.3 | 315 | 36.3 | 313 | 36.6 |
| 450.soplex | 272 | 30.7 | 272 | 30.7 | 271 | 30.7 | 272 | 30.7 | 272 | 30.7 | 271 | 30.7 |
| 453.povray | 190 | 28.0 | 190 | 28.0 | 190 | 28.0 | 156 | 34.1 | 156 | 34.0 | 157 | 33.9 |
| 454.calculix | 276 | 29.9 | 276 | 29.9 | 276 | 29.8 | 260 | 31.7 | 260 | 31.7 | 261 | 31.7 |
| 459.GemsFDTD | 76.1 | 139 | 75.5 | 140 | 75.5 | 140 | 70.6 | 150 | 70.4 | 151 | 71.4 | 149 |
| 465.tonto | 467 | 21.1 | 413 | 23.8 | 414 | 23.8 | 342 | 28.7 | 342 | 28.8 | 341 | 28.8 |
| 470.lbm | 31.8 | 432 | 31.2 | 440 | 32.0 | 429 | 31.8 | 432 | 31.2 | 440 | 32.0 | 429 |
| 481.wrf | 159 | 70.3 | 159 | 70.3 | 159 | 70.3 | 159 | 70.3 | 159 | 70.3 | 159 | 70.3 |
| 482.sphinx3 | 408 | 47.7 | 408 | 47.7 | 407 | 47.9 | 406 | 48.0 | 405 | 48.1 | 406 | 48.0 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
Reclaim mode enabled with:
echo 1 > /proc/sys/vm/zone_reclaim_mode
runspec command invoked through numactl i.e.:
numactl --localalloc runspec <etc>
```



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Platform Notes

BIOS Configuration:

Intel Hyperthreading Options set to Disabled
HP Power Profile set to Maximum Performance
Minimum Processor Idle Power Core State set to C6
Minimum Processor Idle Power Package State set to C6 (retention)
Memory Power Savings Mode set to Maximum Performance
Thermal Configuration set to Maximum Cooling
Collaborative Power Control set to Disabled
Dynamic Power Capping Functionality set to Disabled
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x

Sysinfo program /cpu2006/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on dl380e-gen8-cxd Tue Jan 28 15:42:32 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2450L v2 @ 1.70GHz
 2 "physical id"s (chips)
 20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 10
  siblings  : 10
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB
```

```
From /proc/meminfo
MemTotal:      98903452 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3
```

```
uname -a:
Linux dl380e-gen8-cxd 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
(ccab990) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jan 28 15:37 last=S
```

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Platform Notes (Continued)

SPEC is set to: /cpu2006

| Filesystem | Type | Size | Used | Avail | Use% | Mounted on |
|------------|------|------|------|-------|------|------------|
| /dev/sda3 | ext3 | 273G | 13G | 247G | 5% | / |

Additional information from dmidecode:

BIOS HP P73 11/12/2013

Memory:

12x HP 689911-071 8 GB 1600 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

OMP_NUM_THREADS = "20"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags



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Tested by: Hewlett-Packard Company

Software Availability: Sep-2013

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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Peak Optimization Flags (Continued)

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revD.xml>

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For other inquiries, please contact webmaster@spec.org.

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