



# SPEC<sup>®</sup> CFP2006 Result

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## IBM Corporation

IBM System x3500 M4  
(Intel Xeon E5-2667 v2, 3.30 GHz)

SPECfp<sup>®</sup>2006 = **110**

SPECfp\_base2006 = **105**

CPU2006 license: 11

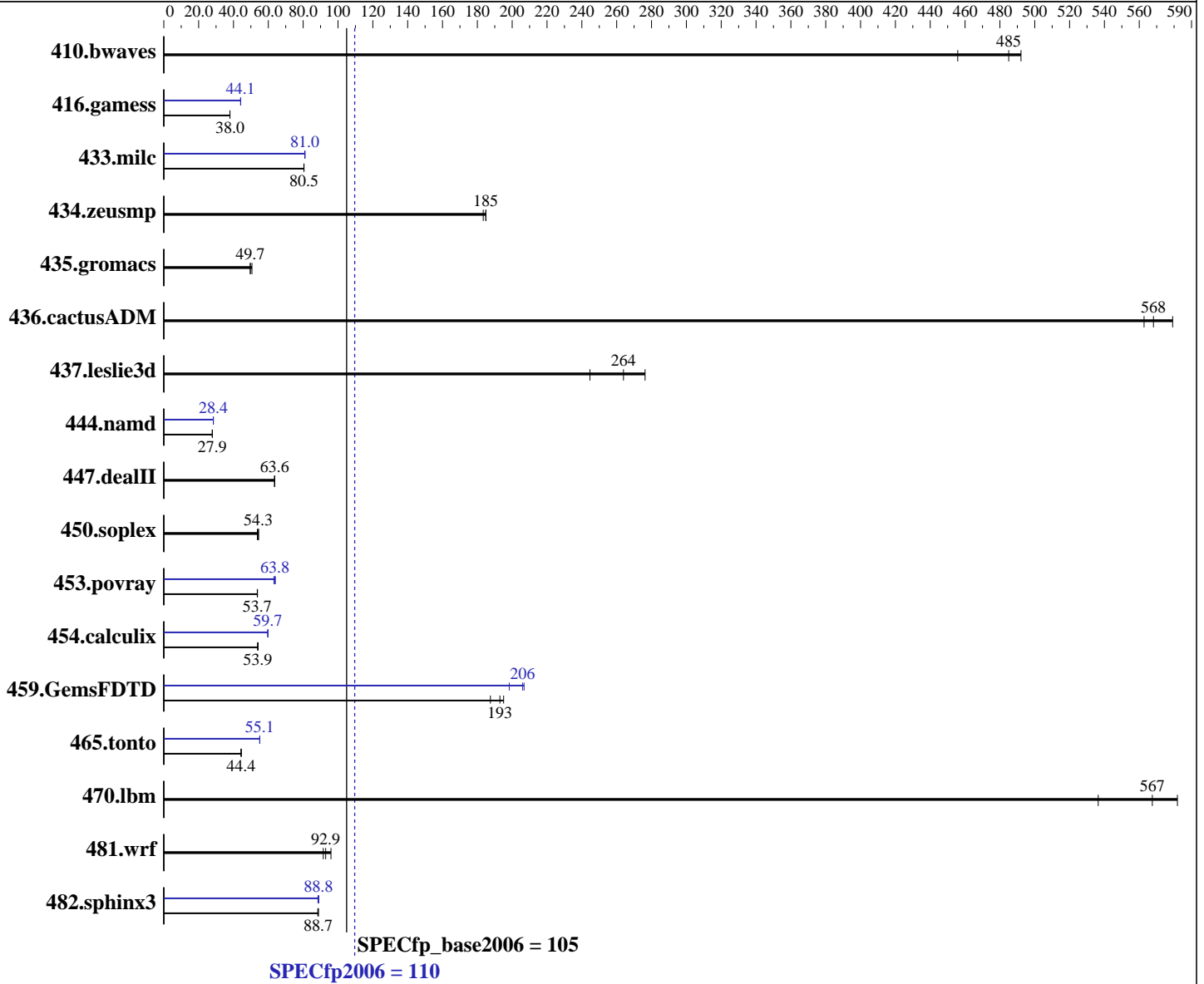
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Feb-2014

Hardware Availability: Dec-2013

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2667 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz  
 CPU MHz: 3300  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 2.6.32-358.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 25 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
Disk Subsystem: 1 x 1 TB SATA, 7200 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>28.0</u>	<u>485</u>	29.8	456	27.6	492	<u>28.0</u>	<u>485</u>	29.8	456	27.6	492
416.gamess	<u>516</u>	<u>38.0</u>	515	38.0	516	37.9	444	44.1	443	44.2	<u>443</u>	<u>44.1</u>
433.milc	114	80.5	<u>114</u>	<u>80.5</u>	114	80.4	113	81.1	113	81.0	<u>113</u>	<u>81.0</u>
434.zeusmp	49.2	185	49.6	183	<u>49.2</u>	<u>185</u>	49.2	185	49.6	183	<u>49.2</u>	<u>185</u>
435.gromacs	141	50.6	144	49.6	<u>144</u>	<u>49.7</u>	141	50.6	144	49.6	<u>144</u>	<u>49.7</u>
436.cactusADM	<u>21.0</u>	<u>568</u>	20.6	579	21.2	563	<u>21.0</u>	<u>568</u>	20.6	579	21.2	563
437.leslie3d	<u>35.6</u>	<u>264</u>	38.4	245	34.0	276	<u>35.6</u>	<u>264</u>	38.4	245	34.0	276
444.namd	288	27.9	<u>288</u>	<u>27.9</u>	288	27.9	282	28.4	282	28.4	<u>282</u>	<u>28.4</u>
447.dealII	180	63.7	<u>180</u>	<u>63.6</u>	180	63.6	180	63.7	<u>180</u>	<u>63.6</u>	180	63.6
450.soplex	153	54.4	<u>154</u>	<u>54.3</u>	155	53.7	153	54.4	<u>154</u>	<u>54.3</u>	155	53.7
453.povray	<u>99.1</u>	<u>53.7</u>	98.9	53.8	99.1	53.7	<u>83.4</u>	<u>63.8</u>	83.1	64.0	84.2	63.2
454.calculix	152	54.1	<u>153</u>	<u>53.9</u>	153	53.9	138	59.8	<u>138</u>	<u>59.7</u>	138	59.7
459.GemsFDTD	<u>55.0</u>	<u>193</u>	54.4	195	56.6	188	<u>51.5</u>	<u>206</u>	53.5	198	51.3	207
465.tonto	<u>222</u>	<u>44.4</u>	221	44.5	222	44.4	179	55.1	<u>179</u>	<u>55.1</u>	179	55.1
470.lbm	25.6	536	<u>24.2</u>	<u>567</u>	23.6	582	25.6	536	<u>24.2</u>	<u>567</u>	23.6	582
481.wrf	116	96.0	<u>120</u>	<u>92.9</u>	122	91.6	116	96.0	<u>120</u>	<u>92.9</u>	122	91.6
482.sphinx3	220	88.4	220	88.7	<u>220</u>	<u>88.7</u>	220	88.5	<u>219</u>	<u>88.8</u>	219	89.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Zone reclaim mode enabled with:  
echo 1 > /proc/sys/vm/zone\_reclaim\_mode

## Platform Notes

BIOS setting:  
Operating Mode set to Maximum Performance  
Hyper-Threading set to Disabled  
Sysinfo program /home/SPECcpu-new/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on x3500M4 Tue Feb 11 09:58:40 2014

This section contains SUT (System Under Test) info as seen by  
Continued on next page



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**Software Availability:** Sep-2013

## Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2667 v2 @ 3.30GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores      : 8
siblings       : 8
physical 0:    cores 1 2 3 4 8 9 10 11
physical 1:    cores 1 2 3 4 8 9 10 11
cache size     : 25600 KB
```

```
From /proc/meminfo
MemTotal:      264656392 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux x3500M4 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Feb 10 14:13
```

```
SPEC is set to: /home/SPECcpu-new
Filesystem      Type      Size Used Avail Use% Mounted on
/dev/mapper/vg_intelcrb-lv_home
                ext4       863G  14G  806G   2% /home
```

```
Additional information from dmidecode:
BIOS IBM      -[TKE133FUS-1.50]- 07/26/2013
Memory:
 8x Not Specified Not Specified
16x Samsung M393B2G70QH0-CMA 16 GB 1867 MHz 2 rank
```

(End of data from sysinfo program)  
"Not Specified" memory information from dmidecode indicates unused DIMM slots.



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## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/SPECcpu-new/libs/32:/home/SPECcpu-new/libs/64:/home/SPECcpu-new/sh"  
OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:  
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:  
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

## Peak Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.deallI: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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