



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R920 (Intel Xeon E7-4830 v2, 2.20 GHz)

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Dell Inc.

SPECint®2006 =

SPECint_base2006

NC

Test date: Feb-2014

Hardware Availability: Mar-2014

Software Availability: Mar-2014

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Dell or Intel.

400.perlbench |

401.bzip2 |

403.gcc |

429.mcf |

445.gobmk |

456.hmmer |

458.sjeng |

462.libquantum |

464.h264ref |

471.omnetpp |

473.astar |

483 |

Hardware

CPU Name: Intel Xeon E7-4830 v2
CPU Characteristics: Intel Turbo Boost Technology up to 2.70 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip
CPU(s) orderable: 2,4 chip
Primary Cache: 32 KB I + 32 KB D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
3.0.76-0.11-default
Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE
for Linux
Auto Parallel: Yes
File System: ext2
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit

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Secondary Cache:	256 KB I+D on chip per core	Peak Powers:	32/64-bit
L3 Cache:	20 MB I+D on chip per chip	Other Software:	Microquill SmartHeap V10.0
Other Cache:	None		
Memory:	1 TB (64 x 16 GB 2Rx4 PC3L-12800R-11, ECC)		
Disk Subsystem:	1 x 400 GB SAS6 SSD		
Other Hardware:	None		

Result Table

Benchmark	Base						Peak					
	Seconds	Ratio										
400.perlbench	NC	NC										
401.bzip2	NC	NC										
403.gcc	NC	NC										
429.mcf	NC	NC										
445.gobmk	NC	NC										
456.hmmer	NC	NC										
458sjeng	NC	NC										
462.libquantum	NC	NC										
464.h264ref	NC	NC										
471.omnetpp	NC	NC										
473.astar	NC	NC										
483.xz	NC	NC										

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:

Virtualization Technology disabled

Execute Disable disabled

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Platform Notes (Continued)

System Profile set to Custom

Logical Processor disabled

Memory Patrol Scrub set to disabled

Sysinfo program

/root/Desktop/Performance/icl..._Oct-11-2013/.config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-01-17 #\\$ e80102572650a6e4d596a3cee98f191

running on slesperf1 Mon Feb 17 03:29:15 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E7-4830 v2 @ 2.20GHz

4 "physical id"s (chips)

40 "processors"

cores, sibling (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 10

siblings : 10

physical 0: cores 0 1 2 3 4 8 9 10 11 12

physical 1: cores 0 1 2 3 4 8 9 10 11 12

physical 2: cores 0 1 2 3 4 8 9 10 11 12

physical 3: cores 0 1 2 3 4 8 9 10 11 12

l1d cache : 20480 KB

From /proc/meminfo

MemTotal: 1058789108 kB

HugePages_Total: 0

HugePagesize: 2048 kB

/usr/bin/lsb_release -d

SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*

SuSE-release:

SUSE Linux Enterprise Server 11 (x86_64)

VERSION = 11

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Platform Notes (Continued)

PATCHLEVEL = 3

```
uname -a:  
Linux slesperf1 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013  
(ccab990) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Feb 17 03:27 last=S
```

```
SPEC is set to: /root/Desktop/Performance/icl4.0_Oct17_2013  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda2 ext2 365G 31G 333G 9% /
```

Additional information from lmdecode:

BIOS Dell Inc. 1.0.4 01/07/2014

Memory:

```
32x 00CE00B300CE M393B2G70BH0-YK0 16 GB 1333 MHz  
10x 00CE00B300CE M393B2G70CB0-YK0 16 GB 1333 MHz  
22x 00CE04B300CE M393B2G70CB0-YK0 16 GB 1333 MHz
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

OMP_NUM_THREADS = "40"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.blalancbm: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
            -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
          -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
            -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32
            -ansi-alias

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -ansi-alias

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -unroll12 -ansi-alias
```

C++ benchmarks:

```
471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -opt-ra-region-strategy=block -ansi-alias
              -Wl,-z,muldefs -L/sh -lsmartheap
```

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L.ash -Lmarthea

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The XML files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revC.xml>

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