



# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R820 (Intel Xeon E5-2430L v2,  
2.40 GHz)

**SPECfp®2006 = 72.4**

**SPECfp\_base2006 = 69.2**

CPU2006 license: 55

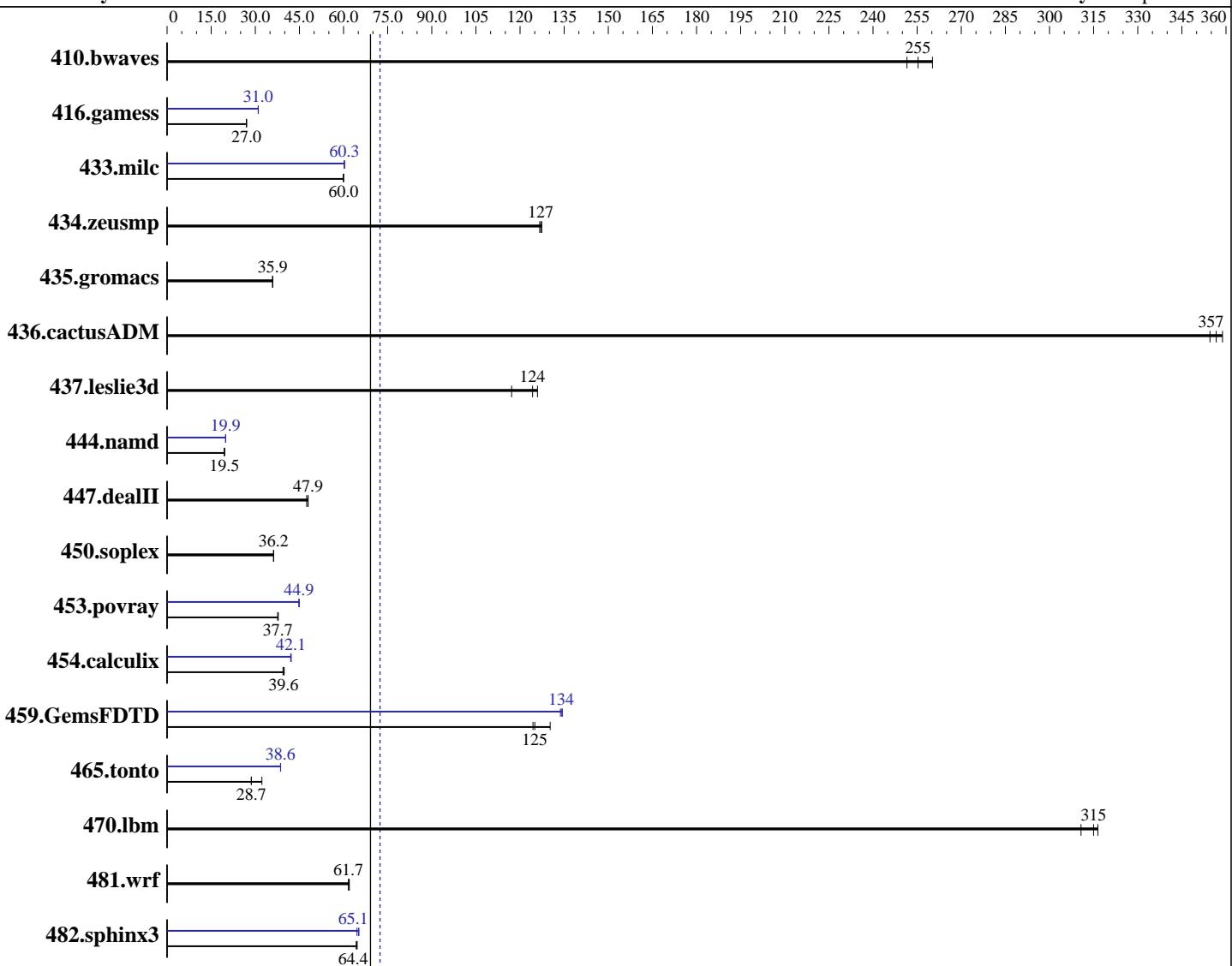
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Nov-2013

Hardware Availability: Jan-2014

Software Availability: Sep-2013



## Hardware

CPU Name: Intel Xeon E5-2430L v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 3.0.70-0.9-default  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE  
 for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran  
 Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Run level 3 (multi-user)

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L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 96 GB (6 x 16 GB 2Rx4 PC3L-12800R-11, ECC)  
 Disk Subsystem: 2 x 50 GB SATA SSD, RAID 0  
 Other Hardware: None

Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	52.2	260	54.0	252	<b>53.2</b>	<b>255</b>	52.2	260	54.0	252	<b>53.2</b>	<b>255</b>
416.gamess	725	27.0	<b>725</b>	<b>27.0</b>	725	27.0	<b>632</b>	<b>31.0</b>	<b>632</b>	<b>31.0</b>	631	31.0
433.milc	153	60.0	<b>153</b>	<b>60.0</b>	153	59.9	<b>152</b>	<b>60.3</b>	<b>152</b>	<b>60.3</b>	152	60.3
434.zeusmp	71.4	127	<b>71.6</b>	<b>127</b>	71.8	127	<b>71.4</b>	<b>127</b>	<b>71.6</b>	<b>127</b>	71.8	127
435.gromacs	<b>199</b>	<b>35.9</b>	199	36.0	199	35.9	<b>199</b>	<b>35.9</b>	199	36.0	199	35.9
436.cactusADM	<b>33.5</b>	<b>357</b>	33.7	355	33.3	359	<b>33.5</b>	<b>357</b>	33.7	355	33.3	359
437.leslie3d	<b>75.6</b>	<b>124</b>	80.2	117	74.6	126	<b>75.6</b>	<b>124</b>	80.2	117	74.6	126
444.namd	<b>411</b>	<b>19.5</b>	411	19.5	411	19.5	<b>403</b>	<b>19.9</b>	403	19.9	<b>403</b>	<b>19.9</b>
447.dealII	239	47.9	<b>239</b>	<b>47.9</b>	241	47.4	<b>239</b>	<b>47.9</b>	<b>239</b>	<b>47.9</b>	241	47.4
450.soplex	230	36.2	230	36.3	<b>230</b>	<b>36.2</b>	<b>230</b>	<b>36.2</b>	<b>230</b>	<b>36.3</b>	<b>230</b>	<b>36.2</b>
453.povray	141	37.8	141	37.6	<b>141</b>	<b>37.7</b>	<b>119</b>	<b>44.8</b>	118	<b>44.9</b>	<b>119</b>	<b>44.9</b>
454.calculix	209	39.4	207	39.8	<b>208</b>	<b>39.6</b>	196	42.2	196	42.0	<b>196</b>	<b>42.1</b>
459.GemsFDTD	81.5	130	<b>84.9</b>	<b>125</b>	85.3	124	<b>79.3</b>	134	<b>79.1</b>	<b>134</b>	78.9	134
465.tonto	344	28.6	305	32.2	<b>343</b>	<b>28.7</b>	<b>255</b>	38.6	<b>255</b>	<b>38.6</b>	255	38.6
470.lbm	<b>43.6</b>	<b>315</b>	43.4	316	44.2	311	<b>43.6</b>	<b>315</b>	43.4	316	44.2	311
481.wrf	<b>181</b>	<b>61.7</b>	181	61.7	180	62.0	<b>181</b>	<b>61.7</b>	181	61.7	180	62.0
482.sphinx3	<b>303</b>	<b>64.4</b>	303	64.3	302	64.6	<b>300</b>	<b>65.1</b>	302	64.5	299	65.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:

Virtualization Technology disabled

Execute Disable disabled

Logical Processor disabled

System Profile set to Performance

Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-07-17 #\\$ e86d102572650a6e4d596a3cee98f191

running on linux Wed Nov 13 21:27:54 2013

This section contains SUT (System Under Test) info as seen by  
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## Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2430L v2 @ 2.40GHz
        2 "physical id"s (chips)
        12 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The
    following excerpts from /proc/cpuinfo might not be reliable. Use with
    caution.)
        cpu cores : 6
        siblings   : 6
        physical 0: cores 0 1 2 3 4 5
        physical 1: cores 0 1 2 3 4 5
    cache size : 15360 KB
```

```
From /proc/meminfo
    MemTotal:      99123324 kB
    HugePages_Total:       0
    Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3
```

```
uname -a:
Linux linux 3.0.70-0.9-default #1 SMP Tue Apr 2 13:01:38 UTC 2013 (25ae5d2)
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 13 14:19 last=S
```

```
SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext2   83G   9.4G   73G  12% /
```

Additional information from dmidecode:

```
BIOS Dell Inc. 2.0.22 09/23/2013
Memory:
 6x 00CE04B300CE M393B2G70BH0-YK0 16 GB 1600 MHz
```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"  
OMP\_NUM\_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll12 -ansi-alias  
-parallel

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## Peak Optimization Flags (Continued)

C++ benchmarks:

```
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32
```

```
447.dealII: basepeak = yes
```

```
450.soplex: basepeak = yes
```

```
453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
```

```
416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
             -inline-level=0 -scalar-rep-
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: basepeak = yes
```

```
459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
                -inline-level=0 -opt-prefetch -parallel
```

```
465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
            -opt-malloc-options=3 -auto -unroll4
```

Benchmarks using both Fortran and C:

```
435.gromacs: basepeak = yes
```

```
436.cactusADM: basepeak = yes
```

```
454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias
```

```
481.wrf: basepeak = yes
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.xml>



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Tested with SPEC CPU2006 v1.2.

Report generated on Thu Jul 24 20:52:10 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 28 January 2014.