



# SPEC® CINT2006 Result

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## Fujitsu

SPECint®2006 = **61.9**

PRIMERGY TX300 S8, Intel Xeon E5-2690 v2, 3.00 GHz

SPECint\_base2006 = **57.5**

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Sep-2013

Hardware Availability: Oct-2013

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2690 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 2.6.32-358.11.1.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	286	34.2	285	34.2	<u>285</u>	<u>34.2</u>	240	40.8	<u>241</u>	<u>40.5</u>	242	40.3
401.bzip2	<u>392</u>	<u>24.6</u>	392	24.6	392	24.6	<u>386</u>	<u>25.0</u>	385	25.1	387	25.0
403.gcc	226	35.7	226	35.6	<u>226</u>	<u>35.6</u>	221	36.5	<u>221</u>	<u>36.4</u>	221	36.4
429.mcf	129	70.9	130	70.0	<u>130</u>	<u>70.4</u>	129	70.9	130	70.0	<u>130</u>	<u>70.4</u>
445.gobmk	404	26.0	<u>404</u>	<u>26.0</u>	404	25.9	369	28.4	<u>369</u>	<u>28.4</u>	369	28.4
456.hammer	<u>149</u>	<u>62.8</u>	148	63.1	150	62.3	<u>149</u>	<u>62.8</u>	148	63.1	150	62.3
458.sjeng	402	30.1	402	30.1	<u>402</u>	<u>30.1</u>	<u>391</u>	<u>31.0</u>	391	31.0	391	31.0
462.libquantum	5.29	3920	5.28	3920	<u>5.28</u>	<u>3920</u>	5.29	3920	5.28	3920	<u>5.28</u>	<u>3920</u>
464.h264ref	439	50.4	440	50.3	<u>440</u>	<u>50.3</u>	375	59.1	<u>375</u>	<u>59.1</u>	375	59.0
471.omnetpp	192	32.6	<u>194</u>	<u>32.2</u>	227	27.5	129	48.4	<u>130</u>	<u>48.2</u>	130	48.2
473.astar	211	33.2	<u>212</u>	<u>33.2</u>	212	33.2	211	33.2	<u>212</u>	<u>33.2</u>	212	33.2
483.xalancbmk	115	60.0	116	59.5	<u>115</u>	<u>59.9</u>	115	60.0	116	59.5	<u>115</u>	<u>59.9</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Energy Performance = Performance  
Utilization Profile = Unbalanced

## General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,compact,1,0"  
LD\_LIBRARY\_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64:/SPECcpu2006/sh"  
OMP\_NUM\_THREADS = "20"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
This result was measured on the PRIMERGY RX350 S8. The PRIMERGY RX350 S8

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## General Notes (Continued)

and the PRIMERGY TX300 S8 are electronically equivalent.  
For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs  
-L/sh -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64  
  
400.perlbench: icc -m32  
  
445.gobmk: icc -m32  
  
464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m64  
  
471.omnetpp: icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-ansi-alias  
  
401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch  
-ansi-alias  
  
403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32  
  
429.mcf: basepeak = yes  
  
445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

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## Peak Optimization Flags (Continued)

456.hmmcr: basepeak = yes

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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