SPEC® CINT2006 Result
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IBM Corporation

IBM System x3100 M4
(Intel Core i3-3220, 3.30 GHz)

SPECint®2006 =   44.4
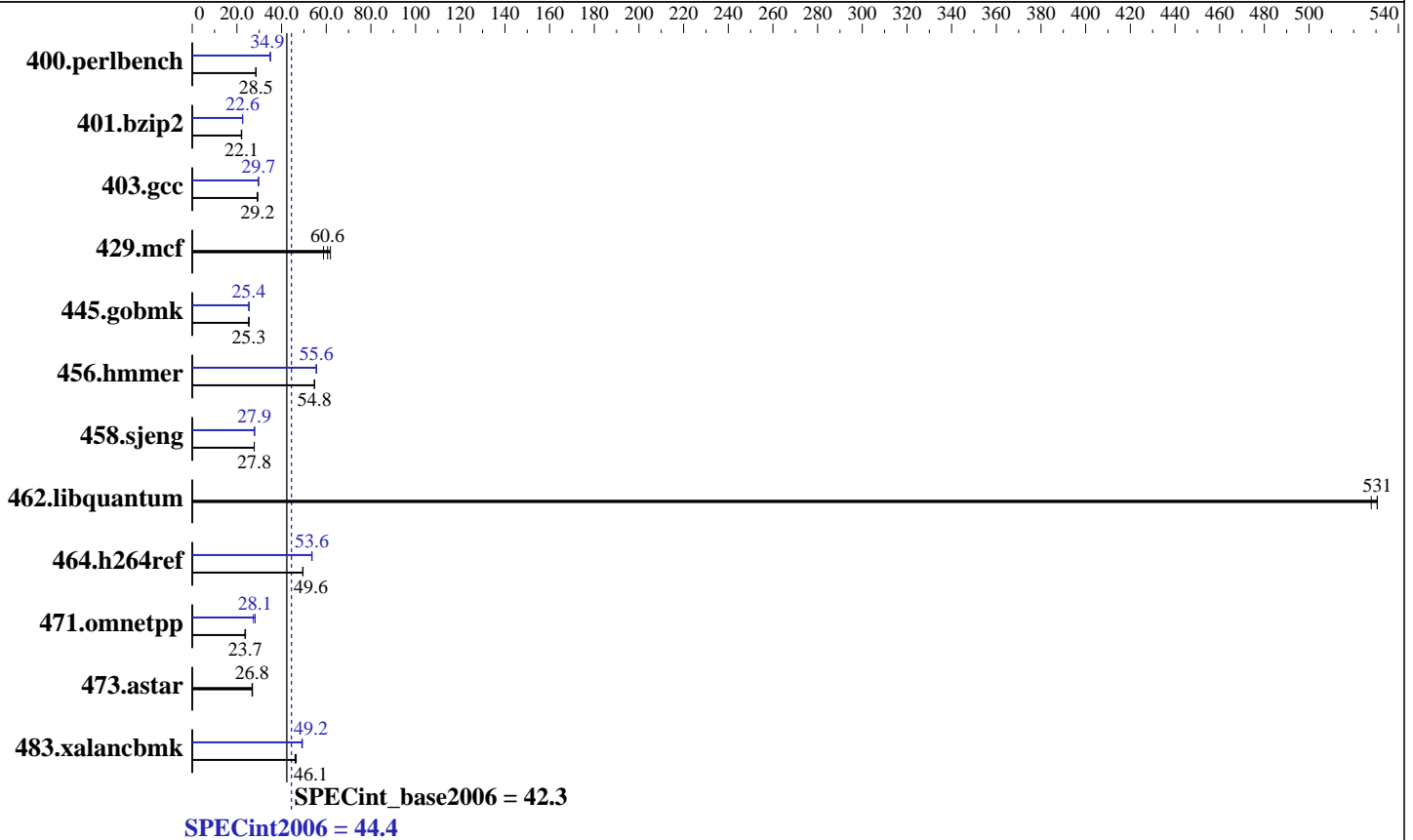
SPECint_base2006 =   42.3

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Oct-2012
Hardware Availability: Sep-2012
Software Availability: Dec-2011

SPECint_base2006 = 42.3

SPECint2006 = 44.4

Hardware

CPU Name:	Intel Core i3-3220
CPU Characteristics:	
CPU MHz:	3300
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	3 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)
Disk Subsystem:	1 x 250 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 (Santiago) 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base						Peak					
	Seconds	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	**343**	**28.5**	342	28.6	343	28.5	280	34.9	**280**	**34.9**	279	35.0
401.bzip2	**437**	**22.1**	440	22.0	436	22.1	**427**	**22.6**	429	22.5	427	22.6
403.gcc	276	29.1	**276**	**29.2**	274	29.3	272	29.6	**271**	**29.7**	271	29.7
429.mcf	155	58.8	**150**	**60.6**	147	61.9	155	58.8	**150**	**60.6**	147	61.9
445.gobmk	**414**	**25.3**	414	25.4	414	25.3	**413**	**25.4**	413	25.4	413	25.4
456.hmmer	**170**	**54.8**	171	54.7	170	54.8	**168**	**55.6**	168	55.5	168	55.6
458.sjeng	436	27.8	436	27.7	**436**	**27.8**	**434**	**27.9**	434	27.9	435	27.8
462.libquantum	39.3	528	39.1	531	**39.1**	**531**	39.3	528	39.1	531	**39.1**	**531**
464.h264ref	**446**	**49.6**	447	49.5	446	49.6	414	53.5	413	53.6	**413**	**53.6**
471.omnetpp	265	23.6	262	23.9	**263**	**23.7**	228	27.4	222	28.2	**222**	**28.1**
473.astar	260	27.0	**262**	**26.8**	262	26.8	260	27.0	**262**	**26.8**	262	26.8
483.xalancbmk	148	46.6	**150**	**46.1**	150	46.1	140	49.3	**140**	**49.2**	141	49.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
    BIOS Settings:
    Turbo Mode enabled in BIOS
    C-State enabled in BIOS
    Sysinfo program /root/SPECcpu1.2/config/sysinfo.rev6800
    $Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
    running on localhost.localdomain Sun Oct 14 08:31:53 2012

    This section contains SUT (System Under Test) info as seen by
    some common utilities.  To remove or add to this section, see:
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
      model name : Intel(R) Core(TM) i3-3220 CPU @ 3.30GHz
         1 "physical id"s (chips)
         4 "processors"
      cores, siblings (Caution: counting these is hw and system dependent.  The
      following excerpts from /proc/cpuinfo might not be reliable.  Use with
      caution.)
         cpu cores : 2
         siblings  : 4
         physical 0: cores 0 1
      cache size : 3072 KB
```

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Platform Notes (Continued)

```
From /proc/meminfo
   MemTotal:       16322724 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux localhost.localdomain 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13
   EST 2011 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 11 15:55

SPEC is set to: /root/SPECcpu1.2
   Filesystem      Type     Size  Used Avail Use% Mounted on
   /dev/mapper/VolGroup-lv_root
                   ext4      50G   47G  651M  99% /

Additional information from dmidecode:
  Memory:
    2x Micron 18JSF1G72AZ-1G6D1 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/SPECcpu1.2/libs/32:/root/SPECcpu1.2/libs/64"
OMP_NUM_THREADS = "2"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
    icc -m64

C++ benchmarks:
    icpc -m64

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Base Portability Flags

```
 400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
     401.bzip2: -DSPEC_CPU_LP64
       403.gcc: -DSPEC_CPU_LP64
       429.mcf: -DSPEC_CPU_LP64
     445.gobmk: -DSPEC_CPU_LP64
     456.hmmer: -DSPEC_CPU_LP64
     458.sjeng: -DSPEC_CPU_LP64
  462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
    464.h264ref: -DSPEC_CPU_LP64
    471.omnetpp: -DSPEC_CPU_LP64
      473.astar: -DSPEC_CPU_LP64
  483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs
-L/smartheap -lsmartheap64
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m64
```

```
400.perlbench: icc -m32
```

```
445.gobmk: icc -m32
```

```
464.h264ref: icc -m32
```

C++ benchmarks (except as noted below):
```
icpc -m32
```

```
473.astar: icpc -m64
```

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Peak Portability Flags

```
   400.perlbench: -DSPEC_CPU_LINUX_IA32
       401.bzip2: -DSPEC_CPU_LP64
         403.gcc: -DSPEC_CPU_LP64
         429.mcf: -DSPEC_CPU_LP64
      456.hmmer: -DSPEC_CPU_LP64
       458.sjeng: -DSPEC_CPU_LP64
   462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
       473.astar: -DSPEC_CPU_LP64
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
                   -ansi-alias

       401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
                   -ansi-alias

         403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
                   -opt-malloc-options=3 -auto-ilp32

         429.mcf: basepeak = yes

       445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
                   -ansi-alias

      456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
                   -ansi-alias

       458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

   462.libquantum: basepeak = yes

      464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
                   -ansi-alias
```

C++ benchmarks:

```
     471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -prof-use(pass 2)
                   -opt-ra-region-strategy=block              -ansi-alias
                   -Wl,-z,muldefs -L/smartheap -lsmartheap
```

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Tested by:	IBM Corporation	**Software Availability:**	Dec-2011

Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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