



# SPEC® CINT2006 Result

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## Fujitsu

**SPECint®2006 = 39.3**

PRIMERGY RX200 S7, Intel Xeon E5-2630L, 2.0 GHz

**SPECint\_base2006 = 36.9**

CPU2006 license: 19

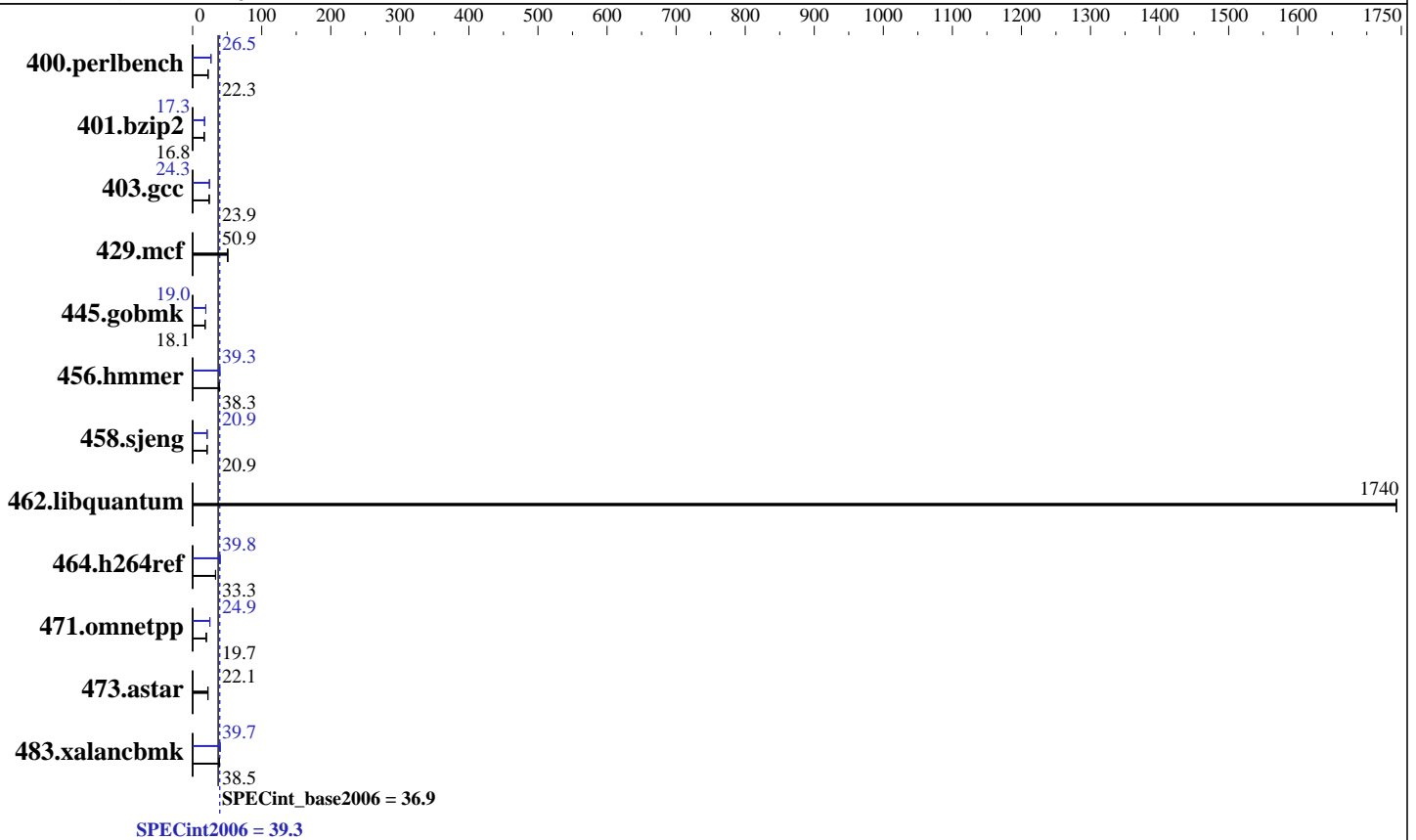
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Feb-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Xeon E5-2630L  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3L-12800R-11, ECC, running at 1333 MHz and CL9)  
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	435	22.4	<b>438</b>	<b>22.3</b>	439	22.3	<b>369</b>	<b>26.5</b>	369	26.5	369	26.5
401.bzip2	575	16.8	<b>575</b>	<b>16.8</b>	575	16.8	<b>558</b>	<b>17.3</b>	558	17.3	557	17.3
403.gcc	336	24.0	<b>336</b>	<b>23.9</b>	336	23.9	<b>331</b>	<b>24.3</b>	331	24.4	331	24.3
429.mcf	182	50.2	<b>179</b>	<b>50.9</b>	179	51.1	182	50.2	<b>179</b>	<b>50.9</b>	179	51.1
445.gobmk	580	18.1	581	18.1	<b>580</b>	<b>18.1</b>	<b>551</b>	<b>19.0</b>	550	19.1	551	19.0
456.hammer	<b>243</b>	<b>38.3</b>	244	38.3	243	38.4	238	39.3	<b>238</b>	<b>39.3</b>	237	39.3
458.sjeng	578	20.9	<b>578</b>	<b>20.9</b>	579	20.9	<b>578</b>	<b>20.9</b>	578	20.9	578	21.0
462.libquantum	<b>11.9</b>	<b>1740</b>	11.9	1740	11.9	1740	<b>11.9</b>	<b>1740</b>	11.9	1740	11.9	1740
464.h264ref	664	33.3	<b>665</b>	<b>33.3</b>	670	33.0	568	38.9	<b>557</b>	<b>39.8</b>	557	39.8
471.omnetpp	<b>316</b>	<b>19.7</b>	315	19.9	317	19.7	251	24.9	<b>251</b>	<b>24.9</b>	251	24.9
473.astar	315	22.3	321	21.9	<b>318</b>	<b>22.1</b>	315	22.3	321	21.9	<b>318</b>	<b>22.1</b>
483.xalancbmk	180	38.3	178	38.7	<b>179</b>	<b>38.5</b>	174	39.7	<b>174</b>	<b>39.7</b>	174	39.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Intel HT Technology = Disable  
Frequency Floor Override = Enable

## General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"  
OMP\_NUM\_THREADS = "12"

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
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## Base Compiler Invocation

C benchmarks:  
icc -m64

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/smartheap -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

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## Peak Compiler Invocation (Continued)

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html>  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml>  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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