



SPEC® CINT2006 Result

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Itaotec

SPECint®_rate2006 = 442

Servidor Itaotec MX224 (Intel Xeon X5690)

SPECint_rate_base2006 = 425

CPU2006 license: 9001

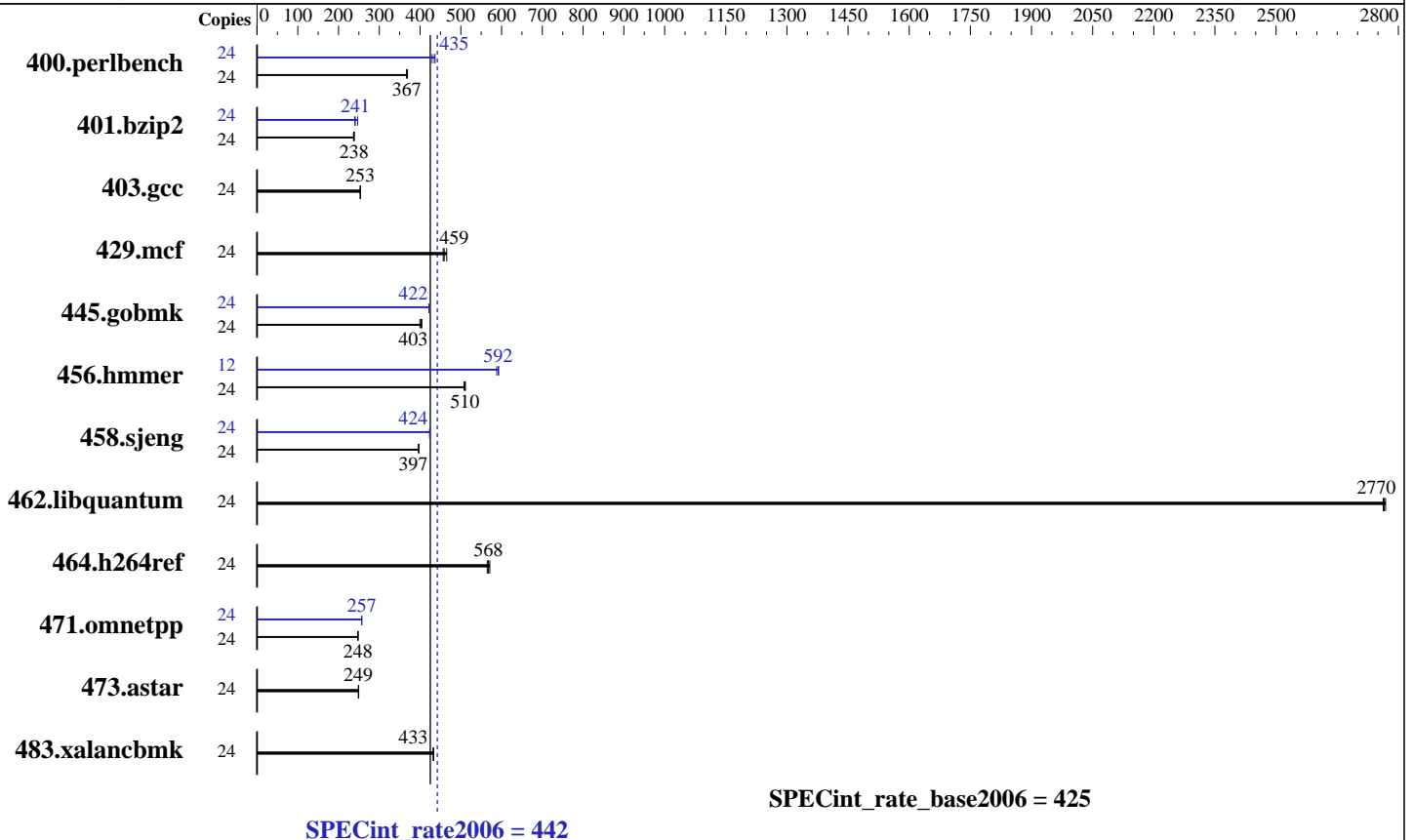
Test date: Mar-2012

Test sponsor: Itaotec

Hardware Availability: Jul-2011

Tested by: Itaotec

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon X5690
 CPU Characteristics: Intel Turbo Boost Technology up to 3.73 GHz
 CPU MHz: 3467
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 2 x 146 GB, SAS, 15000 RPM, RAID 0
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|------------|-------------|------------|------------|------------|------------|--------|------------|-------------|------------|------------|------------|------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 24 | 638 | 367 | 639 | 367 | 635 | 369 | 24 | 539 | 435 | 546 | 430 | 537 | 437 |
| 401.bzip2 | 24 | 978 | 237 | 970 | 239 | 973 | 238 | 24 | 938 | 247 | 967 | 240 | 961 | 241 |
| 403.gcc | 24 | 763 | 253 | 762 | 253 | 762 | 254 | 24 | 763 | 253 | 762 | 253 | 762 | 254 |
| 429.mcf | 24 | 477 | 459 | 470 | 465 | 479 | 457 | 24 | 477 | 459 | 470 | 465 | 479 | 457 |
| 445.gobmk | 24 | 630 | 400 | 625 | 403 | 623 | 404 | 24 | 597 | 422 | 596 | 422 | 597 | 422 |
| 456.hammer | 24 | 439 | 510 | 441 | 508 | 438 | 511 | 12 | 190 | 588 | 189 | 592 | 189 | 593 |
| 458.sjeng | 24 | 733 | 396 | 732 | 397 | 732 | 397 | 24 | 686 | 424 | 685 | 424 | 686 | 424 |
| 462.libquantum | 24 | 180 | 2770 | 180 | 2770 | 180 | 2760 | 24 | 180 | 2770 | 180 | 2770 | 180 | 2760 |
| 464.h264ref | 24 | 935 | 568 | 940 | 565 | 930 | 571 | 24 | 935 | 568 | 940 | 565 | 930 | 571 |
| 471.omnetpp | 24 | 605 | 248 | 606 | 248 | 605 | 248 | 24 | 584 | 257 | 585 | 257 | 584 | 257 |
| 473.astar | 24 | 677 | 249 | 677 | 249 | 675 | 249 | 24 | 677 | 249 | 677 | 249 | 675 | 249 |
| 483.xalancbmk | 24 | 382 | 433 | 383 | 432 | 382 | 433 | 24 | 382 | 433 | 383 | 432 | 382 | 433 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run

Platform Notes

Data Reuse disabled in BIOS.
DCU Prefetcher disabled in BIOS.

General Notes

This result was measured on the Servidor Itaotec MX224.
The Servidor Itaotec MX203+, Servidor Itaotec MX223+ and the Servidor Itaotec MX224 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmarheap

473.astar: basepeak = yes

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Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.html
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.xml
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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