



# SPEC® CINT2006 Result

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## Bull SAS

**SPECint®2006 = 44.3**

NovaScale R460 F3 (Intel Xeon E5-2650, 2.00 GHz)

**SPECint\_base2006 = 41.2**

CPU2006 license: 20

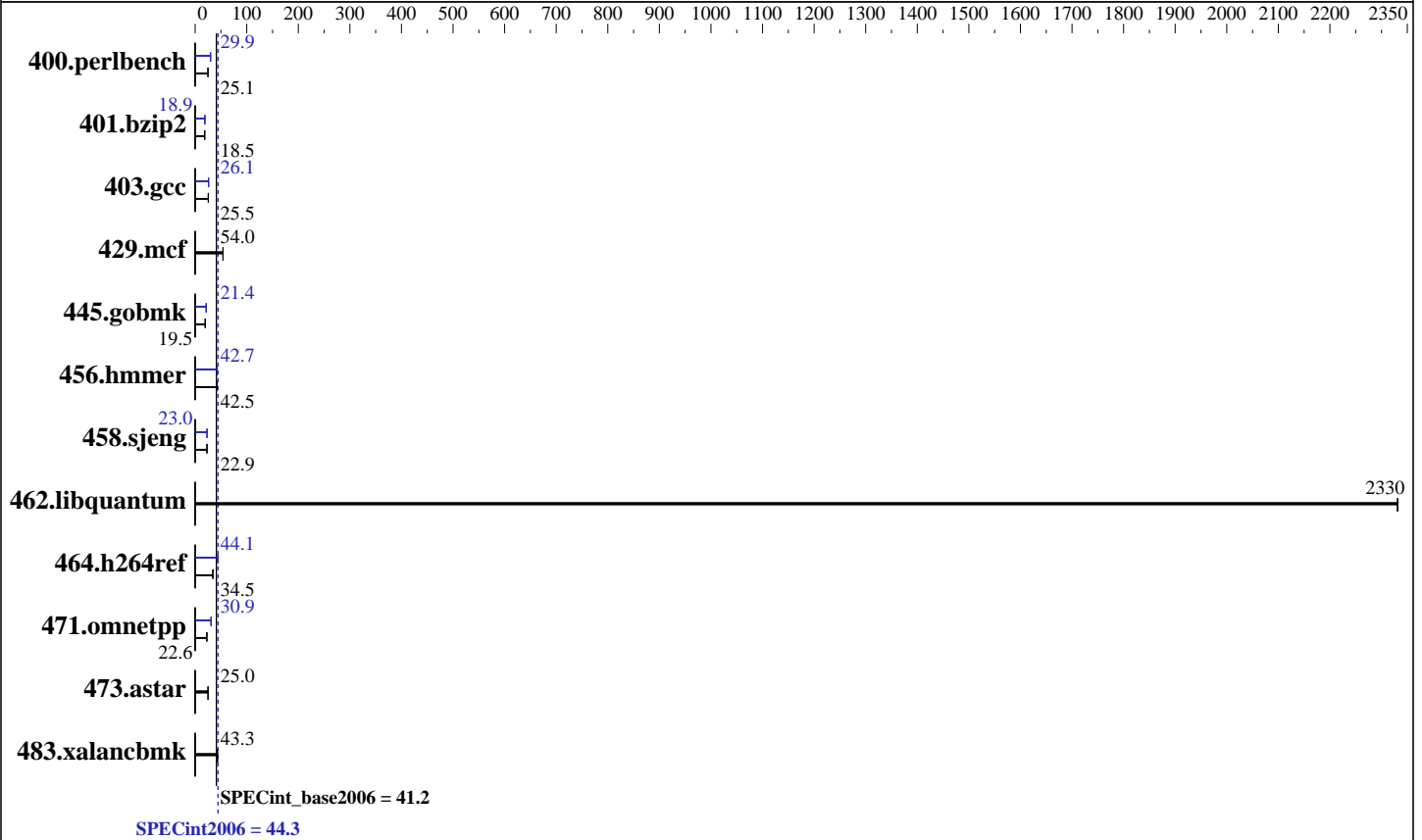
Test date: Feb-2012

Test sponsor: Bull SAS

Hardware Availability: Mar-2012

Tested by: Dell Inc.

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Xeon E5-2650  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 146 GB 10000 RPM SAS  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86\_64) 3.0.13-0.9-default  
 Compiler: C/C++; Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (add definition here)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	398	24.5	<b><u>390</u></b>	<b><u>25.1</u></b>	389	25.1	328	29.8	<b><u>327</u></b>	<b><u>29.9</u></b>	327	29.9
401.bzip2	521	18.5	522	18.5	<b><u>522</u></b>	<b><u>18.5</u></b>	<b><u>511</u></b>	<b><u>18.9</u></b>	511	18.9	511	18.9
403.gcc	<b><u>316</u></b>	<b><u>25.5</u></b>	317	25.4	315	25.5	309	26.1	<b><u>309</u></b>	<b><u>26.1</u></b>	309	26.0
429.mcf	169	54.1	<b><u>169</u></b>	<b><u>54.0</u></b>	169	53.9	169	54.1	<b><u>169</u></b>	<b><u>54.0</u></b>	169	53.9
445.gobmk	537	19.5	<b><u>538</u></b>	<b><u>19.5</u></b>	538	19.5	490	21.4	490	21.4	<b><u>490</u></b>	<b><u>21.4</u></b>
456.hammer	<b><u>219</u></b>	<b><u>42.5</u></b>	219	42.6	220	42.4	<b><u>219</u></b>	<b><u>42.7</u></b>	220	42.4	219	42.7
458.sjeng	529	22.9	<b><u>528</u></b>	<b><u>22.9</u></b>	528	22.9	525	23.1	525	23.0	<b><u>525</u></b>	<b><u>23.0</u></b>
462.libquantum	8.89	2330	8.89	2330	<b><u>8.89</u></b>	<b><u>2330</u></b>	8.89	2330	8.89	2330	<b><u>8.89</u></b>	<b><u>2330</u></b>
464.h264ref	640	34.6	648	34.2	<b><u>642</u></b>	<b><u>34.5</u></b>	<b><u>502</u></b>	<b><u>44.1</u></b>	502	44.1	503	44.0
471.omnetpp	276	22.6	<b><u>276</u></b>	<b><u>22.6</u></b>	275	22.7	<b><u>202</u></b>	<b><u>30.9</u></b>	201	31.0	203	30.8
473.astar	<b><u>281</u></b>	<b><u>25.0</u></b>	281	25.0	281	25.0	<b><u>281</u></b>	<b><u>25.0</u></b>	281	25.0	281	25.0
483.xalancbmk	160	43.1	<b><u>159</u></b>	<b><u>43.3</u></b>	159	43.4	160	43.1	<b><u>159</u></b>	<b><u>43.3</u></b>	159	43.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

```

System Profile set to Custom
CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance
Turbo Boost set to Enabled
C States/C1E set to Enabled
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on linux-r2xm Wed Feb 15 10:45:12 2012

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650 0 @ 2.00GHz
 2 "physical id"s (chips)
 32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7

```

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### Platform Notes (Continued)

physical 1: cores 0 1 2 3 4 5 6 7  
cache size : 20480 KB

```
From /proc/meminfo
MemTotal:      132122696 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 2
```

```
uname -a:
Linux linux-r2xm 3.0.13-0.9-default #1 SMP Mon Jan 16 17:33:03 UTC 2012
(54ddfaf) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Feb 15 10:42 last=S
```

```
SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext3  133G  6.1G  126G   5% /
```

Additional information from dmidecode:

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

```
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64"
OMP_NUM_THREADS = "16"
```

The Dell PowerEdge R720 and the Bull NovaScale R460 F3 models are electronically equivalent. The results have been measured on a Dell PowerEdge R720 model

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop\_caches

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5



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## Base Compiler Invocation

C benchmarks:  
icc -m64  
  
C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
  
C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs  
-L/smartheap -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64

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## Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

471.omnetpp: `icpc -m32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-ansi-alias`

401.bzip2: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch  
-ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias`

456.hmmer: `-xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias`

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## Peak Optimization Flags (Continued)

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120328.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120328.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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