



SPEC[®] CFP2006 Result

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IBM Corporation

IBM BladeCenter HS23 (Intel Xeon E5-2680, 2.70 GHz)

SPECfp[®]2006 = 90.7

SPECfp_base2006 = 85.8

CPU2006 license: 11

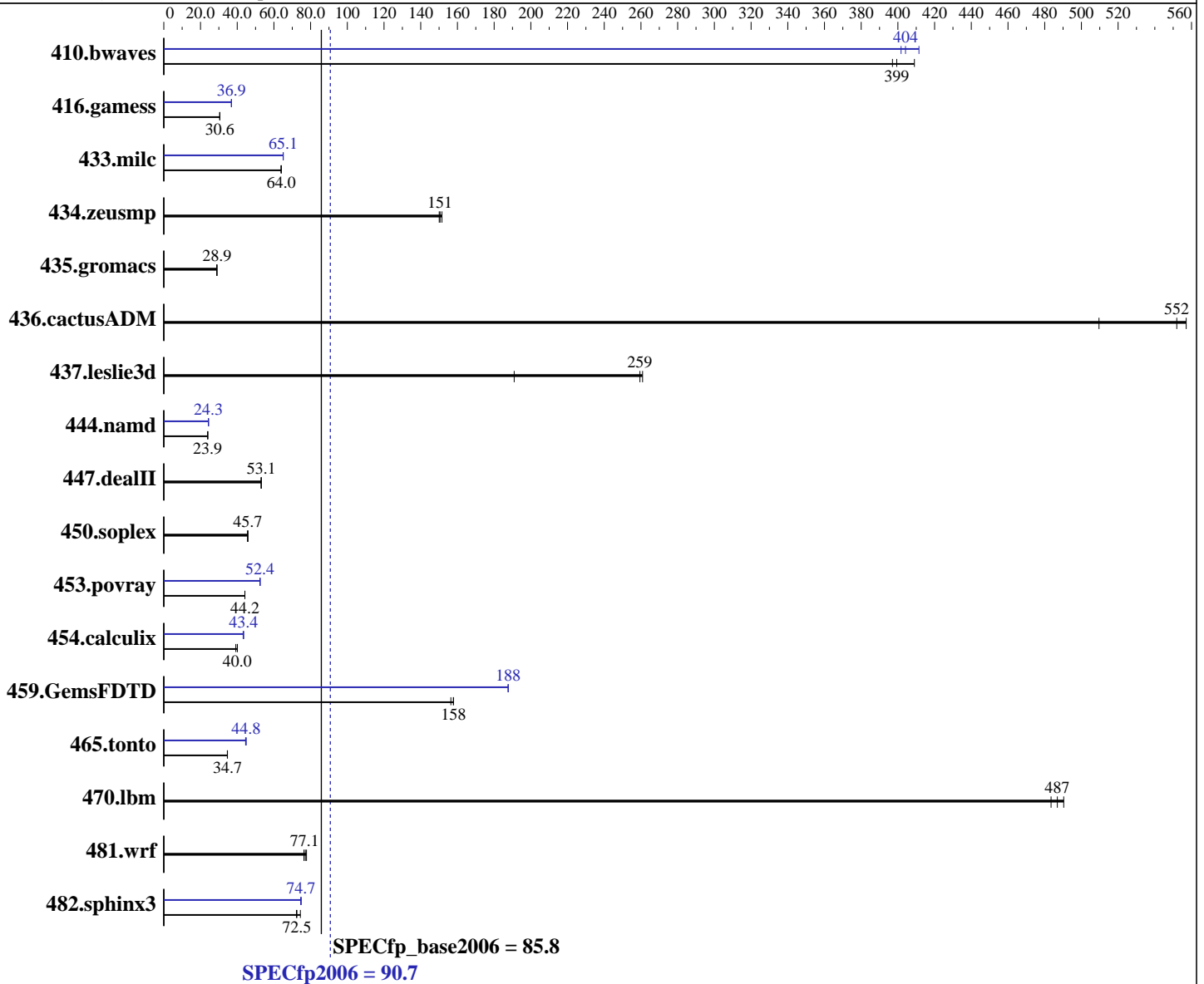
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Feb-2012

Hardware Availability: Mar-2012

Software Availability: Feb-2012



Hardware

CPU Name: Intel Xeon E5-2680
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 2700
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 300 GB SAS, 10000 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	34.2	397	<u>34.0</u>	<u>399</u>	33.2	409	<u>33.6</u>	<u>404</u>	33.0	411	33.8	402
416.gamess	644	30.4	<u>641</u>	<u>30.6</u>	640	30.6	531	36.9	<u>531</u>	<u>36.9</u>	531	36.8
433.milc	144	63.8	143	64.1	<u>143</u>	<u>64.0</u>	141	65.1	141	65.0	<u>141</u>	<u>65.1</u>
434.zeusmp	60.6	150	<u>60.4</u>	<u>151</u>	60.0	152	60.6	150	<u>60.4</u>	<u>151</u>	60.0	152
435.gromacs	247	28.9	<u>247</u>	<u>28.9</u>	246	29.0	247	28.9	<u>247</u>	<u>28.9</u>	246	29.0
436.cactusADM	23.5	510	<u>21.7</u>	<u>552</u>	21.5	557	23.5	510	<u>21.7</u>	<u>552</u>	21.5	557
437.leslie3d	49.2	191	<u>36.2</u>	<u>259</u>	36.0	261	49.2	191	<u>36.2</u>	<u>259</u>	36.0	261
444.namd	<u>335</u>	<u>23.9</u>	335	23.9	335	23.9	330	24.3	330	24.3	<u>330</u>	<u>24.3</u>
447.dealII	<u>215</u>	<u>53.1</u>	215	53.1	216	53.0	<u>215</u>	<u>53.1</u>	215	53.1	216	53.0
450.soplex	<u>182</u>	<u>45.7</u>	183	45.6	182	45.8	<u>182</u>	<u>45.7</u>	183	45.6	182	45.8
453.povray	120	44.2	120	44.2	<u>120</u>	<u>44.2</u>	101	52.5	<u>101</u>	<u>52.4</u>	101	52.4
454.calculix	210	39.2	206	40.1	<u>206</u>	<u>40.0</u>	191	43.2	190	43.5	<u>190</u>	<u>43.4</u>
459.GemsFDTD	<u>67.2</u>	<u>158</u>	67.2	158	67.8	156	56.5	188	<u>56.5</u>	<u>188</u>	56.5	188
465.tonto	<u>284</u>	<u>34.7</u>	284	34.7	284	34.6	221	44.6	219	44.9	<u>220</u>	<u>44.8</u>
470.lbm	28.0	490	28.4	483	<u>28.2</u>	<u>487</u>	28.0	490	28.4	483	<u>28.2</u>	<u>487</u>
481.wrf	<u>145</u>	<u>77.1</u>	144	77.8	146	76.4	<u>145</u>	<u>77.1</u>	144	77.8	146	76.4
482.sphinx3	270	72.3	262	74.4	<u>269</u>	<u>72.5</u>	<u>261</u>	<u>74.7</u>	261	74.6	260	74.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Operating Mode set to Maximum Performance in BIOS
Sysinfo program /cpu2006.1.2/config/sysinfo.rev6800
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3
running on tigershark-pete Mon Feb 27 10:34:40 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

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Platform Notes (Continued)

```
model name : Genuine Intel(R) CPU @ 2.70GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 8
  siblings  : 16
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      132135800 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux tigershark-pete 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST
2011 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Feb 24 13:31
```

```
SPEC is set to: /cpu2006.1.2
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/mapper/vg_tigersharkpet-lv_root
                ext4      265G     9.1G  243G   4% /
```

```
Additional information from dmidecode:
Memory:
  9x Micron 36JDYS1G72PZ-1G6M1 8 GB 1600 MHz 2 rank
  7x Samsung M392B1K70DM0-CK0 8 GB 1600 MHz 2 rank
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64"
OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

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General Notes (Continued)

memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

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Base Optimization Flags (Continued)

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel`

C++ benchmarks:

444.namd: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32`

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Peak Optimization Flags (Continued)

447.deallI: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-A.20120328.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-A.20120328.xml>



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