



SPEC® CFP2006 Result

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Acer Incorporated
(Test Sponsor: Acer Incorporation)

SPECfp®2006 = 64.8

Acer AT310 F2 (Xeon E3-1280)

SPECfp_base2006 = 62.9

CPU2006 license: 97

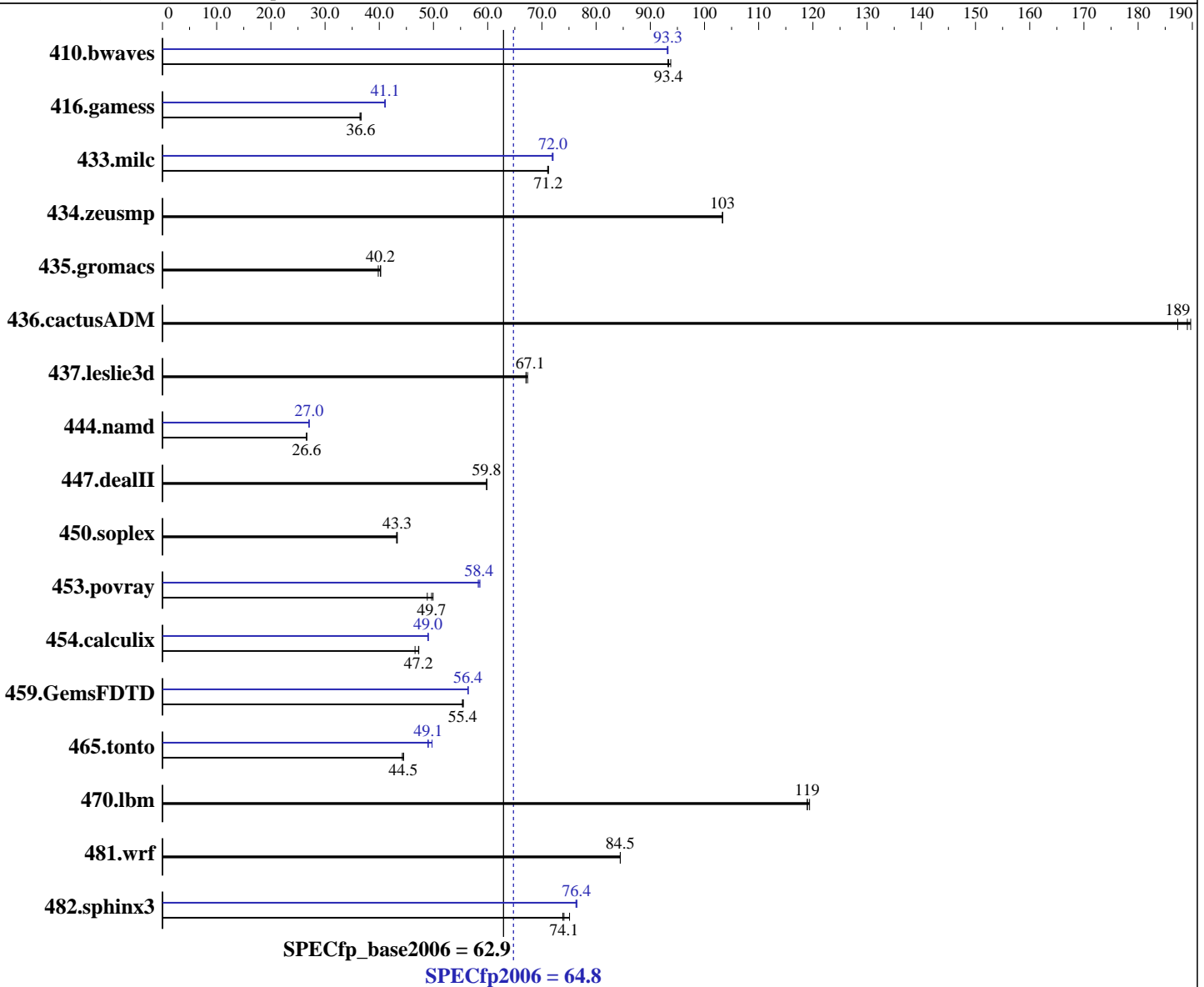
Test date: Dec-2011

Test sponsor: Acer Incorporation

Hardware Availability: Jan-2012

Tested by: Acer Incorporation

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E3-1280
 CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz
 CPU MHz: 3500
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.1
 Kernel 2.6.32-131.0.15.el6.x86_64-default
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9,ECC)
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V9.01

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	146	93.3	145	93.8	146	93.4	146	93.3	146	93.3	146	93.1
416.gamess	537	36.4	535	36.6	535	36.6	477	41.1	478	41.0	476	41.1
433.milc	129	71.2	129	71.2	129	71.1	127	72.0	128	71.9	127	72.1
434.zeusmp	88.1	103	88.1	103	88.1	103	88.1	103	88.1	103	88.1	103
435.gromacs	179	39.8	178	40.2	177	40.3	179	39.8	178	40.2	177	40.3
436.cactusADM	63.0	190	63.8	187	63.2	189	63.0	190	63.8	187	63.2	189
437.leslie3d	140	67.4	140	67.1	140	67.1	140	67.4	140	67.1	140	67.1
444.namd	301	26.6	301	26.6	302	26.6	296	27.1	297	27.0	297	27.0
447.dealII	191	59.8	191	59.8	191	59.8	191	59.8	191	59.8	191	59.8
450.soplex	193	43.3	193	43.2	193	43.3	193	43.3	193	43.2	193	43.3
453.povray	109	48.9	107	49.7	107	49.9	91.1	58.4	91.3	58.2	90.8	58.6
454.calculix	175	47.2	174	47.3	177	46.6	168	49.0	168	49.1	168	49.0
459.GemsFDTD	192	55.4	191	55.5	192	55.3	188	56.4	188	56.4	188	56.4
465.tonto	222	44.3	221	44.5	221	44.5	201	49.1	201	49.0	198	49.7
470.lbm	115	119	115	119	115	119	115	119	115	119	115	119
481.wrf	132	84.5	132	84.5	132	84.5	132	84.5	132	84.5	132	84.5
482.sphinx3	260	75.1	263	74.1	264	73.8	255	76.4	255	76.5	256	76.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Large pages were disabled for this run.

General Notes

Binaries compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:
icc -m64

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Base Compiler Invocation (Continued)

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias



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Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

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Peak Optimization Flags (Continued)

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact webmaster@spec.org.

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