



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

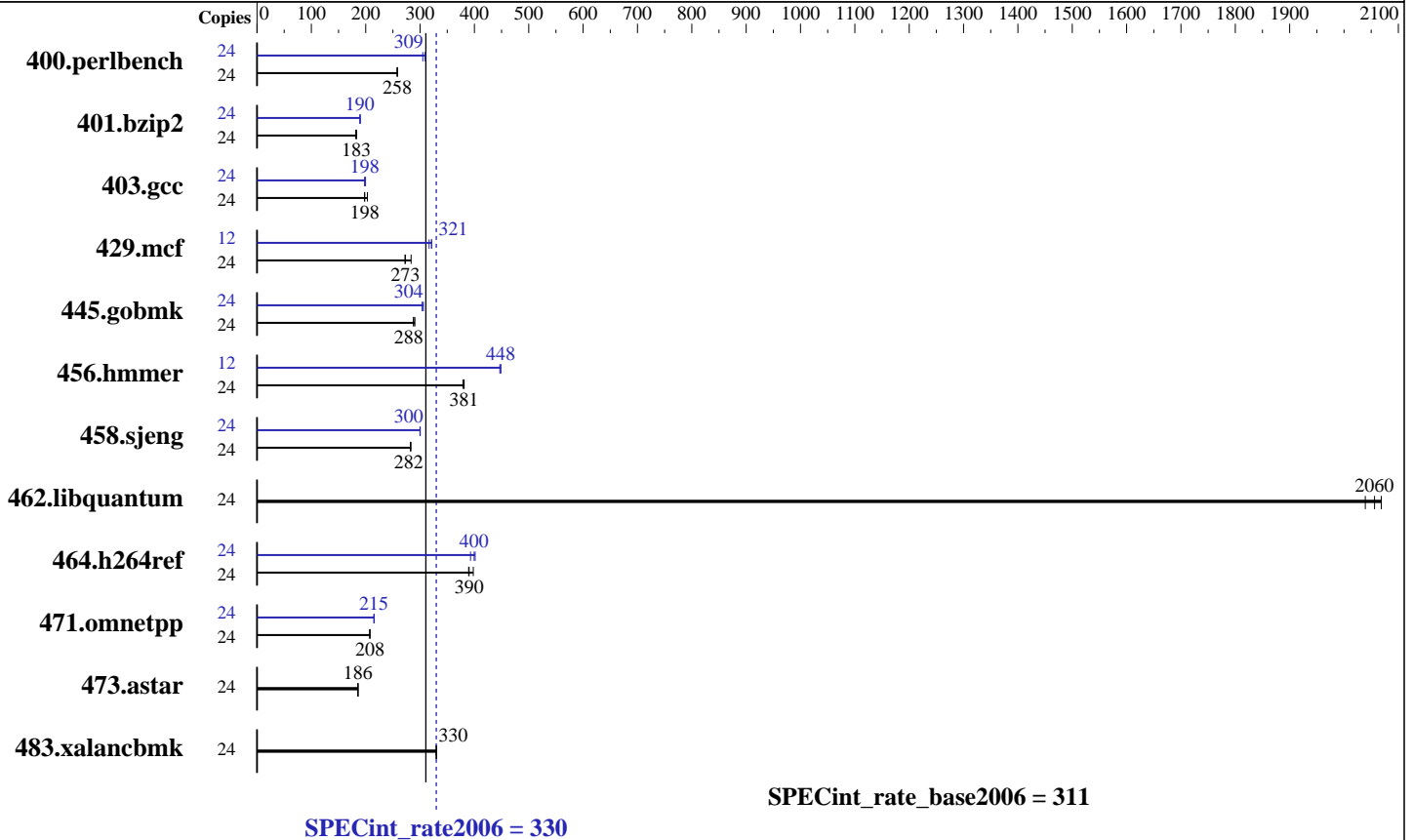
SPECint®\_rate2006 = 330

Servidor Itautec MX223+ (Intel Xeon E5649)

SPECint\_rate\_base2006 = 311

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Dec-2011  
Hardware Availability: Jul-2011  
Software Availability: Aug-2011



## Hardware

CPU Name: Intel Xeon E5649  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.93 GHz  
 CPU MHz: 2533  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 500 GB SAS, 15000 RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: C/C++: Version 12.1.0 of Intel Compiler XE Build 20110811  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint\_rate2006 = 330

Servidor Itaotec MX223+ (Intel Xeon E5649)

SPECint\_rate\_base2006 = 311

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Dec-2011  
Hardware Availability: Jul-2011  
Software Availability: Aug-2011

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	<b>909</b>	<b>258</b>	909	258	907	258	24	<b>758</b>	<b>309</b>	757	310	769	305
401.bzip2	24	<b>1265</b>	<b>183</b>	1275	182	1265	183	24	<b>1222</b>	<b>190</b>	1219	190	1222	190
403.gcc	24	<b>975</b>	<b>198</b>	951	203	977	198	24	976	198	<b>974</b>	<b>198</b>	967	200
429.mcf	24	772	283	804	272	<b>802</b>	<b>273</b>	12	346	317	341	321	<b>341</b>	<b>321</b>
445.gobmk	24	867	290	874	288	<b>873</b>	<b>288</b>	24	822	306	828	304	<b>828</b>	<b>304</b>
456.hammer	24	588	381	<b>588</b>	<b>381</b>	591	379	12	250	448	<b>250</b>	<b>448</b>	251	447
458.sjeng	24	1029	282	<b>1028</b>	<b>282</b>	1026	283	24	<b>968</b>	<b>300</b>	967	300	969	300
462.libquantum	24	<b>242</b>	<b>2060</b>	240	2070	244	2040	24	<b>242</b>	<b>2060</b>	240	2070	244	2040
464.h264ref	24	1335	398	<b>1361</b>	<b>390</b>	1364	389	24	<b>1329</b>	<b>400</b>	1352	393	1323	401
471.omnetpp	24	722	208	723	208	<b>723</b>	<b>208</b>	24	<b>697</b>	<b>215</b>	697	215	696	215
473.astar	24	908	186	<b>907</b>	<b>186</b>	906	186	24	908	186	<b>907</b>	<b>186</b>	906	186
483.xalancbmk	24	502	330	501	330	<b>502</b>	<b>330</b>	24	502	330	501	330	<b>502</b>	<b>330</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
Large pages were not enabled for this run

## Platform Notes

Data Reuse disabled in BIOS.

## General Notes

This result was measured on the Servidor Itaotec MX224.  
The Servidor Itaotec MX203+, Servidor Itaotec MX223+ and the Servidor Itaotec MX224 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint\_rate2006 = 330

Servidor Itaotec MX223+ (Intel Xeon E5649)

SPECint\_rate\_base2006 = 311

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Dec-2011  
Hardware Availability: Jul-2011  
Software Availability: Aug-2011

## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint\_rate2006 = 330

Servidor Itaotec MX223+ (Intel Xeon E5649)

SPECint\_rate\_base2006 = 311

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Dec-2011  
Hardware Availability: Jul-2011  
Software Availability: Aug-2011

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint\_rate2006 = 330

Servidor Itaotec MX223+ (Intel Xeon E5649)

SPECint\_rate\_base2006 = 311

CPU2006 license: 9001

Test sponsor: Itaotec

Tested by: Itaotec

Test date: Dec-2011

Hardware Availability: Jul-2011

Software Availability: Aug-2011

## Peak Optimization Flags (Continued)

471.omnetpp (continued):

-L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html>

<http://www.spec.org/cpu2006/flags/Itaotec-Intel-Linux64-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml>

<http://www.spec.org/cpu2006/flags/Itaotec-Intel-Linux64-Platform.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Thu Jul 24 03:17:07 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 3 January 2012.

Standard Performance Evaluation Corporation

[info@spec.org](mailto:info@spec.org)

<http://www.spec.org/>