



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint®2006 = 48.5

Express5800/R120b-2 (Intel Xeon X5687)

SPECint_base2006 = 45.6

CPU2006 license: 9006

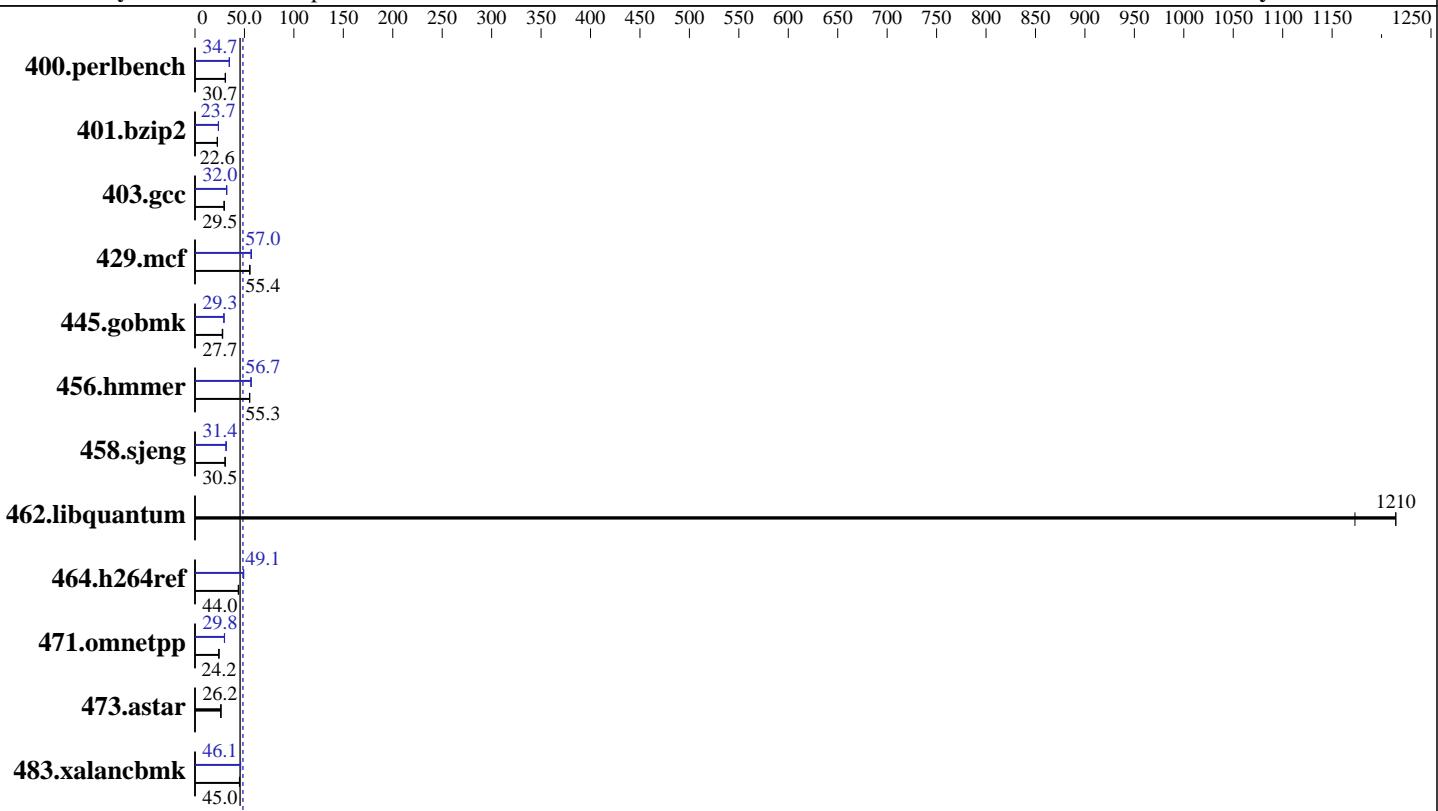
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jun-2011

Hardware Availability: Feb-2011

Software Availability: Mar-2011



Hardware

CPU Name: Intel Xeon X5687
 CPU Characteristics: Intel Turbo Boost Technology up to 3.87 GHz
 CPU MHz: 3600
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: C/C++: Version 12.0.3.174 of Intel Compiler XE Build 20110309
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/R120b-2 (Intel Xeon X5687)

SPECint2006 = 48.5

CPU2006 license: 9006

Test date: Jun-2011

Test sponsor: NEC Corporation

Hardware Availability: Feb-2011

Tested by: NEC Corporation

Software Availability: Mar-2011

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	318	30.7	319	30.7	318	30.7	281	34.7	281	34.7	281	34.7
401.bzip2	427	22.6	427	22.6	427	22.6	407	23.7	407	23.7	407	23.7
403.gcc	271	29.7	273	29.5	274	29.3	251	32.0	251	32.0	251	32.0
429.mcf	165	55.4	165	55.4	164	55.4	161	56.6	160	57.0	160	57.0
445.gobmk	374	28.1	379	27.7	379	27.7	358	29.3	358	29.3	359	29.3
456.hammer	169	55.2	169	55.3	169	55.3	165	56.7	165	56.7	165	56.7
458.sjeng	396	30.5	396	30.6	398	30.4	385	31.5	385	31.4	385	31.4
462.libquantum	17.7	1170	17.1	1210	17.1	1210	17.7	1170	17.1	1210	17.1	1210
464.h264ref	503	44.0	506	43.7	501	44.2	450	49.1	450	49.1	450	49.1
471.omnetpp	259	24.1	259	24.2	258	24.2	209	29.9	210	29.8	210	29.7
473.astar	268	26.2	269	26.1	268	26.2	268	26.2	269	26.1	268	26.2
483.xalancbmk	154	44.9	153	45.1	153	45.0	150	46.1	150	46.1	150	46.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 1800 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

BIOS Settings:
Hyper-Threading Technology: Disabled
Performance/Watt: Traditional
Server Class: Custom
Data Reuse Optimization: Disabled
Memory Voltage: Normal

General Notes

OMP_NUM_THREADS set to number of cores

Base Compiler Invocation

C benchmarks:
icc -m64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/R120b-2 (Intel Xeon X5687)

SPECint2006 = 48.5

SPECint_base2006 = 45.6

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jun-2011

Hardware Availability: Feb-2011

Software Availability: Mar-2011

Base Compiler Invocation (Continued)

C++ benchmarks:

`icpc -m64`

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hammer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib64 -lsmartheap64
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

`400.perlbench: icc -m32`

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation	SPECint2006 =	48.5
Express5800/R120b-2 (Intel Xeon X5687)	SPECint_base2006 =	45.6
CPU2006 license: 9006	Test date:	Jun-2011
Test sponsor: NEC Corporation	Hardware Availability:	Feb-2011
Tested by: NEC Corporation	Software Availability:	Mar-2011

Peak Compiler Invocation (Continued)

429.mcf: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

429.mcf: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -auto-ilp32 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint2006 = 48.5

Express5800/R120b-2 (Intel Xeon X5687)

SPECint_base2006 = 45.6

CPU2006 license: 9006

Test date: Jun-2011

Test sponsor: NEC Corporation

Hardware Availability: Feb-2011

Tested by: NEC Corporation

Software Availability: Mar-2011

Peak Optimization Flags (Continued)

456.hmmr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32

-ansi-alias

-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)

-unroll14

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)

-unroll12 -ansi-alias

-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)

-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs

-L/opt/SmartHeap_8.1/lib -lsmartheap

-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revF.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revF.xml>



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint2006 = 48.5

Express5800/R120b-2 (Intel Xeon X5687)

SPECint_base2006 = 45.6

CPU2006 license: 9006

Test date: Jun-2011

Test sponsor: NEC Corporation

Hardware Availability: Feb-2011

Tested by: NEC Corporation

Software Availability: Mar-2011

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Thu Jul 24 00:37:23 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 20 December 2011.