



# SPEC® CFP2006 Result

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Dell Inc.

SPECfp®2006 = 39.5

PowerEdge R510 (Intel Xeon E5607, 2.26 GHz)

SPECfp\_base2006 = 38.0

CPU2006 license: 55

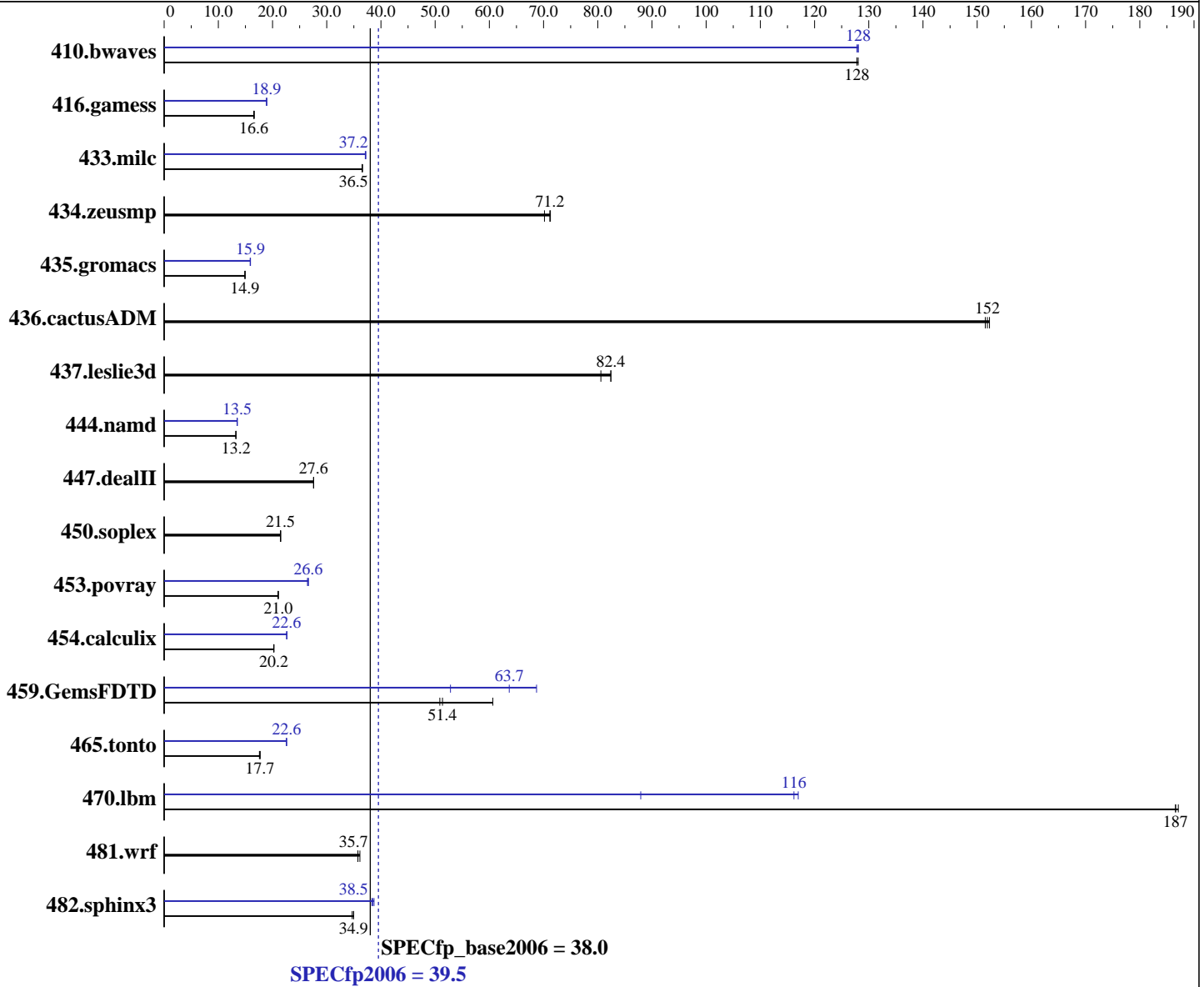
Test date: May-2011

Test sponsor: Dell Inc.

Hardware Availability: Feb-2011

Tested by: Dell Inc.

Software Availability: Apr-2011



### Hardware

CPU Name: Intel Xeon E5607  
 CPU Characteristics:  
 CPU MHz: 2267  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (6 x 8 GB 2Rx4 PC3-10600R-9, ECC, running at 1066 MHz)  
 Disk Subsystem: 2 x 146 GB 15000 RPM SAS  
 Other Hardware: None

Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<b>106</b>	<b>128</b>	106	128	106	128	<b>106</b>	<b>128</b>	106	128	106	128
416.gamess	1177	16.6	<b>1182</b>	<b>16.6</b>	1184	16.5	<b>1036</b>	<b>18.9</b>	1035	18.9	1036	18.9
433.milc	<b>251</b>	<b>36.5</b>	251	36.6	251	36.5	247	37.1	<b>247</b>	<b>37.2</b>	247	37.2
434.zeusmp	130	70.2	<b>128</b>	<b>71.2</b>	128	71.3	130	70.2	<b>128</b>	<b>71.2</b>	128	71.3
435.gromacs	478	15.0	479	14.9	<b>479</b>	<b>14.9</b>	<b>448</b>	<b>15.9</b>	448	15.9	450	15.9
436.cactusADM	<b>78.7</b>	<b>152</b>	78.5	152	78.9	151	<b>78.7</b>	<b>152</b>	78.5	152	78.9	151
437.leslie3d	<b>114</b>	<b>82.4</b>	117	80.6	114	82.4	<b>114</b>	<b>82.4</b>	117	80.6	114	82.4
444.namd	605	13.2	<b>605</b>	<b>13.2</b>	606	13.2	596	13.5	<b>595</b>	<b>13.5</b>	595	13.5
447.dealII	<b>415</b>	<b>27.6</b>	415	27.6	415	27.6	<b>415</b>	<b>27.6</b>	415	27.6	415	27.6
450.soplex	389	21.4	388	21.5	<b>388</b>	<b>21.5</b>	389	21.4	388	21.5	<b>388</b>	<b>21.5</b>
453.povray	253	21.0	<b>253</b>	<b>21.0</b>	253	21.1	<b>200</b>	<b>26.6</b>	201	26.4	200	26.6
454.calculix	<b>407</b>	<b>20.2</b>	408	20.2	407	20.2	365	22.6	<b>365</b>	<b>22.6</b>	364	22.6
459.GemsFDTD	175	60.6	209	50.9	<b>207</b>	<b>51.4</b>	201	52.8	<b>167</b>	<b>63.7</b>	154	68.7
465.tonto	556	17.7	560	17.6	<b>556</b>	<b>17.7</b>	<b>436</b>	<b>22.6</b>	436	22.6	436	22.6
470.lbm	73.6	187	73.4	187	<b>73.6</b>	<b>187</b>	156	87.9	117	117	<b>118</b>	<b>116</b>
481.wrf	309	36.1	312	35.7	<b>312</b>	<b>35.7</b>	309	36.1	312	35.7	<b>312</b>	<b>35.7</b>
482.sphinx3	<b>558</b>	<b>34.9</b>	562	34.7	557	35.0	508	38.3	<b>506</b>	<b>38.5</b>	503	38.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

BIOS Settings:  
 Power Management = Maximum Performance (Default = Active Power Controller)  
 Data Reuse = Disabled (Default = Enabled)



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## General Notes

OMP\_NUM\_THREADS set to number of cores  
Binaries were compiled on RHEL5.5  
The Dell PowerEdge R510 and  
the Bull NovaScale R450 F2 models are electronically equivalent.  
The results have been measured on a Dell PowerEdge R510 model.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -parallel  
-ansi-alias -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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