SPEC® CINT2006 Result
2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GW2000h-GW170h F1 (Intel Xeon X5675, 3.06GHz)

SPECint®_rate2006 =   396
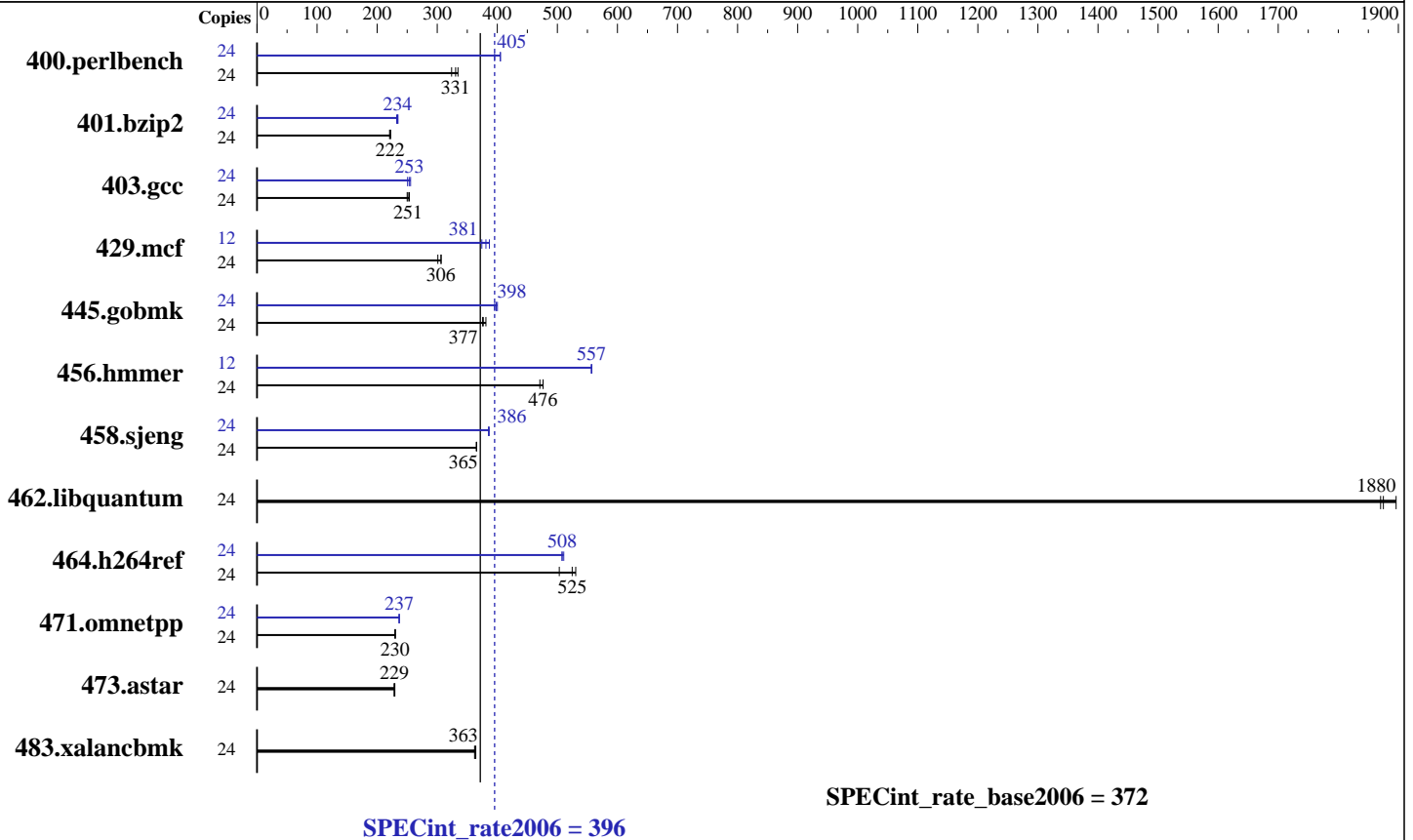
SPECint_rate_base2006 =   372

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: May-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011

SPECint_rate_base2006 = 372

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Hardware

CPU Name:	Intel Xeon X5675
CPU Characteristics:	Intel Turbo Boost Technology up to 3.46 GHz
CPU MHz:	3067
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)
Disk Subsystem:	1 x 1000 GB SATA 7200RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	701	335	724	324	**709**	**331**	24	593	396	579	405	**579**	**405**
401.bzip2	24	**1044**	**222**	1050	221	1041	222	24	987	235	**989**	**234**	996	232
403.gcc	24	762	254	773	250	**769**	**251**	24	**762**	**253**	756	256	771	251
429.mcf	24	727	301	714	306	**715**	**306**	12	**287**	**381**	292	374	283	387
445.gobmk	24	661	381	**668**	**377**	670	376	24	629	400	**632**	**398**	636	396
456.hmmer	24	470	476	**470**	**476**	476	471	12	201	557	**201**	**557**	201	557
458.sjeng	24	**795**	**365**	795	365	794	366	24	753	385	751	386	**753**	**386**
462.libquantum	24	262	1900	266	1870	**265**	**1880**	24	262	1900	266	1870	**265**	**1880**
464.h264ref	24	**1012**	**525**	1001	531	1055	503	24	1046	508	**1046**	**508**	1040	511
471.omnetpp	24	653	230	**652**	**230**	651	230	24	633	237	634	237	**634**	**237**
473.astar	24	**736**	**229**	734	229	738	228	24	**736**	**229**	734	229	738	228
483.xalancbmk	24	455	364	**456**	**363**	457	362	24	455	364	**456**	**363**	457	362

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

    'ulimit -s unlimited' was used to set environment stack size
    Large pages were disabled for this run

Platform Notes

    BIOS Settings:
    Fan speed = full speed (Default = Energy Saving)
    Data Reuse = Disabled (Default = Enabled)

General Notes

    Binaries compiled on RHEL5.5

    The Acer AW2000h-AW170h F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1 and Gateway GW2000ht-GW170ht F1
    are electronically equivalent. This result was measured on Gateway GW2000ht-GW170ht F1.

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Base Compiler Invocation

C benchmarks:
      `icc  -m32`

C++ benchmarks:
      `icpc -m32`

Base Portability Flags

      400.perlbench: `-DSPEC_CPU_LINUX_IA32`
      462.libquantum: `-DSPEC_CPU_LINUX`
      483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
      `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch`
      `-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

C++ benchmarks:
      `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs`
      `-L/smartheap -lsmartheap`
      `-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

Base Other Flags

C benchmarks:

      403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
      `icc  -m32`

   400.perlbench: `icc -m64`

   401.bzip2: `icc -m64`

   456.hmmer: `icc -m64`

   458.sjeng: `icc -m64`

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Peak Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m32

Peak Portability Flags

       400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
          401.bzip2: -DSPEC_CPU_LP64
          456.hmmer: -DSPEC_CPU_LP64
          458.sjeng: -DSPEC_CPU_LP64
      462.libquantum: -DSPEC_CPU_LINUX
      483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

       400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                      -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                      -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

          401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                     -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                     -opt-prefetch -auto-ilp32 -ansi-alias
                     -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

            403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
                     -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

            429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                     -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                     -ansi-alias -auto-ilp32

          445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
                     -ansi-alias -auto-ilp32

          456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
                     -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

          458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                     -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                     -unroll4 -auto-ilp32
                     -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

      462.libquantum: basepeak = yes

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Peak Optimization Flags (Continued)

464.h264ref: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/smartheap -lsmartheap`

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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