



# SPEC® CFP2006 Result

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## IBM Corporation

SPECfp®\_rate2006 = 286

### IBM System x3690 X5 (Intel Xeon E7-2830)

SPECfp\_rate\_base2006 = 274

CPU2006 license: 11

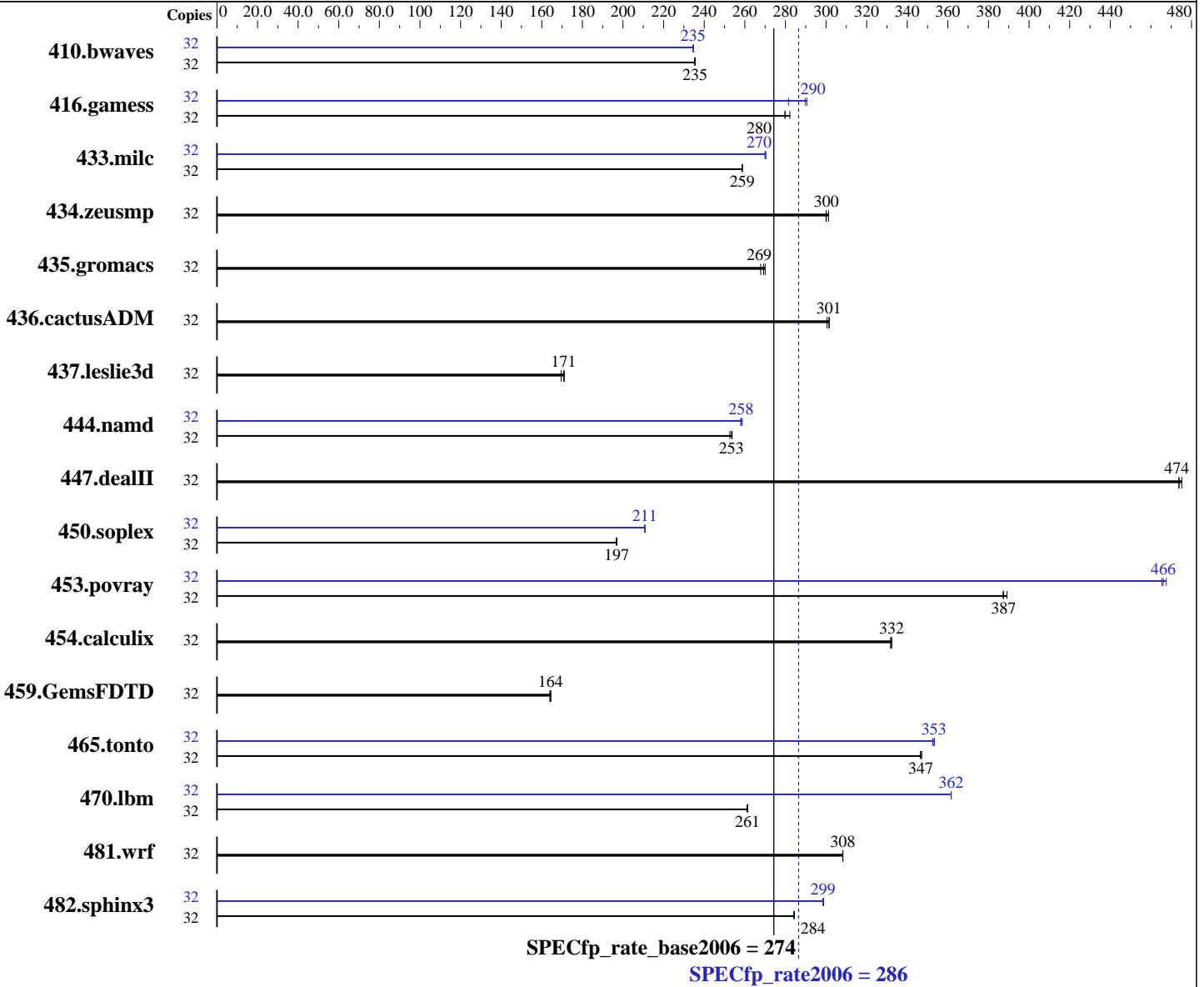
Test date: May-2011

Test sponsor: IBM Corporation

Hardware Availability: May-2011

Tested by: IBM Corporation

Software Availability: Jan-2011



### Hardware

CPU Name: Intel Xeon E7-2830  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz  
 CPU MHz: 2133  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 24 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (32 x 8 GB 4Rx8 PC3-8500R-7, ECC)  
 Disk Subsystem: 1 x 300 GB SAS, 10000 RPM  
 Other Hardware: None

Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	32	1846	236	<b>1847</b>	<b>235</b>	1848	235	32	1854	235	1853	235	<b>1854</b>	<b>235</b>
416.gamess	32	<b>2239</b>	<b>280</b>	2239	280	2220	282	32	2226	282	2156	291	<b>2162</b>	<b>290</b>
433.milc	32	1135	259	1136	259	<b>1136</b>	<b>259</b>	32	1088	270	<b>1087</b>	<b>270</b>	1087	270
434.zeusmp	32	967	301	971	300	<b>970</b>	<b>300</b>	32	967	301	971	300	<b>970</b>	<b>300</b>
435.gromacs	32	853	268	846	270	<b>849</b>	<b>269</b>	32	853	268	846	270	<b>849</b>	<b>269</b>
436.cactusADM	32	<b>1269</b>	<b>301</b>	1268	302	1273	300	32	<b>1269</b>	<b>301</b>	1268	302	1273	300
437.leslie3d	32	<b>1761</b>	<b>171</b>	1774	170	1758	171	32	<b>1761</b>	<b>171</b>	1774	170	1758	171
444.namd	32	1011	254	<b>1013</b>	<b>253</b>	1016	253	32	992	259	<b>994</b>	<b>258</b>	995	258
447.dealII	32	770	475	<b>773</b>	<b>474</b>	773	474	32	770	475	<b>773</b>	<b>474</b>	773	474
450.soplex	32	1357	197	<b>1355</b>	<b>197</b>	1355	197	32	1266	211	<b>1266</b>	<b>211</b>	1267	211
453.povray	32	440	387	437	389	<b>439</b>	<b>387</b>	32	<b>365</b>	<b>466</b>	366	465	364	468
454.calculix	32	794	332	796	332	<b>794</b>	<b>332</b>	32	794	332	796	332	<b>794</b>	<b>332</b>
459.GemsFDTD	32	<b>2065</b>	<b>164</b>	2065	164	2071	164	32	<b>2065</b>	<b>164</b>	2065	164	2071	164
465.tonto	32	909	346	907	347	<b>908</b>	<b>347</b>	32	893	353	<b>892</b>	<b>353</b>	891	353
470.lbm	32	1684	261	1682	261	<b>1683</b>	<b>261</b>	32	<b>1216</b>	<b>362</b>	1216	362	1216	362
481.wrf	32	1160	308	1159	308	<b>1159</b>	<b>308</b>	32	1160	308	1159	308	<b>1159</b>	<b>308</b>
482.sphinx3	32	2195	284	2193	284	<b>2194</b>	<b>284</b>	32	2089	299	<b>2089</b>	<b>299</b>	2087	299

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
 numactl was used to bind copies to the cores

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited
'nodenv /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
echo 19200 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```



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## Platform Notes

Load Default BIOS Settings and then change the following  
Turbo Boost set to Traditional

## General Notes

Binaries compiled on RHEL 5.5

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.deallI: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

### C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

### C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

### Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

### Benchmarks using both Fortran and C:

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## Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 17:54:16 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 7 June 2011.