



# SPEC® CINT2006 Result

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## Hewlett-Packard Company

SPECint®\_rate2006 = 165

ProLiant ML110 G7 (3.50 GHz Intel Xeon E3-1280)

SPECint\_rate\_base2006 = 157

CPU2006 license: 3

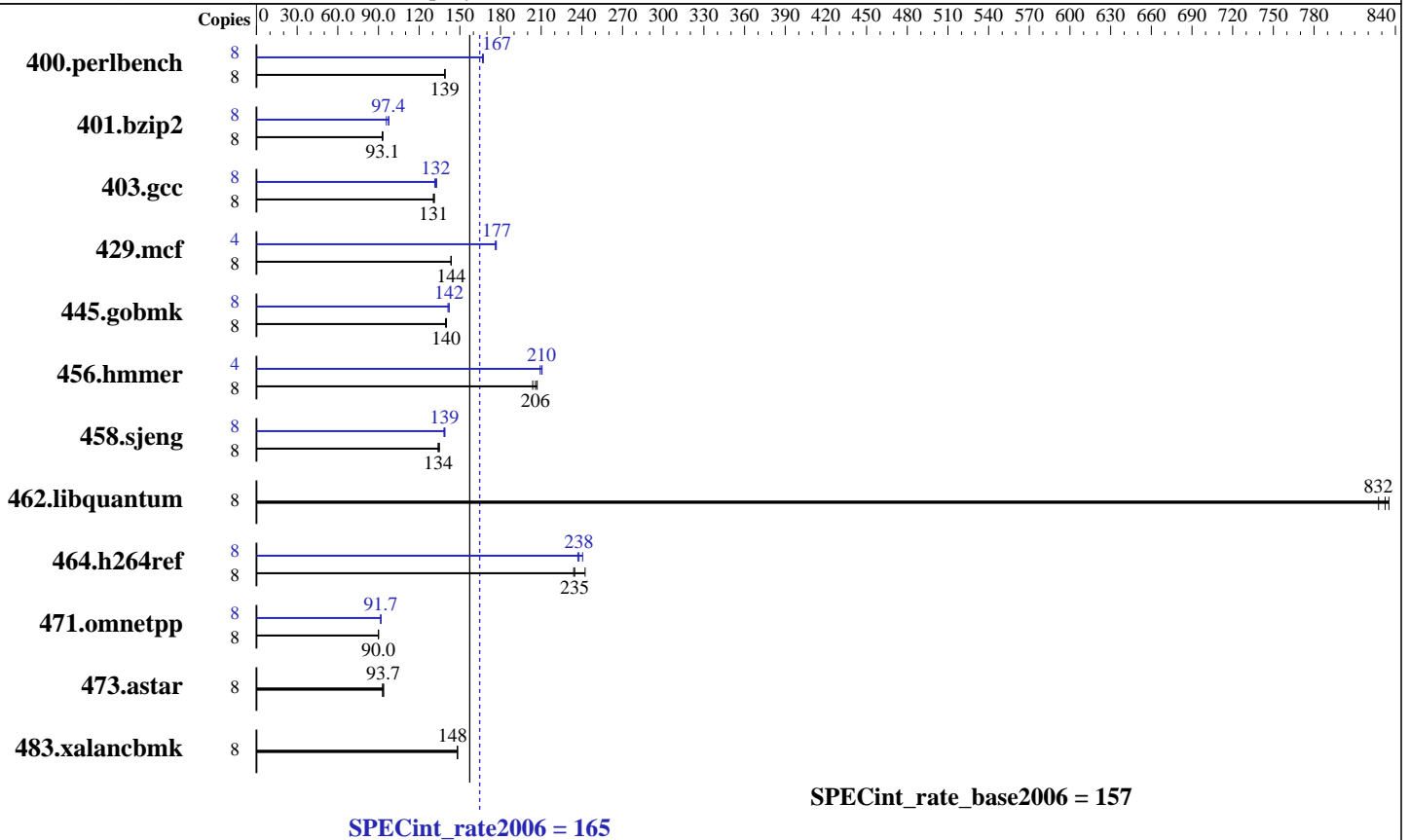
Test date: May-2011

Test sponsor: Hewlett-Packard Company

Hardware Availability: Mar-2011

Tested by: Hewlett-Packard Company

Software Availability: Dec-2010



### Hardware

CPU Name: Intel Xeon E3-1280  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz  
 CPU MHz: 3500  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (4 x 4 GB 2Rx8 PC3-10600E-9, ECC)  
 Disk Subsystem: 1 x 250 GB 7.2 K SATA  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32, Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	563	139	<u>562</u>	<u>139</u>	562	139	8	<u>468</u>	<u>167</u>	469	167	467	167
401.bzip2	8	833	92.7	<u>830</u>	<u>93.1</u>	829	93.1	8	791	97.6	<u>793</u>	<u>97.4</u>	806	95.8
403.gcc	8	493	131	<u>493</u>	<u>131</u>	491	131	8	490	131	485	133	<u>487</u>	<u>132</u>
429.mcf	8	508	144	509	143	<u>508</u>	<u>144</u>	4	<u>207</u>	<u>177</u>	206	177	207	176
445.gobmk	8	599	140	602	139	<u>600</u>	<u>140</u>	8	590	142	<u>591</u>	<u>142</u>	594	141
456.hammer	8	366	204	<u>363</u>	<u>206</u>	360	207	4	178	209	177	211	<u>177</u>	<u>210</u>
458.sjeng	8	722	134	717	135	<u>721</u>	<u>134</u>	8	700	138	696	139	<u>698</u>	<u>139</u>
462.libquantum	8	<u>199</u>	<u>832</u>	200	828	198	835	8	<u>199</u>	<u>832</u>	200	828	198	835
464.h264ref	8	757	234	730	242	<u>754</u>	<u>235</u>	8	736	241	<u>744</u>	<u>238</u>	747	237
471.omnetpp	8	555	90.0	556	89.9	<u>556</u>	<u>90.0</u>	8	544	91.8	<u>545</u>	<u>91.7</u>	546	91.6
473.astar	8	599	93.7	604	92.9	<u>599</u>	<u>93.7</u>	8	599	93.7	604	92.9	<u>599</u>	<u>93.7</u>
483.xalancbmk	8	373	148	<u>372</u>	<u>148</u>	372	148	8	373	148	<u>372</u>	<u>148</u>	372	148

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

numactl was used to bind copies to the cores  
The config file option 'submit' was used.

## Operating System Notes

```
'nodev /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
echo 3600 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

BIOS configuration:  
HP Power Profile set to Maximum Performance  
Thermal Configuration set to Increased Cooling

## General Notes

Binaries compiled on RHEL5.5



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## Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

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## Peak Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xAVX -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias  
-auto-ilp32

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

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## Peak Optimization Flags (Continued)

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias  
-opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20110316.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20110316.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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