



# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

SPECfp®2006 = 54.5

NovaScale R480 F2 (Intel Xeon X7560, 2.27 GHz)

SPECfp\_base2006 = 49.7

CPU2006 license: 20

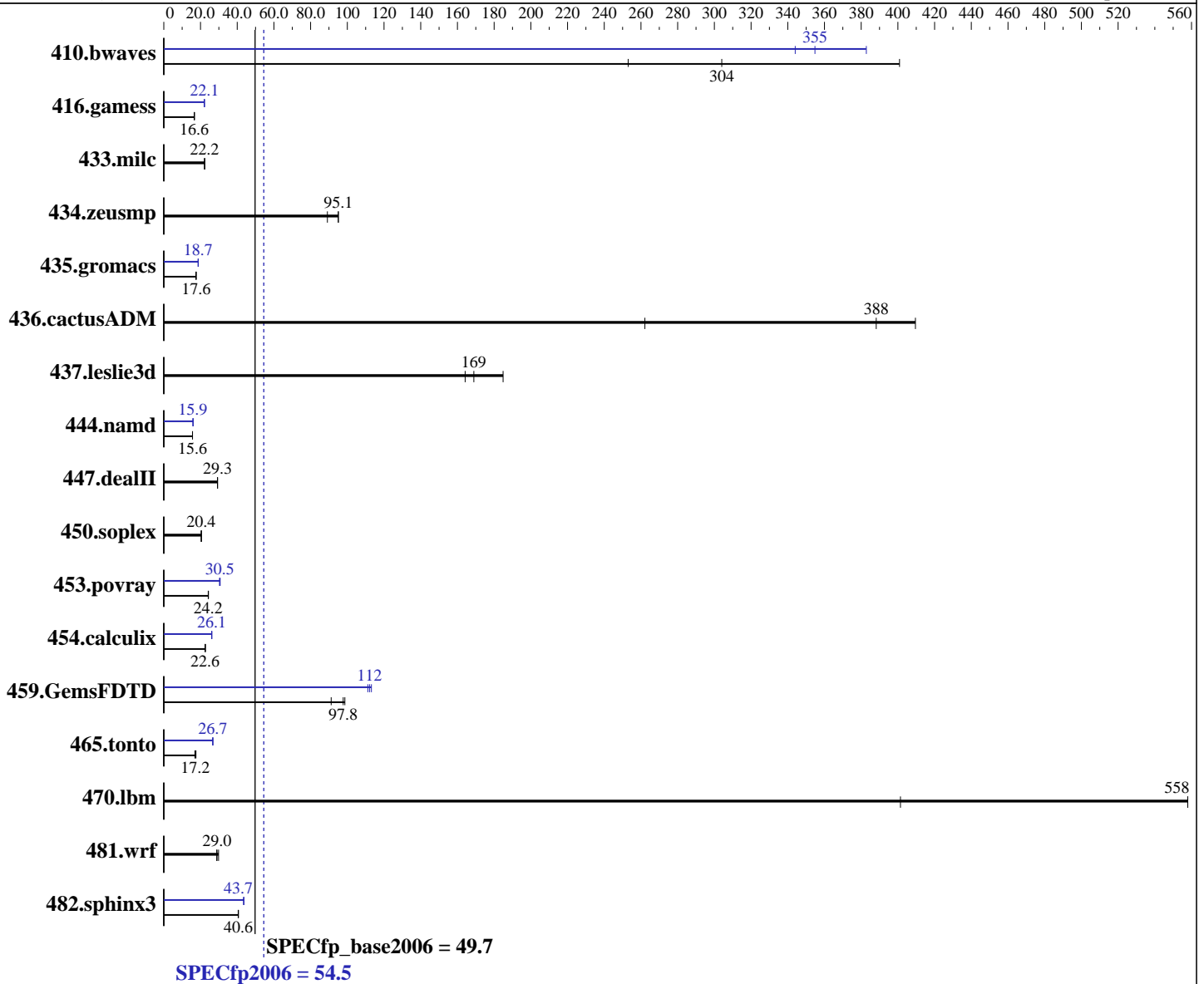
Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: Apr-2011

Hardware Availability: Jul-2011

Software Availability: Apr-2011



### Hardware

CPU Name: Intel Xeon X7560  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz  
 CPU MHz: 2266  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

SPECfp2006 = **54.5**

NovaScale R480 F2 (Intel Xeon X7560, 2.27 GHz)

SPECfp\_base2006 = **49.7**

CPU2006 license: 20

Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: Apr-2011

Hardware Availability: Jul-2011

Software Availability: Apr-2011

L3 Cache: 24 MB I+D on chip per chip  
Other Cache: None  
Memory: 512 GB (64 x 8 GB 4Rx8 PC3-8500R-7, ECC)  
Disk Subsystem: 1 x 500 GB 7200 RPM SAS 6Gb  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<b>44.7</b>	<b>304</b>	33.9	401	53.7	253	39.5	344	35.5	383	<b>38.3</b>	<b>355</b>
416.gamess	<b>1178</b>	<b>16.6</b>	1173	16.7	1181	16.6	884	22.2	<b>885</b>	<b>22.1</b>	889	22.0
433.milc	412	22.3	413	22.2	<b>413</b>	<b>22.2</b>	412	22.3	413	22.2	<b>413</b>	<b>22.2</b>
434.zeusmp	<b>95.7</b>	<b>95.1</b>	95.7	95.1	102	89.1	<b>95.7</b>	<b>95.1</b>	95.7	95.1	102	89.1
435.gromacs	404	17.7	408	17.5	<b>406</b>	<b>17.6</b>	380	18.8	<b>381</b>	<b>18.7</b>	382	18.7
436.cactusADM	29.2	410	<b>30.8</b>	<b>388</b>	45.6	262	29.2	410	<b>30.8</b>	<b>388</b>	45.6	262
437.leslie3d	50.8	185	57.2	164	<b>55.6</b>	<b>169</b>	50.8	185	57.2	164	<b>55.6</b>	<b>169</b>
444.namd	512	15.7	513	15.6	<b>513</b>	<b>15.6</b>	503	15.9	<b>503</b>	<b>15.9</b>	503	15.9
447.dealII	391	29.3	<b>391</b>	<b>29.3</b>	391	29.3	391	29.3	<b>391</b>	<b>29.3</b>	391	29.3
450.soplex	407	20.5	409	20.4	<b>408</b>	<b>20.4</b>	407	20.5	409	20.4	<b>408</b>	<b>20.4</b>
453.povray	220	24.2	219	24.3	<b>220</b>	<b>24.2</b>	173	30.7	<b>174</b>	<b>30.5</b>	175	30.4
454.calculix	364	22.7	<b>366</b>	<b>22.6</b>	366	22.5	316	26.1	316	26.1	<b>316</b>	<b>26.1</b>
459.GemsFDTD	<b>109</b>	<b>97.8</b>	108	98.7	116	91.2	<b>94.6</b>	<b>112</b>	95.4	111	93.8	113
465.tonto	<b>572</b>	<b>17.2</b>	562	17.5	580	17.0	369	26.7	<b>369</b>	<b>26.7</b>	368	26.7
470.lbm	<b>24.6</b>	<b>558</b>	34.2	401	24.6	558	<b>24.6</b>	<b>558</b>	34.2	401	24.6	558
481.wrf	<b>385</b>	<b>29.0</b>	387	28.9	374	29.8	<b>385</b>	<b>29.0</b>	387	28.9	374	29.8
482.sphinx3	479	40.7	<b>480</b>	<b>40.6</b>	481	40.5	449	43.4	<b>446</b>	<b>43.7</b>	446	43.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

BIOS Settings:  
Power Management = Maximum Performance (Default = Active Power Controller)  
Node Interleaving = Enabled (Default = Disabled)



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

SPECfp2006 = 54.5

NovaScale R480 F2 (Intel Xeon X7560, 2.27 GHz)

SPECfp\_base2006 = 49.7

CPU2006 license: 20

Test date: Apr-2011

Test sponsor: Bull SAS

Hardware Availability: Jul-2011

Tested by: Dell Inc.

Software Availability: Apr-2011

## General Notes

The Dell PowerEdge R910 and the Bull NovaScale R480 F2 models are electronically equivalent. The results have been measured on a Dell PowerEdge R910 model.  
OMP\_NUM\_THREADS set to number of cores  
Binaries were compiled on RHEL5.5

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

**SPECfp2006 = 54.5**

NovaScale R480 F2 (Intel Xeon X7560, 2.27 GHz)

**SPECfp\_base2006 = 49.7**

**CPU2006 license:** 20

**Test sponsor:** Bull SAS

**Tested by:** Dell Inc.

**Test date:** Apr-2011

**Hardware Availability:** Jul-2011

**Software Availability:** Apr-2011

## Base Optimization Flags (Continued)

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

`433.milc: basepeak = yes`

`470.lbm: basepeak = yes`

`482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel`

C++ benchmarks:

`444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32`

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Bull SAS**

**SPECfp2006 = 54.5**

NovaScale R480 F2 (Intel Xeon X7560, 2.27 GHz)

**SPECfp\_base2006 = 49.7**

**CPU2006 license:** 20  
**Test sponsor:** Bull SAS  
**Tested by:** Dell Inc.

**Test date:** Apr-2011  
**Hardware Availability:** Jul-2011  
**Software Availability:** Apr-2011

## Peak Optimization Flags (Continued)

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

### Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>  
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>  
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml>



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

SPECfp2006 = 54.5

NovaScale R480 F2 (Intel Xeon X7560, 2.27 GHz)

SPECfp\_base2006 = 49.7

**CPU2006 license:** 20

**Test sponsor:** Bull SAS

**Tested by:** Dell Inc.

**Test date:** Apr-2011

**Hardware Availability:** Jul-2011

**Software Availability:** Apr-2011

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 20:12:02 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 24 May 2011.