



# SPEC® CINT2006 Result

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## Acer Incorporated

### SPECint®\_rate2006 = 586

### Gateway GR585 F1 (AMD Opteron 6136)

### SPECint\_rate\_base2006 = 504

CPU2006 license: 97

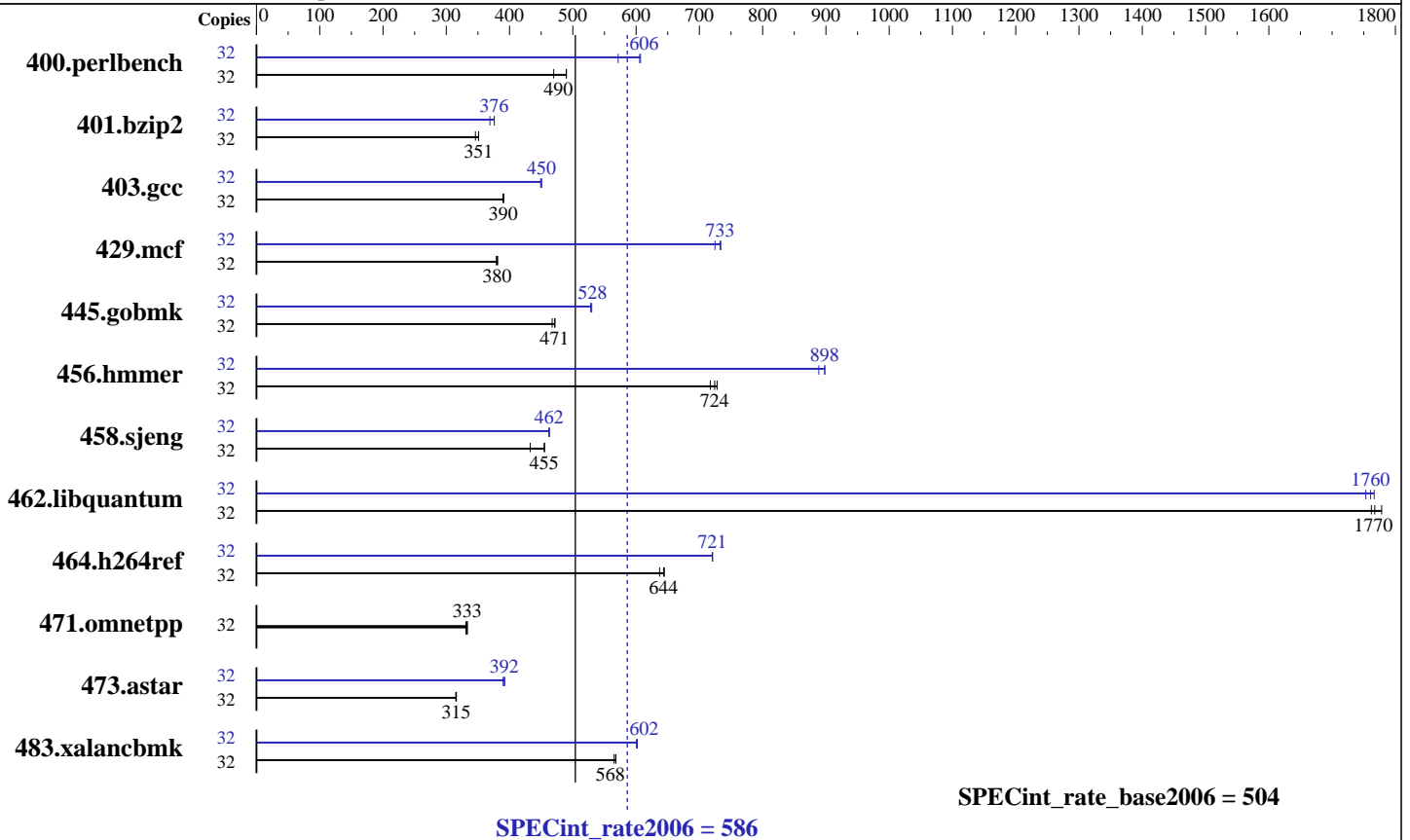
Test date: Mar-2011

Test sponsor: Acer Incorporated

Hardware Availability: Jul-2010

Tested by: Acer Incorporated

Software Availability: Jul-2010



#### Hardware

CPU Name: AMD Opteron 6136  
 CPU Characteristics:  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 64 KB I + 64 KB D on chip per core  
 Secondary Cache: 512 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip, 6 MB shared / 4 cores  
 Other Cache: None  
 Memory: 128 GB (32 x 4 GB 2Rx8 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

#### Software

Operating System: SUSE Linux Enterprise Server 11 SP1  
 Kernel 2.6.32.12-0.7-default  
 x86 Open64 4.2.4 Compiler Suite (from AMD)  
 Compiler: No  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multiuser)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap 8.1 32-bit Library for Linux



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### Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbenc	32	665	470	638	490	<b>638</b>	<b>490</b>	32	547	572	515	607	<b>516</b>	<b>606</b>
401.bzip2	32	892	346	880	351	<b>880</b>	<b>351</b>	32	837	369	<b>822</b>	<b>376</b>	821	376
403.gcc	32	<b>660</b>	<b>390</b>	661	389	659	391	32	574	449	572	451	<b>572</b>	<b>450</b>
429.mcf	32	<b>768</b>	<b>380</b>	766	381	770	379	32	398	734	<b>398</b>	<b>733</b>	403	725
445.gobmk	32	<b>712</b>	<b>471</b>	718	467	712	472	32	635	528	<b>635</b>	<b>528</b>	633	530
456.hammer	32	410	728	416	717	<b>412</b>	<b>724</b>	32	<b>332</b>	<b>898</b>	332	898	336	889
458.sjeng	32	850	455	895	433	<b>852</b>	<b>455</b>	32	837	462	<b>837</b>	<b>462</b>	836	463
462.libquantum	32	373	1780	<b>375</b>	<b>1770</b>	376	1760	32	375	1770	<b>377</b>	<b>1760</b>	378	1750
464.h264ref	32	1099	645	<b>1100</b>	<b>644</b>	1111	637	32	<b>983</b>	<b>721</b>	982	721	983	720
471.omnetpp	32	<b>601</b>	<b>333</b>	600	333	604	331	32	<b>601</b>	<b>333</b>	600	333	604	331
473.astar	32	711	316	<b>712</b>	<b>315</b>	713	315	32	576	390	573	392	<b>574</b>	<b>392</b>
483.xalancbmk	32	391	565	<b>389</b>	<b>568</b>	389	568	32	367	602	368	601	<b>367</b>	<b>602</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

### Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set vm/nr\_hugepages=28672 in /etc/sysctl.conf  
mount -t hugetlbfs nodev /mnt/hugepages

### General Notes

Environment variables set by runspec before the start of the run:  
HUGETLB\_LIMIT = "896"  
LD\_LIBRARY\_PATH = "/usr/cpu2006/amd1002-rate-libs-revC/64:/usr/cpu2006/amd1002-rate-libs-revC/32"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at <http://developer.amd.com/cpu/open64>  
Binaries were compiled on SLES10 SP2 with binutils 2.18  
This result was measured on the Gateway GR585 F1.  
The Acer AR585 F1 and Gateway GR585 F1 are electronically equivalent.



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## Base Compiler Invocation

C benchmarks:  
opencc

C++ benchmarks:  
openCC

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:  
-march=barcelona -mso -Ofast -CG:local\_sched\_alg=1  
-INLINE:aggressive=on -IPA:plimit=8000 -IPA:small\_pu=100  
-HP:bdt=2m:heap=2m

C++ benchmarks:  
-march=barcelona -mso -Ofast -m32 -INLINE:aggressive=on  
-CG:cmp\_peep=on -L/root/work/libraries/SmartHeap-8.1/lib -lsmarheap

## Peak Compiler Invocation

C benchmarks:  
opencc

C++ benchmarks:  
openCC

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
```

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## Peak Portability Flags (Continued)

```
401.bzip2: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

## Peak Optimization Flags

C benchmarks:

```
400.perlbench: -march=barcelona -mso -fb_create fbdata(pass 1)
               -fb_opt fbdata(pass 2) -Ofast -IPA:plimit=20000 -LNO:opt=0
               -OPT:unroll_times_max=8 -OPT:unroll_size=256
               -OPT:unroll_level=2 -OPT:keep_ext=on -WOPT:if_conv=0
               -CG:local_sched_alg=1 -CG:unroll_fb_req=on
               -HP:bdt=2m:heap=2m

401.bzip2: -march=barcelona -mso -fb_create fbdata(pass 1)
            -fb_opt fbdata(pass 2) -O3 -OPT:alias=disjoint
            -OPT:goto=off -CG:local_sched_alg=1 -HP:bdt=2m:heap=2m

403.gcc: -march=barcelona -mso -fb_create fbdata(pass 1)
          -fb_opt fbdata(pass 2) -Ofast -LNO:trip_count=256
          -LNO:prefetch Ahead=10 -CG:cmp_peep=on -m32
          -HP:bdt=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200

429.mcf: -march=barcelona -mso -O3 -ipa -INLINE:aggressive=on
          -CG:gcm=off -GRA:prioritize_by_density=on -m32
          -HP:bdt=2m:heap=2m

445.gobmk: -march=barcelona -mso -fb_create fbdata(pass 1)
            -fb_opt fbdata(pass 2) -O3 -OPT:alias=restrict
            -OPT:unroll_times_max=8 -OPT:unroll_size=256
            -OPT:unroll_level=2 -OPT:keep_ext=on -ipa -IPA:plimit=750
            -IPA:min_hotness=300 -IPA:pu_reorder=1 -LNO:prefetch=1
            -LNO:ignore_feedback=off -CG:p2align=on
            -CG:unroll_fb_req=on -HP:bdt=2m:heap=2m

456.hmmer: -march=barcelona -mso -fb_create fbdata(pass 1)
            -fb_opt fbdata(pass 2) -Ofast -LNO:prefetch=0
            -OPT:alias=disjoint -OPT:unroll_times_max=8
            -OPT:unroll_size=256 -OPT:unroll_level=2 -OPT:keep_ext=on
            -CG:local_sched_alg=1 -CG:cflow=0
            -CG:push_pop_int_saved_regs=off -CG:cmp_peep=on
            -HP:bdt=2m:heap=2m
```

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## Peak Optimization Flags (Continued)

```
458.sjeng: -march=barcelona -mso -fb_create fbdata(pass 1)
          -fb_opt fbdata(pass 2) -O3 -ipa -LNO:ignore_feedback=off
          -LNO:full_unroll=10 -LNO:fusion=0 -LNO:fission=2
          -IPA:pu_reorder=2 -CG:ptr_load_use=0
          -OPT:unroll_times_max=8 -INLINE:aggressive=on
```

```
462.libquantum: -march=barcelona -mso -Ofast -LNO:pf2=0 -CG:gcm=off
                -CG:use_prefetchnta=on -CG:cmp_peep=on -WOPT:aggstr=0
                -HP:bdt=2m:heap=2m -OPT:alias=disjoint
                -INLINE:aggressive=on -IPA:space=1000 -IPA:plimit=20000
```

```
464.h264ref: -march=barcelona -mso -fb_create fbdata(pass 1)
             -fb_opt fbdata(pass 2) -O3 -IPA:plimit=20000
             -OPT:alias=disjoint -LNO:prefetch=0 -CG:ptr_load_use=0
             -CG:push_pop_int_saved_regs=off
```

C++ benchmarks:

```
471.omnetpp: basepeak = yes
```

```
473.astar: -march=barcelona -mso -fb_create fbdata(pass 1)
           -fb_opt fbdata(pass 2) -Ofast -TENV:frame_pointer=off
           -WOPT:if_conv=0 -GRA:optimize_boundary=on
           -OPT:alias=disjoint -INLINE:aggressive=on
           -IPA:small_pu=3000 -IPA:plimit=3000 -m32
           -HP:bdt=2m:heap=2m
```

```
483.xalancbmk: -march=barcelona -mso -Ofast -INLINE:aggressive=on -m32
              -CG:cmp_peep=on -GRA:unspill=on -TENV:frame_pointer=off
              -fno-emit-exceptions
              -L/root/work/libraries/SmartHeap-8.1/lib -lsmarheap
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.html>

<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.xml>

<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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