SPEC® CINT2006 Result
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Fujitsu

PRIMERGY RX900 S1, Intel Xeon X7550, 2.00 GHz

SPECint®_rate2006 =   1240

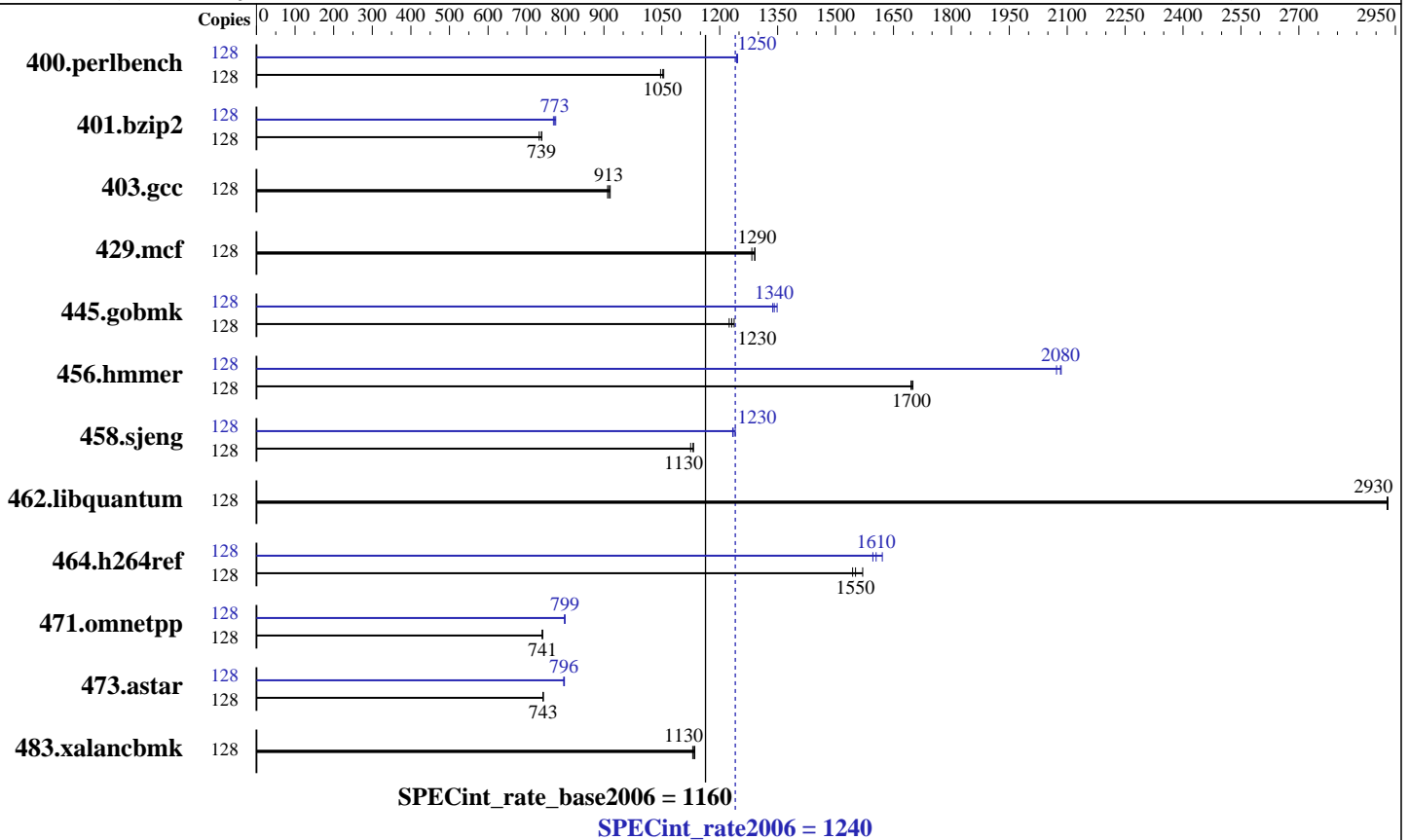SPECint_rate_base2006 = 1160

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Aug-2010
Hardware Availability: Aug-2010
Software Availability: Feb-2010

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Hardware

CPU Name: Intel Xeon X7550
CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 64 cores, 8 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable: 4,6,8 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 18 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (128 x 8 GB DDR3-1066 DIMMs)
Disk Subsystem: 2 x 147 GB (SAS, 15000 RPM, RAID0)
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
Build 20100203 Package ID: l_cproc_p_11.1.069
Auto Parallel: No
File System: ext2
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V9.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	128	1195	1050	**1188**	**1050**	1185	1060	128	1003	1250	1005	1240	**1004**	**1250**
401.bzip2	128	1687	732	**1672**	**739**	1672	739	128	1606	769	1593	775	**1599**	**773**
403.gcc	128	1133	910	1124	916	**1129**	**913**	128	1133	910	1124	916	**1129**	**913**
429.mcf	128	910	1280	**904**	**1290**	904	1290	128	910	1280	**904**	**1290**	904	1290
445.gobmk	128	1097	1220	**1091**	**1230**	1085	1240	128	996	1350	1004	1340	**1001**	**1340**
456.hmmer	128	704	1700	702	1700	**704**	**1700**	128	573	2080	**573**	**2080**	576	2070
458.sjeng	128	1377	1120	**1370**	**1130**	1368	1130	128	1249	1240	1256	1230	**1254**	**1230**
462.libquantum	128	**905**	**2930**	906	2930	905	2930	128	**905**	**2930**	906	2930	905	2930
464.h264ref	128	1804	1570	**1825**	**1550**	1834	1540	128	1747	1620	**1765**	**1610**	1774	1600
471.omnetpp	128	1079	741	1081	740	**1080**	**741**	128	**1002**	**799**	1001	799	1002	798
473.astar	128	1210	743	1210	742	**1210**	**743**	128	1126	798	**1128**	**796**	1129	796
483.xalancbmk	128	**779**	**1130**	778	1140	782	1130	128	**779**	**1130**	778	1140	782	1130

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

    The following command was used prior to run

        ulimit -s unlimited
        echo 1 > /proc/sys/vm/zone_reclaim_mode

Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_IA32
        462.libquantum: -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs`
`-L/opt/SmartHeap_9/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc  -m32`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks (except as noted below):
`icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`

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Peak Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
`-prof-use`(pass 2) `-ansi-alias`

401.bzip2: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
`-prof-use`(pass 2) `-opt-prefetch` `-ansi-alias` `-auto-ilp32`

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-prof-use`(pass 2) `-O2`
`-ipo` `-no-prec-div` `-ansi-alias`

456.hmmer: `-xSSE4.2` `-ipo` `-O3` `-no-prec-div` `-static` `-unroll2`
`-ansi-alias` `-auto-ilp32`

458.sjeng: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
`-prof-use`(pass 2) `-unroll4` `-auto-ilp32`

462.libquantum: basepeak = yes

464.h264ref: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2)
`-prof-use`(pass 2) `-unroll2` `-ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-ansi-alias` `-opt-ra-region-strategy=block` `-Wl,-z,muldefs`
`-L/opt/SmartHeap_9/lib` `-lsmartheap`

473.astar: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-ansi-alias` `-opt-ra-region-strategy=routine` `-Wl,-z,muldefs`
`-L/opt/SmartHeap_9_64/lib` `-lsmartheap64`

483.xalancbmk: basepeak = yes

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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Fujitsu.RX900.ic11.1-linux64.20100901.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Fujitsu.RX900.ic11.1-linux64.20100901.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 12:07:38 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 31 August 2010.