



# SPEC® CFP2006 Result

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## IBM Corporation

SPECfp®2006 = 38.7

## IBM System x3500 M3 (Intel Xeon L5640)

SPECfp\_base2006 = 35.7

CPU2006 license: 11

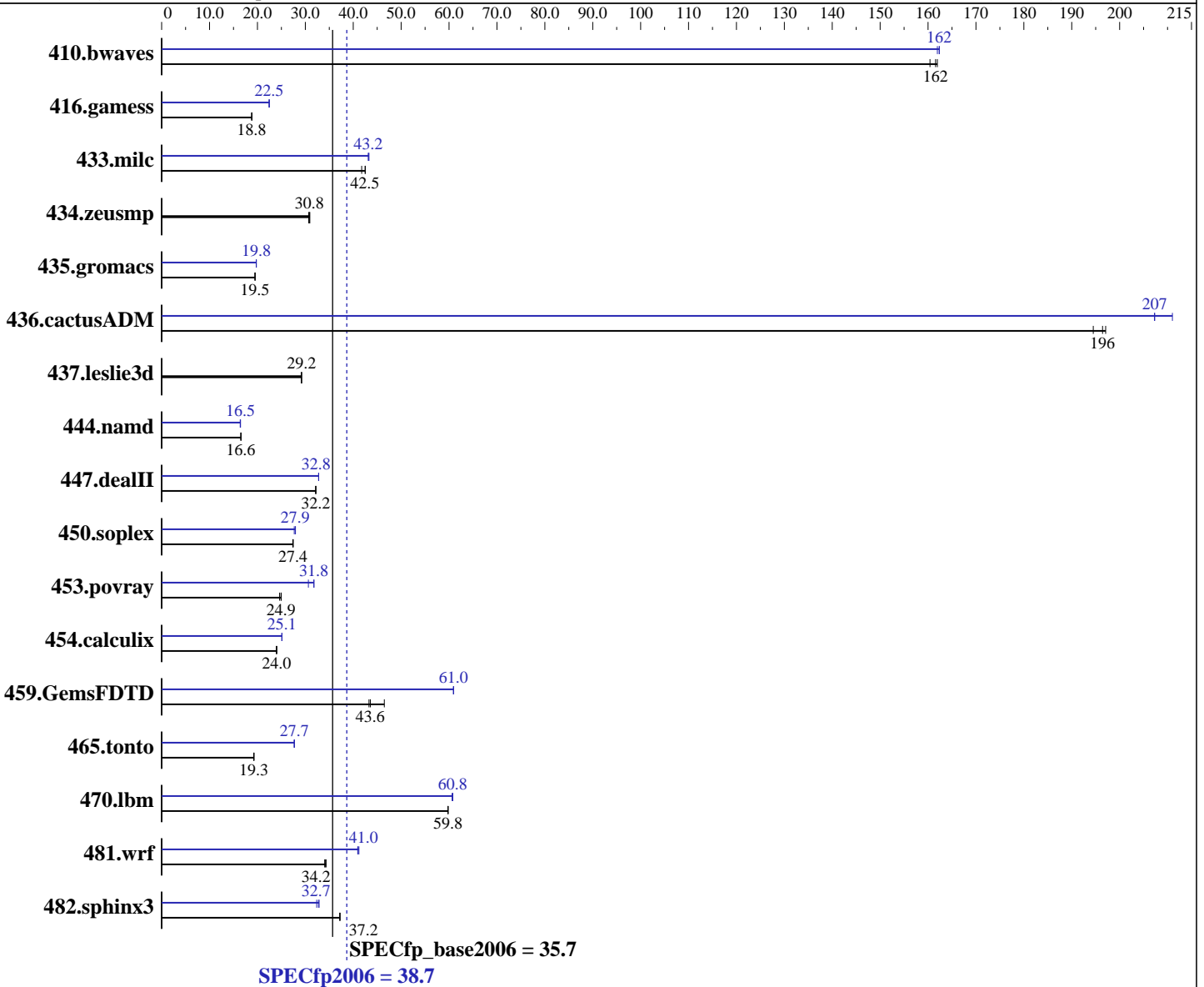
Test date: Jul-2010

Test sponsor: IBM Corporation

Hardware Availability: Jun-2010

Tested by: IBM Corporation

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon L5640  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2267  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SuSe Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064, l\_cprof\_p\_11.1.064  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)

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L3 Cache: 12 MB I+D on chip per chip  
Other Cache: None  
Memory: 48 GB (12 x 4 GB PC3-10600R CL9, 2 Rank)  
Disk Subsystem: 1 x 73 GB SAS, 15000 RPM  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	84.7	160	83.9	162	<b>84.1</b>	<b>162</b>	<b>83.7</b>	<b>162</b>	83.9	162	83.7	162
416.gamess	1038	18.9	1042	18.8	<b>1041</b>	<b>18.8</b>	871	22.5	<b>872</b>	<b>22.5</b>	873	22.4
433.milc	220	41.8	<b>216</b>	<b>42.5</b>	216	42.5	212	43.3	<b>213</b>	<b>43.2</b>	213	43.2
434.zeusmp	294	30.9	296	30.7	<b>295</b>	<b>30.8</b>	294	30.9	296	30.7	<b>295</b>	<b>30.8</b>
435.gromacs	<b>366</b>	<b>19.5</b>	366	19.5	367	19.5	362	19.7	361	19.8	<b>361</b>	<b>19.8</b>
436.cactusADM	61.4	194	<b>60.8</b>	<b>196</b>	60.6	197	56.6	211	57.7	207	<b>57.6</b>	<b>207</b>
437.leslie3d	<b>322</b>	<b>29.2</b>	322	29.2	322	29.2	<b>322</b>	<b>29.2</b>	322	29.2	322	29.2
444.namd	484	16.6	485	16.5	<b>484</b>	<b>16.6</b>	<b>488</b>	<b>16.5</b>	489	16.4	487	16.5
447.dealII	356	32.2	<b>355</b>	<b>32.2</b>	355	32.2	349	32.8	<b>349</b>	<b>32.8</b>	349	32.8
450.soplex	305	27.4	304	27.5	<b>304</b>	<b>27.4</b>	301	27.7	299	27.9	<b>299</b>	<b>27.9</b>
453.povray	<b>214</b>	<b>24.9</b>	213	25.0	216	24.6	<b>167</b>	<b>31.8</b>	174	30.6	167	31.8
454.calculix	343	24.0	345	23.9	<b>344</b>	<b>24.0</b>	<b>329</b>	<b>25.1</b>	328	25.1	329	25.1
459.GemsFDTD	245	43.3	<b>244</b>	<b>43.6</b>	228	46.5	174	61.0	174	60.9	<b>174</b>	<b>61.0</b>
465.tonto	512	19.2	510	19.3	<b>511</b>	<b>19.3</b>	355	27.7	<b>355</b>	<b>27.7</b>	356	27.7
470.lbm	230	59.8	230	59.8	<b>230</b>	<b>59.8</b>	<b>226</b>	<b>60.8</b>	226	60.8	227	60.6
481.wrf	<b>327</b>	<b>34.2</b>	328	34.0	325	34.3	271	41.2	273	40.9	<b>272</b>	<b>41.0</b>
482.sphinx3	523	37.2	<b>524</b>	<b>37.2</b>	524	37.2	593	32.9	602	32.4	<b>595</b>	<b>32.7</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

Turbo Mode Enable  
Turbo Boost set to Traditional  
CPU C State Enable  
Data Reuse Disable

## General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502  
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter  
KMP\_STACKSIZE set to 200M



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## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



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## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-parallel -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32  
-unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias -scalar-rep- -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -auto-ilp32

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## Peak Optimization Flags (Continued)

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-inline-alloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100804.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100804.xml>



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