



SPEC® CFP2006 Result

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Supermicro

Motherboard X8DTT-IBQF (Intel Xeon E5630, 2.53 GHz)

SPECfp®_rate2006 = 174

SPECfp_rate_base2006 = 168

CPU2006 license: 001176

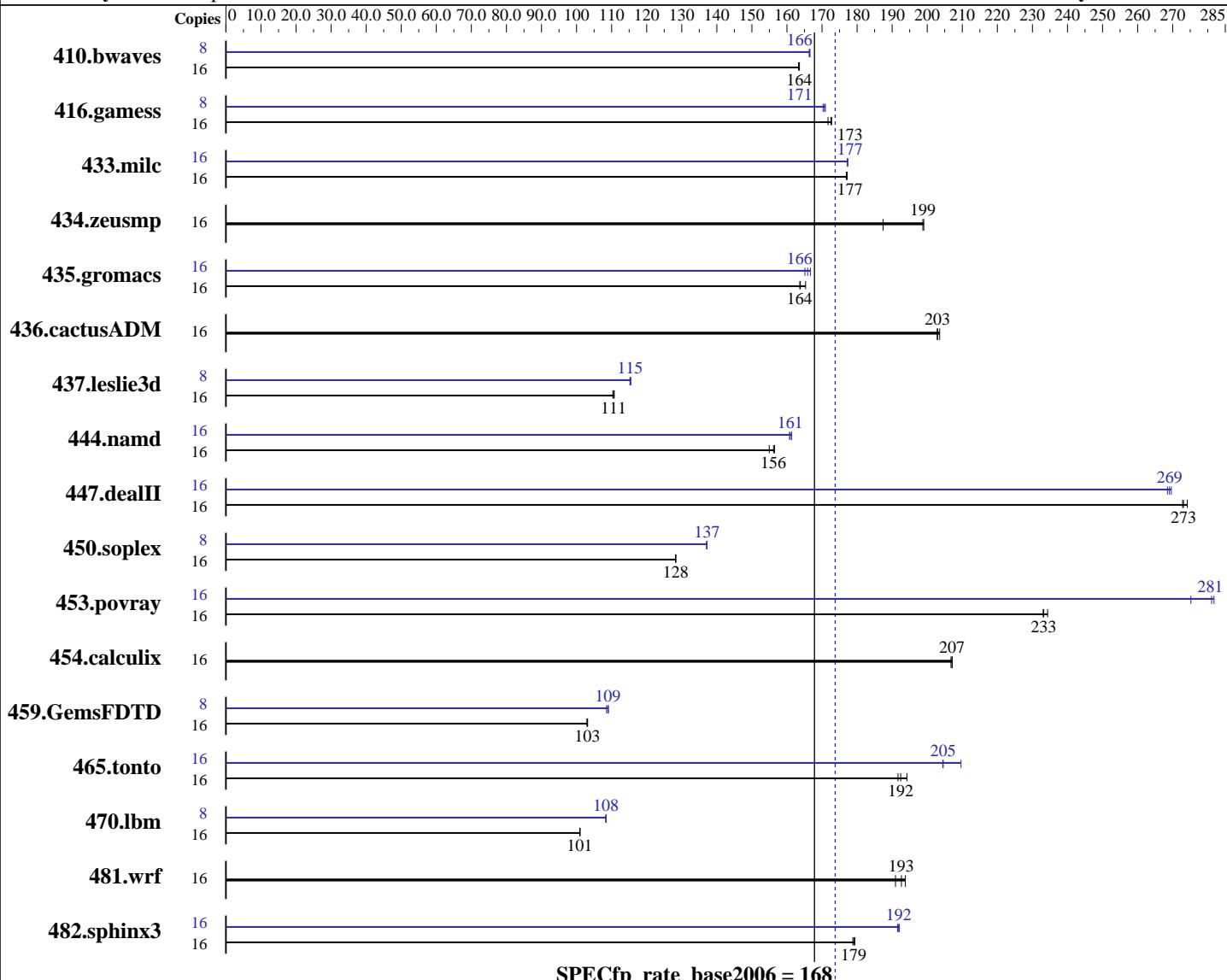
Test date: Jun-2010

Test sponsor: Supermicro

Hardware Availability: Mar-2010

Tested by: Supermicro

Software Availability: Jan-2010



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Hardware

CPU Name: Intel Xeon E5630
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz: 2533
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
Compiler: Kernel 2.6.27.19-5-default
Auto Parallel: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1
File System: Build 20091130 Package ID: l_cproc_p_11.1.064,
System State: l_cprof_p_11.1.064
ext3
Run level 3 (multi-user)

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L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB DDR3-1066 RDIMM, ECC, CL7)
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1329	164	<u>1330</u>	<u>164</u>	1332	163	8	653	167	<u>653</u>	<u>166</u>	654	166
416.gamess	16	1825	172	<u>1816</u>	<u>173</u>	1814	173	8	<u>918</u>	<u>171</u>	920	170	916	171
433.milc	16	829	177	<u>829</u>	<u>177</u>	830	177	16	829	177	<u>828</u>	<u>177</u>	828	177
434.zeusmp	16	<u>733</u>	<u>199</u>	777	187	732	199	16	<u>733</u>	<u>199</u>	777	187	732	199
435.gromacs	16	698	164	691	165	<u>697</u>	<u>164</u>	16	<u>689</u>	<u>166</u>	692	165	685	167
436.cactusADM	16	943	203	939	204	<u>942</u>	<u>203</u>	16	943	203	939	204	<u>942</u>	<u>203</u>
437.leslie3d	16	1358	111	<u>1361</u>	<u>111</u>	1363	110	8	651	115	653	115	<u>652</u>	<u>115</u>
444.namd	16	820	156	828	155	<u>822</u>	<u>156</u>	16	798	161	<u>797</u>	<u>161</u>	795	161
447.dealII	16	671	273	<u>670</u>	<u>273</u>	668	274	16	679	270	<u>680</u>	<u>269</u>	682	269
450.soplex	16	1040	128	1040	128	<u>1040</u>	<u>128</u>	8	486	137	<u>486</u>	<u>137</u>	487	137
453.povray	16	365	233	363	234	<u>365</u>	<u>233</u>	16	302	282	<u>303</u>	<u>281</u>	309	275
454.calculix	16	637	207	638	207	<u>637</u>	<u>207</u>	16	637	207	638	207	<u>637</u>	<u>207</u>
459.GemsFDTD	16	1646	103	1649	103	<u>1646</u>	<u>103</u>	8	782	109	<u>779</u>	<u>109</u>	778	109
465.tonto	16	<u>818</u>	<u>192</u>	821	192	811	194	16	751	210	<u>770</u>	<u>205</u>	770	204
470.lbm	16	2176	101	<u>2178</u>	<u>101</u>	2179	101	8	1015	108	1014	108	<u>1014</u>	<u>108</u>
481.wrf	16	922	194	936	191	<u>928</u>	<u>193</u>	16	922	194	936	191	<u>928</u>	<u>193</u>
482.sphinx3	16	1739	179	1744	179	<u>1742</u>	<u>179</u>	16	1624	192	1628	192	<u>1625</u>	<u>192</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Platform Notes

Fan speed set to Full Speed in BIOS Setup.
 As tested, the system used a Supermicro CSE-827H-R1400B chassis.
 The chassis is bundled with a PWS-1K41P-1R power supply, 2 SNK-P0037P heatsinks, and 4 FAN-00111L4 cooling fans.



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General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

433.milc: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -opt-prefetch

470.lbm: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -ansi-alias -auto-ilp32

482.sphinx3: -xsse4 .2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xsse4 .2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xsse4 .2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0

465.tonto: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: -xSSE4_2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100804.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100804.xml>

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