



SPEC® CFP2006 Result

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Supermicro

SPECfp®2006 = 29.7

Motherboard X8SIU-F (Intel Core i3-530, 2.93 GHz)

SPECfp_base2006 = 28.5

CPU2006 license: 001176

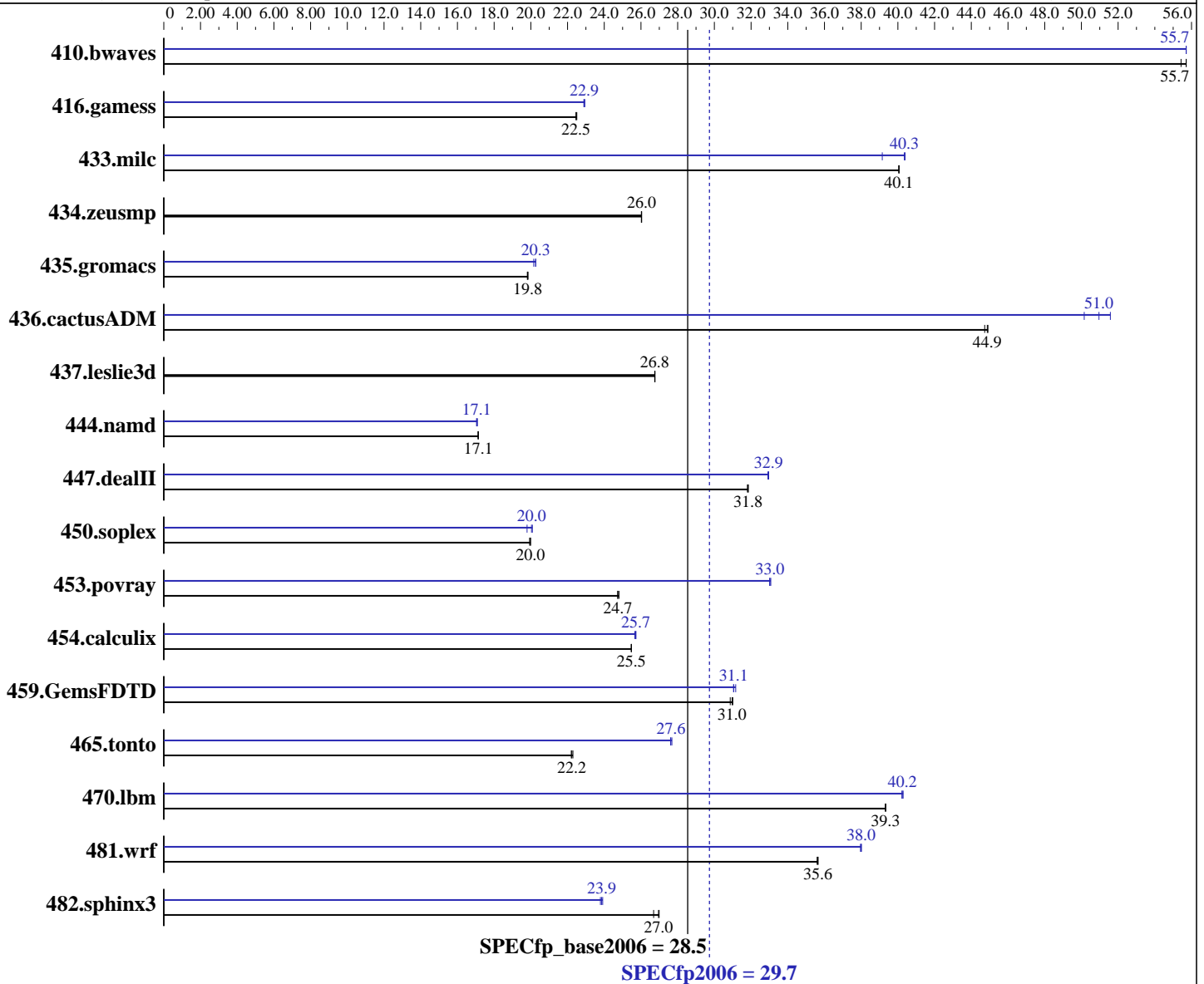
Test date: Jun-2010

Test sponsor: Supermicro

Hardware Availability: Apr-2010

Tested by: Supermicro

Software Availability: Jan-2010



Hardware		Software	
CPU Name:	Intel Core i3-530	Operating System:	SUSE Linux Enterprise Server 11 (x86_64)
CPU Characteristics:			Kernel 2.6.27.19-5-default
CPU MHz:	2933	Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1
FPU:	Integrated		Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip, 2 threads/core	Auto Parallel:	Yes
CPU(s) orderable:	1 chip	File System:	ext3
Primary Cache:	32 KB I + 32 KB D on chip per core	System State:	Run level 3 (multi-user)
Secondary Cache:	256 KB I+D on chip per core		
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L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4 GB 2Rx8 DDR3-1333 UDIMM, ECC, CL9)
 Disk Subsystem: 1 x 160 GB SATA II, 7200 RPM
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	245	55.4	244	55.7	244	55.7	244	55.7	244	55.7	244	55.7
416.gamess	872	22.5	872	22.5	870	22.5	854	22.9	854	22.9	855	22.9
433.milc	229	40.1	229	40.1	229	40.1	227	40.4	234	39.1	228	40.3
434.zeusmp	350	26.0	350	26.0	350	26.0	350	26.0	350	26.0	350	26.0
435.gromacs	360	19.8	360	19.8	360	19.8	352	20.3	352	20.3	354	20.2
436.cactusADM	266	44.9	266	44.9	267	44.7	232	51.6	235	51.0	238	50.2
437.leslie3d	351	26.8	351	26.8	351	26.8	351	26.8	351	26.8	351	26.8
444.namd	468	17.1	468	17.1	468	17.1	469	17.1	470	17.1	471	17.0
447.dealII	359	31.8	360	31.8	359	31.8	347	33.0	348	32.9	347	32.9
450.soplex	417	20.0	417	20.0	418	19.9	415	20.1	416	20.0	421	19.8
453.povray	215	24.7	215	24.7	214	24.8	161	33.1	161	33.0	161	33.0
454.calculix	324	25.5	324	25.5	324	25.5	321	25.7	321	25.7	321	25.7
459.GemsFDTD	344	30.9	343	31.0	342	31.0	342	31.1	340	31.2	342	31.1
465.tonto	441	22.3	443	22.2	443	22.2	355	27.7	356	27.6	356	27.6
470.lbm	349	39.3	349	39.3	349	39.3	341	40.3	341	40.2	342	40.2
481.wrf	313	35.6	314	35.6	313	35.6	294	38.0	294	38.0	294	38.0
482.sphinx3	723	27.0	722	27.0	730	26.7	817	23.9	819	23.8	815	23.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to granularity=fine,scatter
 KMP_STACKSIZE set to 200M

Platform Notes

Fan speed set to Full Speed in BIOS Setup.
 As tested, the system used a Supermicro CSE-815TQ-330UB chassis.
 The chassis is bundled with a PWS-333-1H20 power supply, a SNK-P0046P heatsink,
 and 4 FAN-0086L4 cooling fans.



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General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-ansi-alias`

470.lbm: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-parallel -ansi-alias -auto-ilp32`

482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-unroll2`

C++ benchmarks:

444.namd: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32`

447.dealIII: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -auto-ilp32`

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Peak Optimization Flags (Continued)

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100915.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100915.xml>

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