



SPEC® CFP2006 Result

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Fujitsu

SPECfp®_rate2006 = 90.1

PRIMERGY BX924 S2, Intel Xeon E5630, 2.53 GHz

SPECfp_rate_base2006 = 86.9

CPU2006 license: 19

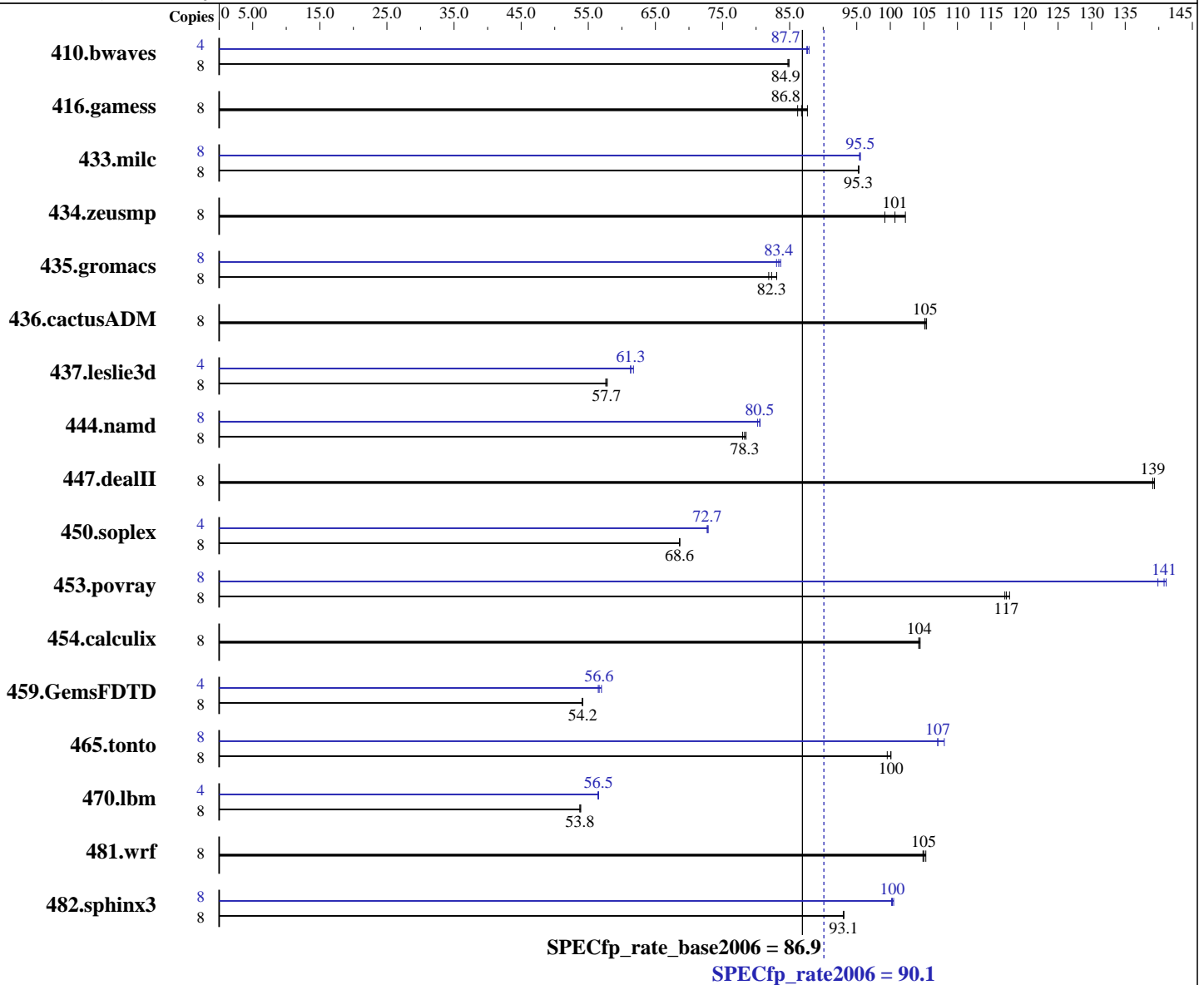
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2010

Hardware Availability: Jun-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon E5630
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2533
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Multi-User Run Level 3

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L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
 Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	1281	84.9	<u>1281</u>	<u>84.9</u>	1283	84.7	4	621	87.6	619	87.9	<u>620</u>	<u>87.7</u>
416.gamess	8	<u>1805</u>	<u>86.8</u>	1787	87.7	1817	86.2	8	<u>1805</u>	<u>86.8</u>	1787	87.7	1817	86.2
433.milc	8	770	95.3	<u>771</u>	<u>95.3</u>	771	95.2	8	770	95.4	<u>769</u>	<u>95.5</u>	769	95.5
434.zeusmp	8	<u>723</u>	<u>101</u>	734	99.2	712	102	8	<u>723</u>	<u>101</u>	734	99.2	712	102
435.gromacs	8	688	83.1	<u>694</u>	<u>82.3</u>	698	81.9	8	683	83.7	688	83.1	<u>685</u>	<u>83.4</u>
436.cactusADM	8	<u>909</u>	<u>105</u>	907	105	909	105	8	<u>909</u>	<u>105</u>	907	105	909	105
437.leslie3d	8	<u>1303</u>	<u>57.7</u>	1305	57.6	1300	57.8	4	<u>613</u>	<u>61.3</u>	609	61.7	613	61.3
444.namd	8	<u>820</u>	<u>78.3</u>	817	78.5	823	78.0	8	<u>797</u>	<u>80.5</u>	800	80.2	796	80.6
447.dealII	8	658	139	<u>658</u>	<u>139</u>	657	139	8	658	139	<u>658</u>	<u>139</u>	657	139
450.soplex	8	972	68.6	<u>973</u>	<u>68.6</u>	973	68.6	4	458	72.9	<u>459</u>	<u>72.7</u>	459	72.7
453.povray	8	364	117	<u>363</u>	<u>117</u>	361	118	8	<u>302</u>	<u>141</u>	304	140	302	141
454.calculix	8	<u>633</u>	<u>104</u>	633	104	632	104	8	<u>633</u>	<u>104</u>	633	104	632	104
459.GemsFDTD	8	1570	54.1	1567	54.2	<u>1567</u>	<u>54.2</u>	4	<u>750</u>	<u>56.6</u>	745	57.0	751	56.5
465.tonto	8	<u>787</u>	<u>100</u>	787	100	791	99.5	8	<u>735</u>	<u>107</u>	729	108	735	107
470.lbm	8	<u>2045</u>	<u>53.8</u>	2045	53.8	2040	53.9	4	974	56.4	<u>972</u>	<u>56.5</u>	972	56.5
481.wrf	8	<u>851</u>	<u>105</u>	852	105	849	105	8	<u>851</u>	<u>105</u>	852	105	849	105
482.sphinx3	8	<u>1675</u>	<u>93.1</u>	1677	93.0	1675	93.1	8	<u>1554</u>	<u>100</u>	1551	100	1556	100

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

The system automatically configures the memory to run at 1067 MHz.
BIOS configuration:
Data Reuse Optimization = Disable
Performance/Power Setting = Traditional



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General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

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Base Optimization Flags (Continued)

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

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Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml>

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