



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

SPECint®2006 = 21.2

BL265  
(Intel Xeon E5504, 2.00 GHz)

SPECint\_base2006 = 19.1

CPU2006 license: 20

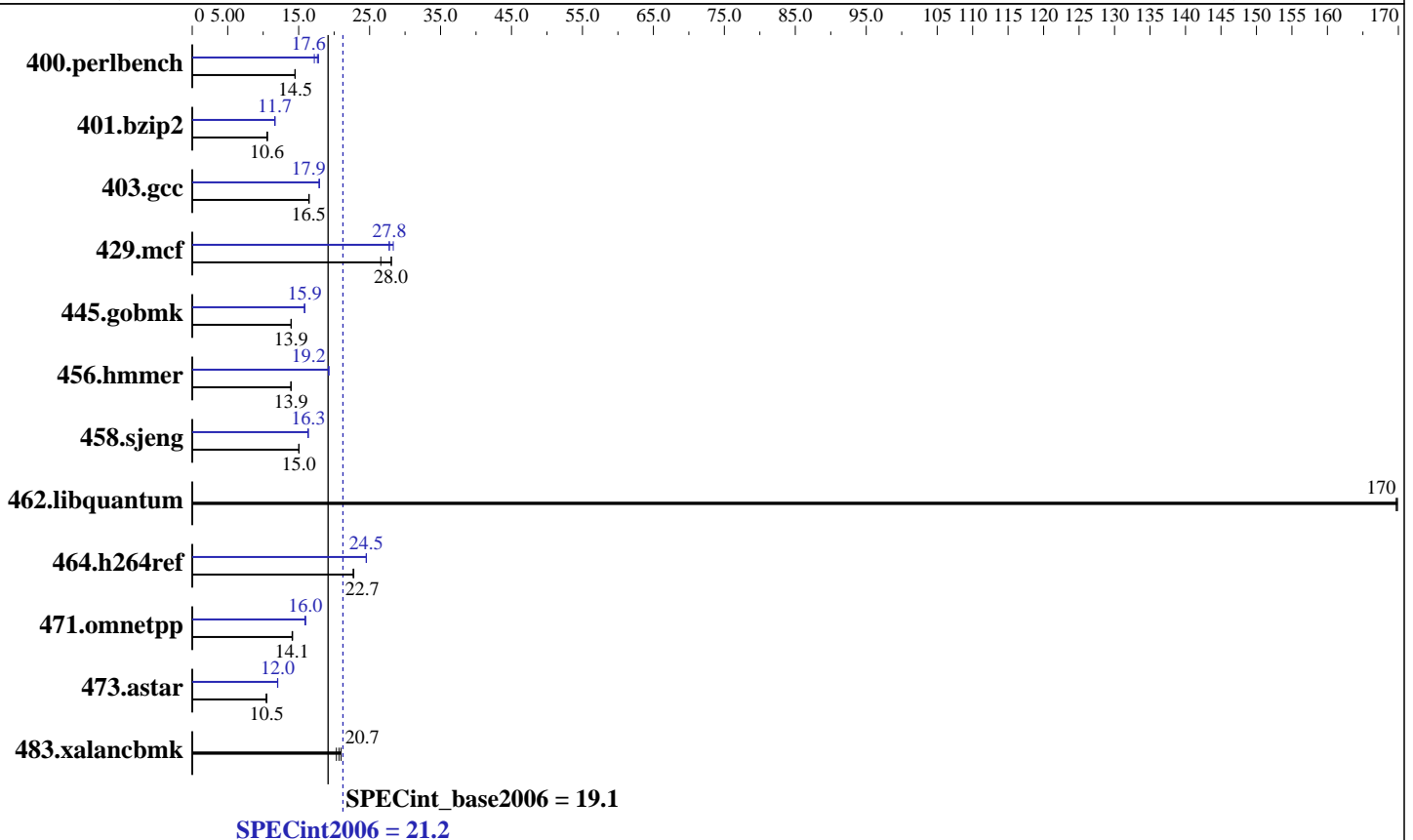
Test date: Jan-2010

Test sponsor: Bull SAS

Hardware Availability: May-2009

Tested by: Bull SAS

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon E5504  
 CPU Characteristics:  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4 GB PC3-10600R, 2 Rank, running at 800 MHz)  
 Disk Subsystem: 1 x 73 GB SAS, 10000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

BL265  
(Intel Xeon E5504, 2.00 GHz)

SPECint2006 = 21.2

SPECint\_base2006 = 19.1

CPU2006 license: 20  
Test sponsor: Bull SAS  
Tested by: Bull SAS

Test date: Jan-2010  
Hardware Availability: May-2009  
Software Availability: Feb-2009

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>673</b>	<b>14.5</b>	673	14.5	675	14.5	<b>554</b>	<b>17.6</b>	549	17.8	568	17.2
401.bzip2	916	10.5	<b>914</b>	<b>10.6</b>	909	10.6	828	11.7	830	11.6	<b>828</b>	<b>11.7</b>
403.gcc	488	16.5	<b>489</b>	<b>16.5</b>	490	16.4	<b>450</b>	<b>17.9</b>	450	17.9	449	17.9
429.mcf	343	26.6	325	28.1	<b>325</b>	<b>28.0</b>	322	28.3	328	27.8	<b>328</b>	<b>27.8</b>
445.gobmk	752	13.9	<b>752</b>	<b>13.9</b>	753	13.9	661	15.9	<b>661</b>	<b>15.9</b>	664	15.8
456.hammer	671	13.9	670	13.9	<b>670</b>	<b>13.9</b>	485	19.2	485	19.2	<b>485</b>	<b>19.2</b>
458.sjeng	804	15.0	<b>805</b>	<b>15.0</b>	805	15.0	<b>740</b>	<b>16.3</b>	741	16.3	740	16.3
462.libquantum	122	170	<b>122</b>	<b>170</b>	122	170	122	170	<b>122</b>	<b>170</b>	122	170
464.h264ref	972	22.8	<b>975</b>	<b>22.7</b>	975	22.7	<b>902</b>	<b>24.5</b>	902	24.5	902	24.5
471.omnetpp	444	14.1	<b>443</b>	<b>14.1</b>	442	14.2	393	15.9	391	16.0	<b>392</b>	<b>16.0</b>
473.astar	674	10.4	668	10.5	<b>670</b>	<b>10.5</b>	<b>583</b>	<b>12.0</b>	583	12.0	584	12.0
483.xalancbmk	<b>334</b>	<b>20.7</b>	329	21.0	340	20.3	<b>334</b>	<b>20.7</b>	329	21.0	340	20.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

BL265  
(Intel Xeon E5504, 2.00 GHz)

SPECint2006 = 21.2

SPECint\_base2006 = 19.1

CPU2006 license: 20  
Test sponsor: Bull SAS  
Tested by: Bull SAS

Test date: Jan-2010  
Hardware Availability: May-2009  
Software Availability: Feb-2009

## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):

icpc

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32  
401.bzip2: -DSPEC_CPU_LP64  
456.hmmer: -DSPEC_CPU_LP64  
458.sjeng: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LINUX  
473.astar: -DSPEC_CPU_LP64  
483.xalancbmk: -DSPEC_CPU_LINUX
```



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

**SPECint2006 = 21.2**

BL265  
(Intel Xeon E5504, 2.00 GHz)

**SPECint\_base2006 = 19.1**

**CPU2006 license:** 20  
**Test sponsor:** Bull SAS  
**Tested by:** Bull SAS

**Test date:** Jan-2010  
**Hardware Availability:** May-2009  
**Software Availability:** Feb-2009

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzp2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmarheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap64

483.xalancbmk: basepeak = yes



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

BL265  
(Intel Xeon E5504, 2.00 GHz)

SPECint2006 = 21.2

SPECint\_base2006 = 19.1

**CPU2006 license:** 20  
**Test sponsor:** Bull SAS  
**Tested by:** Bull SAS

**Test date:** Jan-2010  
**Hardware Availability:** May-2009  
**Software Availability:** Feb-2009

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20100202.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20100202.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 05:50:05 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 2 February 2010.