



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = 28.0

IBM System x3200 M3 (Intel Xeon X3430)

SPECint_base2006 = 24.8

CPU2006 license: 11

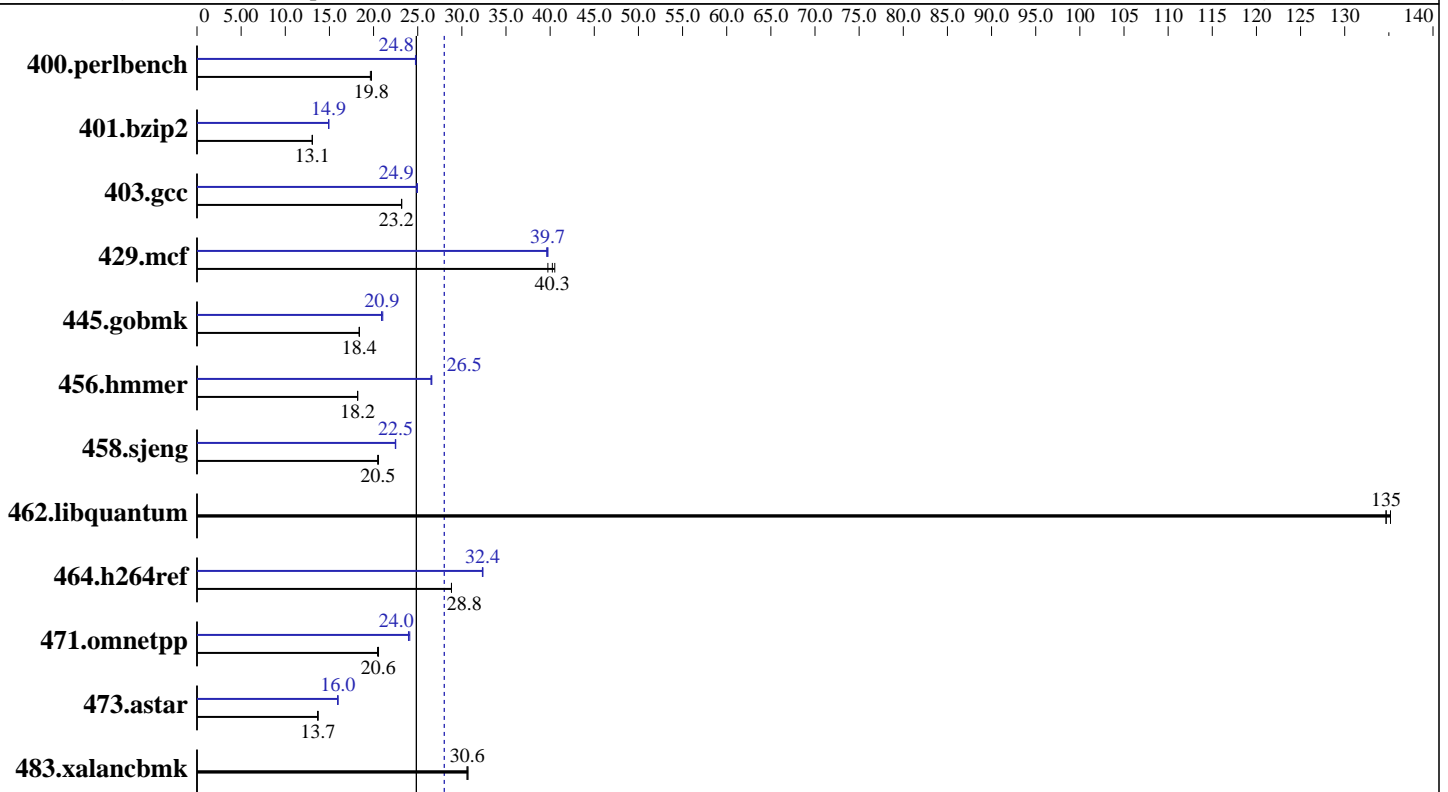
Test date: Oct-2009

Test sponsor: IBM Corporation

Hardware Availability: Oct-2009

Tested by: IBM Corporation

Software Availability: Mar-2009



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Hardware

CPU Name: Intel Xeon X3430
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4 GB PC3-10600R)
 Disk Subsystem: 1 x 250 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	497	19.6	494	19.8	<u>495</u>	<u>19.8</u>	395	24.7	394	24.8	<u>395</u>	<u>24.8</u>
401.bzip2	738	13.1	<u>739</u>	<u>13.1</u>	739	13.1	<u>647</u>	<u>14.9</u>	647	14.9	647	14.9
403.gcc	347	23.2	<u>347</u>	<u>23.2</u>	348	23.2	322	25.0	323	24.9	<u>323</u>	<u>24.9</u>
429.mcf	229	39.7	225	40.5	<u>227</u>	<u>40.3</u>	229	39.7	230	39.6	<u>230</u>	<u>39.7</u>
445.gobmk	571	18.4	<u>570</u>	<u>18.4</u>	570	18.4	502	20.9	<u>501</u>	<u>20.9</u>	498	21.0
456.hammer	<u>513</u>	<u>18.2</u>	513	18.2	513	18.2	351	26.6	352	26.5	<u>352</u>	<u>26.5</u>
458.sjeng	<u>591</u>	<u>20.5</u>	591	20.5	589	20.6	<u>538</u>	<u>22.5</u>	538	22.5	539	22.5
462.libquantum	153	135	<u>154</u>	<u>135</u>	154	135	153	135	<u>154</u>	<u>135</u>	154	135
464.h264ref	768	28.8	<u>768</u>	<u>28.8</u>	768	28.8	685	32.3	<u>684</u>	<u>32.4</u>	684	32.4
471.omnetpp	306	20.4	304	20.6	<u>304</u>	<u>20.6</u>	259	24.1	<u>261</u>	<u>24.0</u>	261	24.0
473.astar	<u>512</u>	<u>13.7</u>	514	13.7	512	13.7	441	15.9	439	16.0	<u>440</u>	<u>16.0</u>
483.xalanbmk	<u>225</u>	<u>30.6</u>	226	30.6	225	30.7	<u>225</u>	<u>30.6</u>	226	30.6	225	30.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
 CPU C-States Enable and Adjacent Sector Prefetch Enable
 Turbo Mode Enable
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalanbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc
```

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):

```
icpc
```

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

Peak Portability Flags

```
400.perlbenc: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
473.astar: -DSPEC_CPU_LP64
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzp2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
 -opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091028.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091028.xml>

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For other inquiries, please contact webmaster@spec.org.

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