



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/B120a
(Intel Xeon E5502)

SPECint®2006 = 19.8

SPECint_base2006 = 17.9

CPU2006 license: 9006

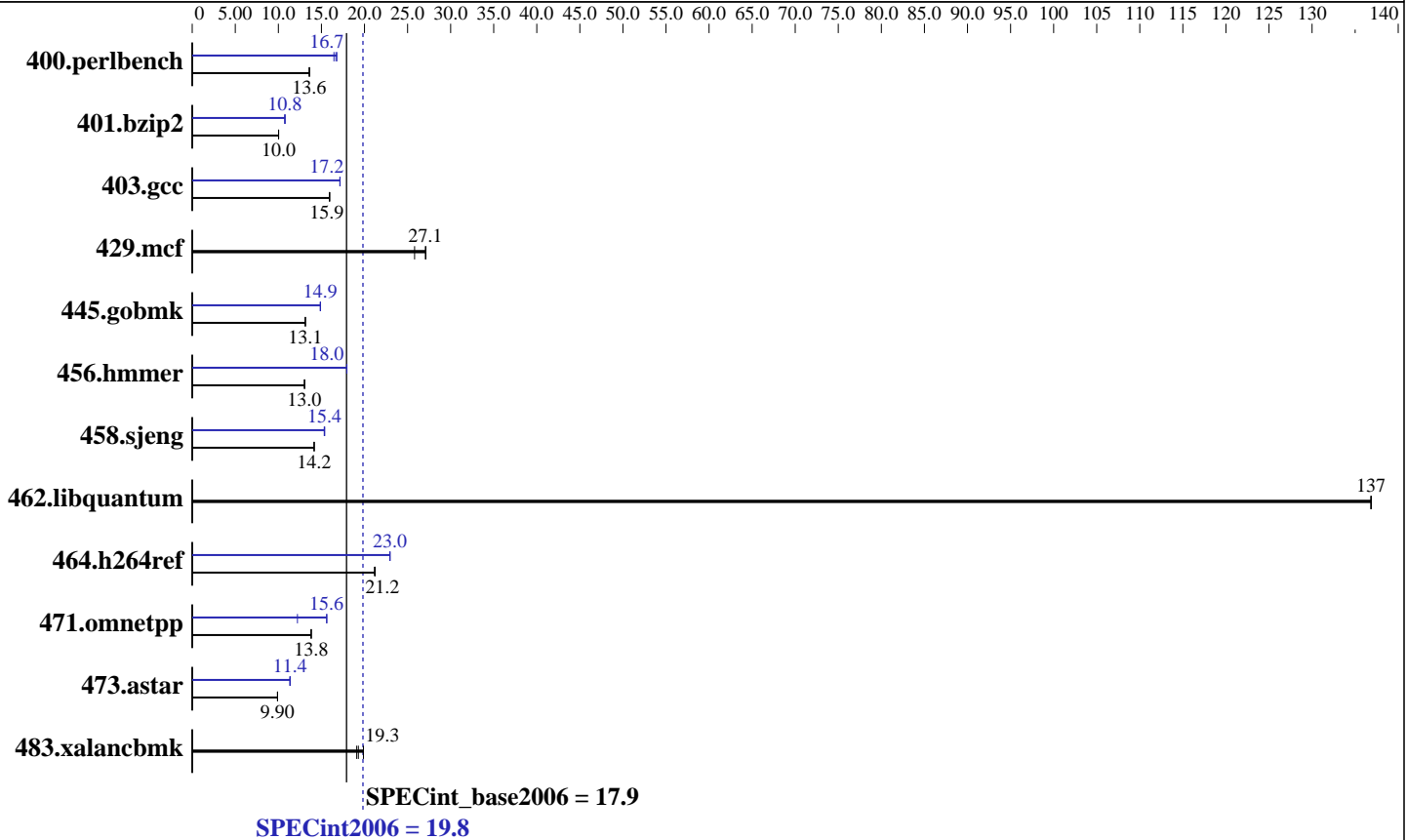
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Aug-2009

Hardware Availability: Apr-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5502
 CPU Characteristics: 1867
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 X 4 GB PC3-8500R running at 800 MHz)
 Disk Subsystem: 1x73 GB SATA2, 10000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp
 Compiler: Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.081
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	719	13.6	<u>718</u>	<u>13.6</u>	718	13.6	594	16.4	<u>586</u>	<u>16.7</u>	582	16.8
401.bzip2	<u>962</u>	<u>10.0</u>	962	10.0	963	10.0	898	10.7	<u>896</u>	<u>10.8</u>	896	10.8
403.gcc	504	16.0	506	15.9	<u>505</u>	<u>15.9</u>	470	17.1	469	17.2	<u>469</u>	<u>17.2</u>
429.mcf	353	25.8	<u>337</u>	<u>27.1</u>	336	27.1	353	25.8	<u>337</u>	<u>27.1</u>	336	27.1
445.gobmk	799	13.1	<u>799</u>	<u>13.1</u>	799	13.1	706	14.9	704	14.9	<u>705</u>	<u>14.9</u>
456.hammer	<u>715</u>	<u>13.0</u>	715	13.0	716	13.0	<u>520</u>	<u>18.0</u>	519	18.0	520	17.9
458.sjeng	<u>855</u>	<u>14.2</u>	855	14.1	855	14.2	787	15.4	<u>788</u>	<u>15.4</u>	790	15.3
462.libquantum	151	137	<u>151</u>	<u>137</u>	151	137	151	137	<u>151</u>	<u>137</u>	151	137
464.h264ref	1042	21.2	1046	21.1	<u>1043</u>	<u>21.2</u>	<u>964</u>	<u>23.0</u>	964	23.0	965	22.9
471.omnetpp	<u>453</u>	<u>13.8</u>	452	13.8	453	13.8	<u>401</u>	<u>15.6</u>	512	12.2	400	15.6
473.astar	<u>709</u>	<u>9.90</u>	709	9.90	709	9.91	617	11.4	620	11.3	<u>617</u>	<u>11.4</u>
483.xalancbmk	347	19.9	<u>358</u>	<u>19.3</u>	361	19.1	347	19.9	<u>358</u>	<u>19.3</u>	361	19.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter

Platform Notes

BIOS setting:
NUMA configuration : Enabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/081/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/081/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/081/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/081/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzp2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmarheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib64 -lsmarheap64

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revG.html>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revG.xml>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 02:10:41 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 1 September 2009.