



SPEC® CINT2006 Result

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Intel Corporation

SPECint®2006 = 21.2

Intel DQ45CB motherboard (Intel Core 2 Quad Q8300)

SPECint_base2006 = 19.4

CPU2006 license: 13

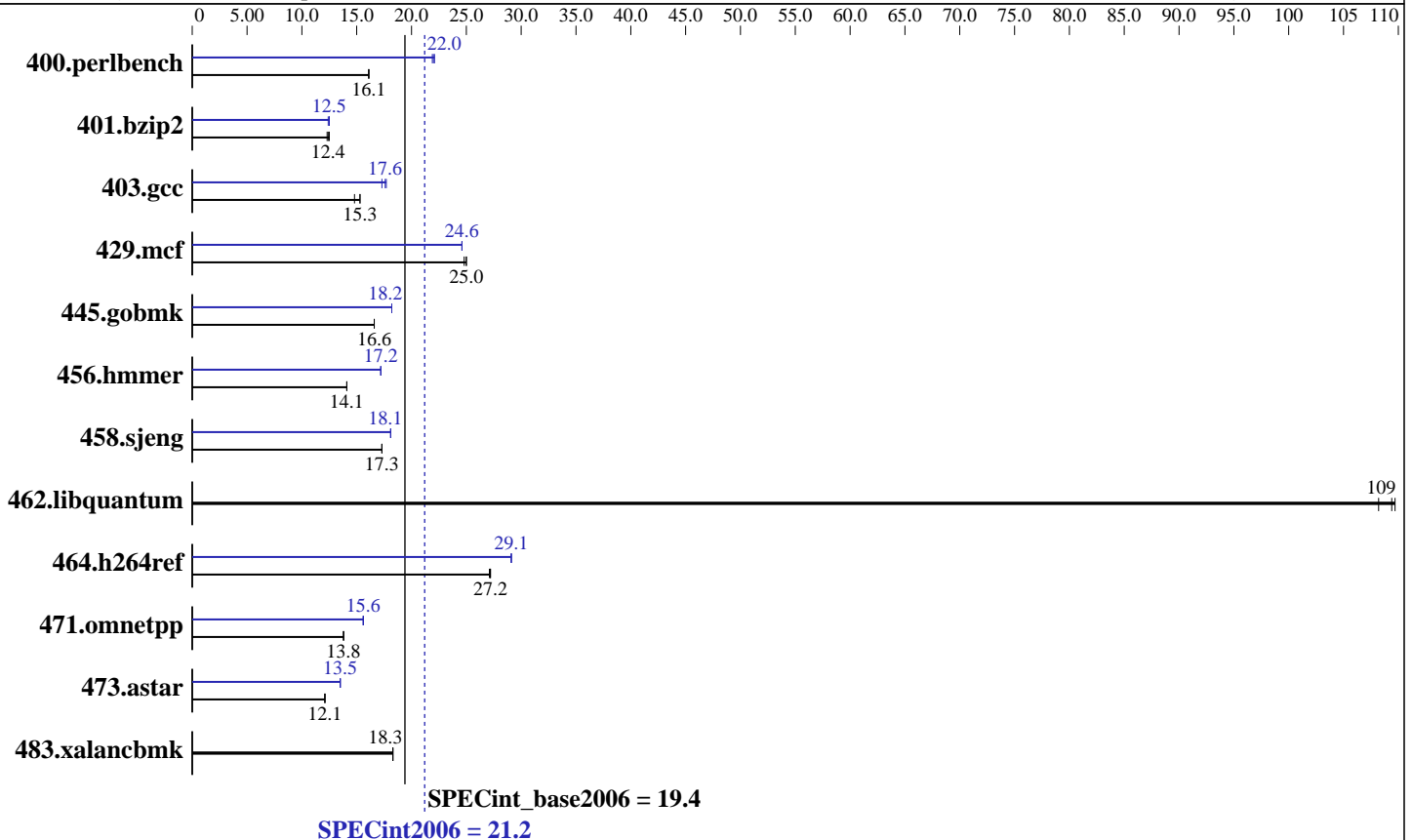
Test date: Apr-2009

Test sponsor: Intel Corporation

Hardware Availability: May-2009

Tested by: Intel Corporation

Software Availability: Nov-2008



Hardware

CPU Name: Intel Core 2 Quad Q8300
 CPU Characteristics:
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip, 2 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 4 GB (4x1GB DDR2-800 CL5)
 Disk Subsystem: Seagate 320 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)
 Compiler: Intel C++ Compiler Professional 11.0 for IA32
 Build 20080930 Package ID: w_cproc_p_11.0.054
 Microsoft Visual Studio 2008 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.1 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	605	16.1	606	16.1	606	16.1	446	21.9	443	22.0	443	22.1
401.bzip2	776	12.4	783	12.3	775	12.5	770	12.5	775	12.4	770	12.5
403.gcc	542	14.8	527	15.3	527	15.3	459	17.6	465	17.3	454	17.7
429.mcf	368	24.8	364	25.0	364	25.0	371	24.6	371	24.6	371	24.6
445.gobmk	632	16.6	631	16.6	632	16.6	576	18.2	576	18.2	576	18.2
456.hammer	661	14.1	660	14.1	661	14.1	542	17.2	542	17.2	542	17.2
458.sjeng	701	17.3	701	17.3	701	17.3	668	18.1	668	18.1	668	18.1
462.libquantum	192	108	189	109	189	110	192	108	189	109	189	110
464.h264ref	815	27.1	814	27.2	815	27.2	761	29.1	761	29.1	761	29.1
471.omnetpp	453	13.8	452	13.8	453	13.8	401	15.6	401	15.6	401	15.6
473.astar	581	12.1	580	12.1	580	12.1	519	13.5	519	13.5	519	13.5
483.xalancbmk	377	18.3	377	18.3	377	18.3	377	18.3	377	18.3	377	18.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case,
 Antec Truepower Trio power supply TP3-650
 Binaries were built on Windows Vista Ultimate (32-bit)
 Binaries were built on Windows Vista Ultimate (32-bit)
 OMP_NUM_THREADS set to number of logical processors as seen by the OS
 KMP_AFFINITY set to physical,0

Base Compiler Invocation

C benchmarks:
 icl -Qvc9 -Qc99

C++ benchmarks:
 icl -Qvc9

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
 464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
 483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword



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Base Optimization Flags

C benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qpar-runtime-control -Qvec-guard-write /F512000000

C++ benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qc99

C++ benchmarks:

icl -Qvc9

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalanbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
/F512000000

403.gcc: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- /F512000000

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Peak Optimization Flags (Continued)

429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

445.gobmk: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmr: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

458.sjeng: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.xml>



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For other inquiries, please contact webmaster@spec.org.

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