SPEC® CINT2006 Result

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Intel Corporation

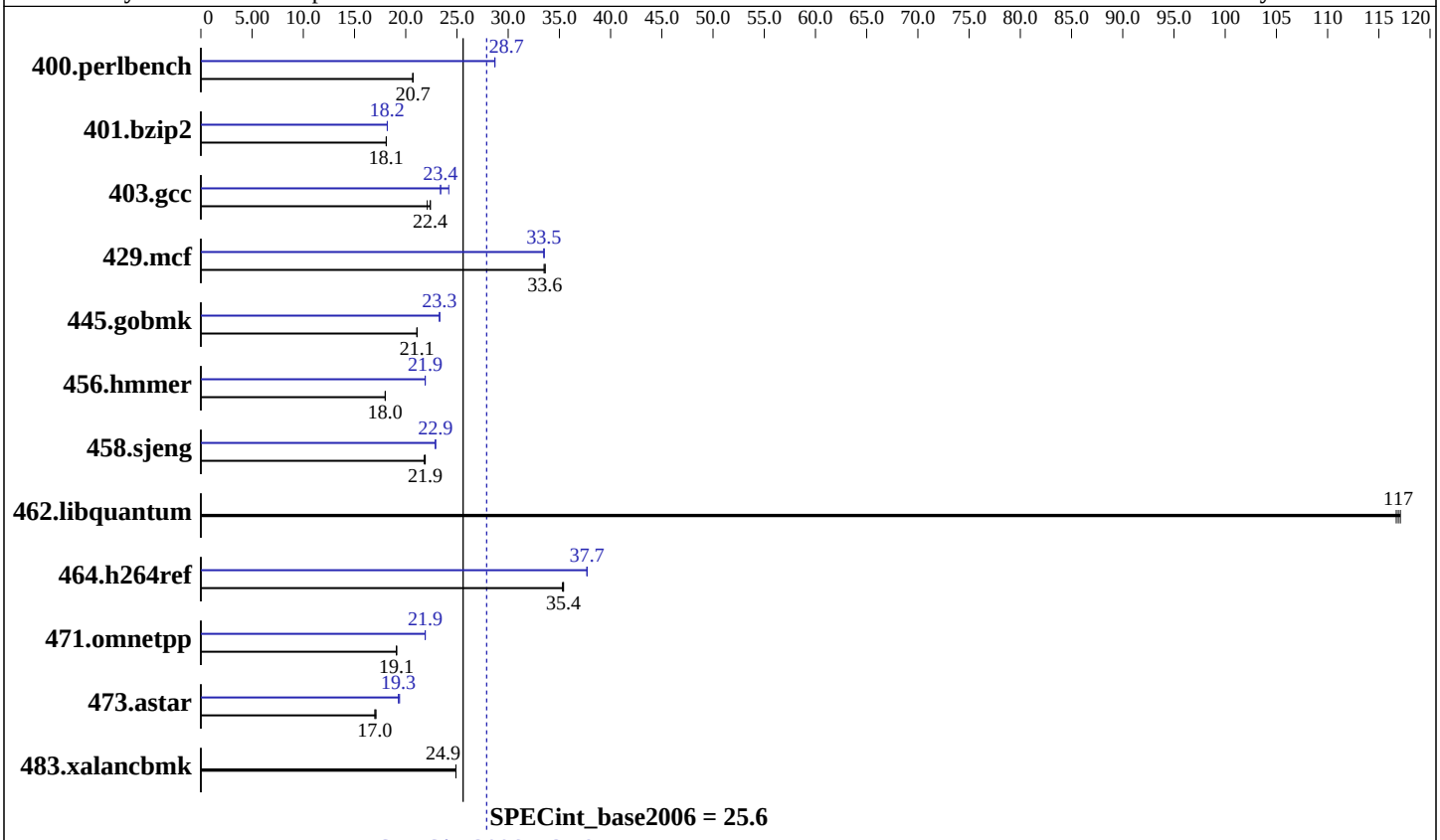
Intel DQ45CB motherboard (Intel Core 2 Duo E8500)

SPECint®2006 = 27.9

SPECint_base2006 = 25.6

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Apr-2009
Hardware Availability: May-2009
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Core 2 Duo E8500
CPU Characteristics:	
CPU MHz:	3166
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (4x1GB DDR2-800 CL5)
Disk Subsystem:	Seagate 320 GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Ultimate w/ SP1 (64-bit)
Compiler:	Intel C++ Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cproc_p_11.0.054 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	472	20.7	**472**	**20.7**	471	20.7	340	28.7	**340**	**28.7**	340	28.7
401.bzip2	534	18.1	**533**	**18.1**	532	18.1	529	18.2	530	18.2	**530**	**18.2**
403.gcc	365	22.1	359	22.4	**360**	**22.4**	344	23.4	333	24.2	**344**	**23.4**
429.mcf	272	33.6	272	33.5	**272**	**33.6**	**272**	**33.5**	272	33.5	272	33.5
445.gobmk	498	21.1	**498**	**21.1**	498	21.1	**451**	**23.3**	451	23.3	451	23.3
456.hmmer	519	18.0	**519**	**18.0**	519	18.0	**426**	**21.9**	426	21.9	425	21.9
458.sjeng	554	21.9	554	21.8	**554**	**21.9**	529	22.9	**529**	**22.9**	529	22.9
462.libquantum	177	117	178	117	**177**	**117**	177	117	178	117	**177**	**117**
464.h264ref	626	35.4	626	35.3	**626**	**35.4**	**586**	**37.7**	587	37.7	586	37.7
471.omnetpp	328	19.1	328	19.1	**328**	**19.1**	286	21.9	286	21.9	**286**	**21.9**
473.astar	412	17.1	412	17.0	**412**	**17.0**	**363**	**19.3**	363	19.3	363	19.4
483.xalancbmk	277	24.9	**278**	**24.9**	278	24.9	277	24.9	**278**	**24.9**	278	24.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with Shin-G ATX case,
Antec Truepower Trio power supply TP3-650
Binaries were built on Windows Vista Ultimate (32-bit)
Binaries were built on Windows Vista Ultimate (32-bit)
OMP_NUM_THREADS set to number of logical processors as seen by the OS
KMP_AFFINITY set to physical,0
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qc99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qpar-runtime-control -Qvec-guard-write /F512000000
```

C++ benchmarks:
```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qc99
```

C++ benchmarks:
```
icl -Qvc9
```

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch`
`/F512000000 shlw32m.lib            -link /FORCE:MULTIPLE`

401.bzip2: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias`
`/F512000000`

403.gcc: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- /F512000000`

Continued on next page

Peak Optimization Flags (Continued)

429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

445.gobmk: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

458.sjeng: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.xml

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