SPEC® CFP2006 Result
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Fujitsu

PRIMERGY RX200 S5, Intel Xeon E5506, 2.13 GHz

$SPECfp^®_rate2006 = $ 119

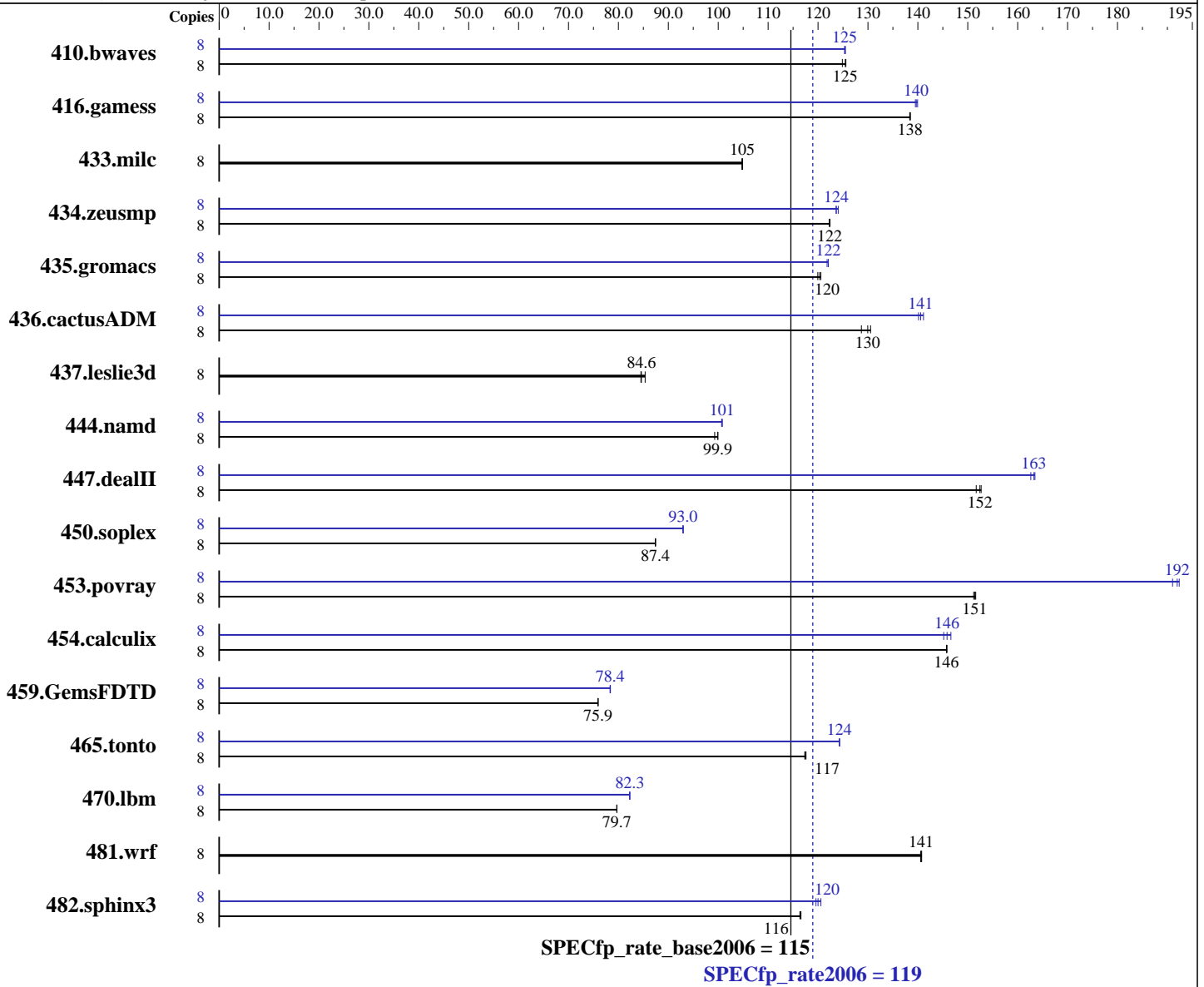$SPECfp_rate_base2006 = $ 115

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu Siemens Computers

Test date: Feb-2009
Hardware Availability: Apr-2009
Software Availability: Feb-2009

SPECfp_rate_base2006 = 115

SPECfp_rate2006 = 119

Hardware

CPU Name: Intel Xeon E5506
CPU Characteristics:
CPU MHz: 2133
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
Auto Parallel: No
File System: ext3
System State: Multi-User Run Level 3
Base Pointers: 64-bit

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L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem:	1 x SAS, 73 GB, 10000 RPM
Other Hardware:	None

| Peak Pointers: | 32/64-bit |
| Other Software: | Binutils 2.18.50.0.7.20080502 |

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	871	125	866	126	**867**	**125**	8	866	125	**867**	**125**	867	125
416.gamess	8	**1131**	**138**	1132	138	1131	138	8	**1121**	**140**	1119	140	1123	140
433.milc	8	700	105	**701**	**105**	701	105	8	700	105	**701**	**105**	701	105
434.zeusmp	8	**595**	**122**	596	122	595	122	8	**589**	**124**	589	124	587	124
435.gromacs	8	474	121	476	120	**475**	**120**	8	468	122	**468**	**122**	469	122
436.cactusADM	8	743	129	**736**	**130**	732	131	8	677	141	682	140	**680**	**141**
437.leslie3d	8	881	85.4	**889**	**84.6**	890	84.5	8	881	85.4	**889**	**84.6**	890	84.5
444.namd	8	642	99.9	**642**	**99.9**	646	99.3	8	**637**	**101**	636	101	637	101
447.dealII	8	603	152	599	153	**600**	**152**	8	560	163	**561**	**163**	563	163
450.soplex	8	763	87.5	763	87.4	**763**	**87.4**	8	718	92.9	**718**	**93.0**	717	93.0
453.povray	8	281	152	**281**	**151**	281	151	8	**222**	**192**	223	191	221	192
454.calculix	8	453	146	453	146	**453**	**146**	8	450	147	**453**	**146**	455	145
459.GemsFDTD	8	1117	76.0	**1118**	**75.9**	1119	75.9	8	1083	78.4	**1083**	**78.4**	1084	78.3
465.tonto	8	669	118	671	117	**670**	**117**	8	633	124	**633**	**124**	633	124
470.lbm	8	1379	79.7	1380	79.7	**1379**	**79.7**	8	1336	82.3	1336	82.3	**1336**	**82.3**
481.wrf	8	**635**	**141**	636	141	635	141	8	**635**	**141**	636	141	635	141
482.sphinx3	8	1340	116	1337	117	**1339**	**116**	8	1293	121	**1299**	**120**	1304	120

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

     For information about Fujitsu please visit: http://www.fujitsu.com

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Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Fortran benchmarks:
    ifort

Benchmarks using both Fortran and C:
    icc ifort

Base Portability Flags

         410.bwaves: -DSPEC_CPU_LP64
         416.gamess: -DSPEC_CPU_LP64
           433.milc: -DSPEC_CPU_LP64
         434.zeusmp: -DSPEC_CPU_LP64
        435.gromacs: -DSPEC_CPU_LP64 -nofor_main
      436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
        437.leslie3d: -DSPEC_CPU_LP64
          444.namd: -DSPEC_CPU_LP64
         447.dealII: -DSPEC_CPU_LP64
         450.soplex: -DSPEC_CPU_LP64
         453.povray: -DSPEC_CPU_LP64
        454.calculix: -DSPEC_CPU_LP64 -nofor_main
      459.GemsFDTD: -DSPEC_CPU_LP64
          465.tonto: -DSPEC_CPU_LP64
           470.lbm: -DSPEC_CPU_LP64
            481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
        482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

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Peak Compiler Invocation

C benchmarks (except as noted below):
        `icc`

        482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
        `icpc`

        450.soplex: `icpc -m32`

Fortran benchmarks:
        `ifort`

Benchmarks using both Fortran and C:
        `icc ifort`

Peak Portability Flags

        410.bwaves: `-DSPEC_CPU_LP64`
        416.gamess: `-DSPEC_CPU_LP64`
          433.milc: `-DSPEC_CPU_LP64`
        434.zeusmp: `-DSPEC_CPU_LP64`
        435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
      436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
        437.leslie3d: `-DSPEC_CPU_LP64`
          444.namd: `-DSPEC_CPU_LP64`
          447.dealII: `-DSPEC_CPU_LP64`
         453.povray: `-DSPEC_CPU_LP64`
        454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
      459.GemsFDTD: `-DSPEC_CPU_LP64`
          465.tonto: `-DSPEC_CPU_LP64`
           470.lbm: `-DSPEC_CPU_LP64`
           481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

        433.milc: `basepeak = yes`

        470.lbm: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`
                `-auto-ilp32`

        482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`

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Peak Optimization Flags (Continued)

C++ benchmarks:

    444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -fno-alias -auto-ilp32

    447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -unroll2 -ansi-alias -scalar-rep-

    450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -opt-malloc-options=3

    453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -unroll4 -ansi-alias

Fortran benchmarks:

    410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

    416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -unroll2 -Ob0 -ansi-alias -scalar-rep-

    434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

    437.leslie3d: basepeak = yes

    459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -unroll2 -Ob0 -opt-prefetch

    465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -unroll4 -auto

Benchmarks using both Fortran and C:

    435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -opt-prefetch -auto-ilp32

    436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -unroll2 -opt-prefetch -auto-ilp32

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Peak Optimization Flags (Continued)

454.calculix: `-xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.xml`

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