



# SPEC® CINT2006 Result

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## NEC Corporation

Express5800/i120Ra-e1  
(Intel Xeon L5215)

SPECint®\_rate2006 = 56.9

SPECint\_rate\_base2006 = 53.0

CPU2006 license: 9006

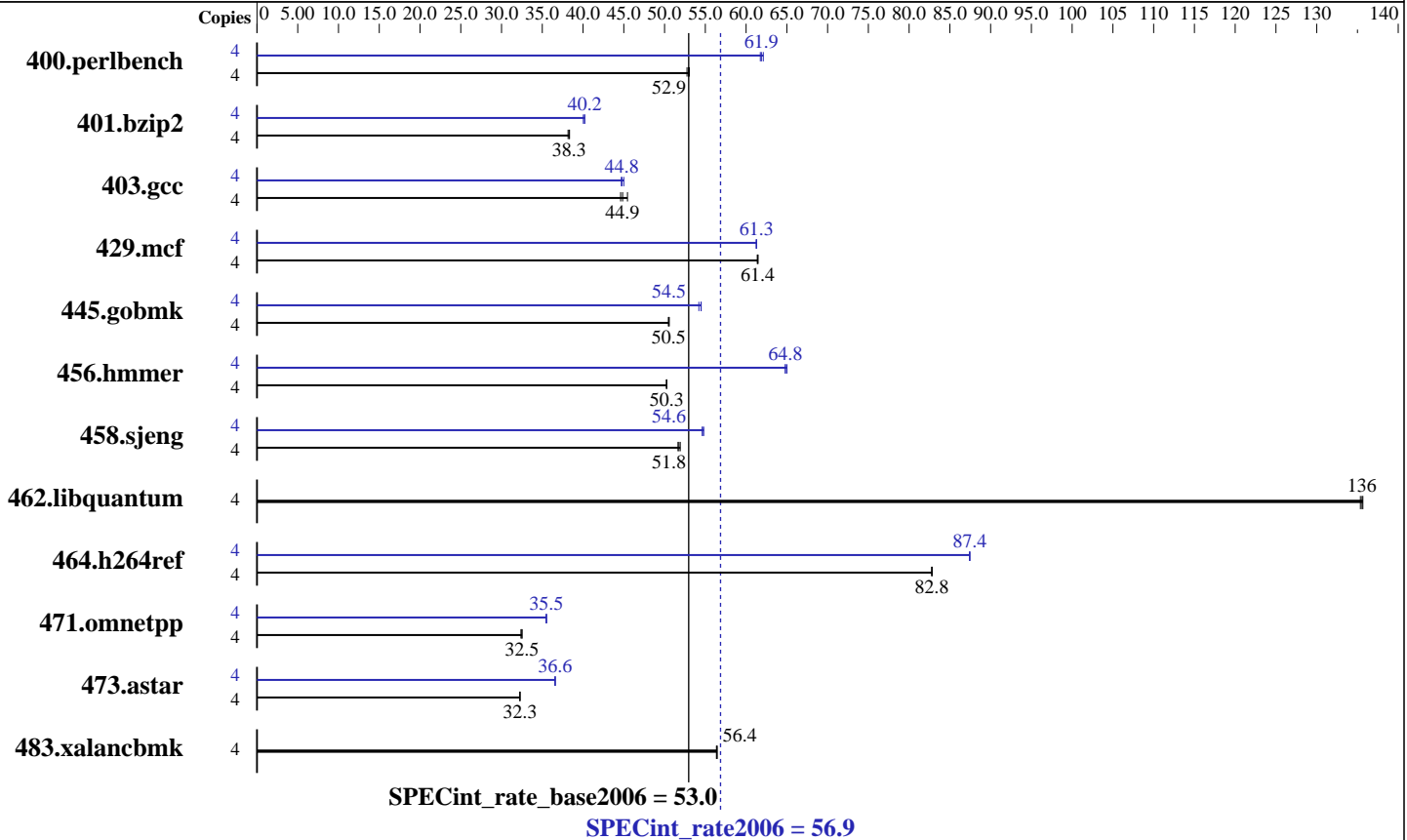
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jan-2009

Hardware Availability: Jan-2009

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon L5215  
 CPU Characteristics: 1066 MHz system bus  
 CPU MHz: 1867  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (4x4 GB PC2-5300P, 2 rank, CL5-5-5, ECC)  
 Disk Subsystem: 1x80 GB SATAII, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20081105 Package ID: l\_cproc\_p\_11.0.074  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap Library 8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	737	53.0	<b>738</b>	<b>52.9</b>	741	52.8	4	<b>632</b>	<b>61.9</b>	629	62.1	633	61.7
401.bzip2	4	1007	38.3	<b>1008</b>	<b>38.3</b>	1012	38.2	4	964	40.0	960	40.2	<b>960</b>	<b>40.2</b>
403.gcc	4	722	44.6	<b>718</b>	<b>44.9</b>	708	45.4	4	720	44.7	<b>719</b>	<b>44.8</b>	716	45.0
429.mcf	4	594	61.4	593	61.5	<b>594</b>	<b>61.4</b>	4	<b>595</b>	<b>61.3</b>	596	61.2	595	61.3
445.gobmk	4	<b>831</b>	<b>50.5</b>	830	50.6	831	50.5	4	770	54.5	774	54.2	<b>770</b>	<b>54.5</b>
456.hammer	4	<b>743</b>	<b>50.3</b>	743	50.2	743	50.3	4	574	65.0	576	64.8	<b>576</b>	<b>64.8</b>
458.sjeng	4	932	51.9	<b>935</b>	<b>51.8</b>	937	51.6	4	886	54.6	883	54.8	<b>886</b>	<b>54.6</b>
462.libquantum	4	612	135	<b>611</b>	<b>136</b>	611	136	4	612	135	<b>611</b>	<b>136</b>	611	136
464.h264ref	4	1069	82.8	1069	82.8	<b>1069</b>	<b>82.8</b>	4	1012	87.5	1013	87.4	<b>1012</b>	<b>87.4</b>
471.omnetpp	4	772	32.4	<b>769</b>	<b>32.5</b>	769	32.5	4	705	35.5	<b>704</b>	<b>35.5</b>	704	35.5
473.astar	4	<b>871</b>	<b>32.3</b>	870	32.3	871	32.2	4	767	36.6	<b>768</b>	<b>36.6</b>	768	36.6
483.xalancbmk	4	<b>489</b>	<b>56.4</b>	490	56.4	489	56.5	4	<b>489</b>	<b>56.4</b>	490	56.4	489	56.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
taskset was used to bind processes to cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

Bios settings:  
Hardware Prefetcher: Enabled  
Adjacent Cache Line Prefetch: Disabled

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/074/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/074/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/074/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/074/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/074/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/074/ipp/em64t/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.html>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.xml>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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