SPEC® CINT2006 Result

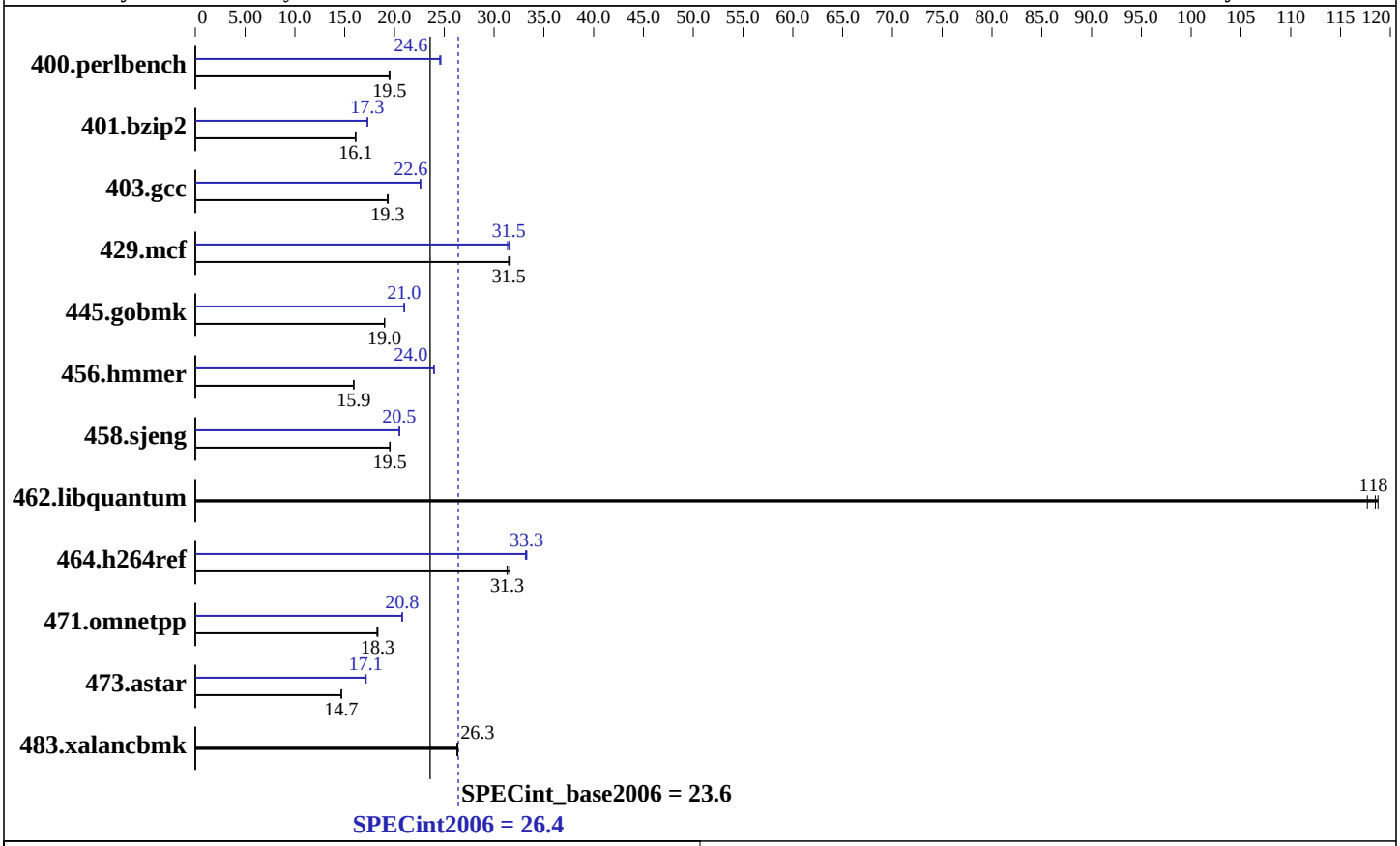
NTT System S. A.
NTT Tytan 516I

SPECint®2006 = 26.4

SPECint_base2006 = 23.6

CPU2006 license: 9013
Test sponsor: NTT System S. A.
Tested by: NTT System S. A.

Test date: Dec-2008
Hardware Availability: Dec-2008
Software Availability: Dec-2008



Hardware

CPU Name:	Intel Xeon X3360
CPU Characteristics:	2.83 GHz, 2x6 MB L2 shared, 1333 MHz System Bus
CPU MHz:	2833
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2GB)
Disk Subsystem:	400 GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	SuSe Linux SLES10 SP1, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

SPEC CINT2006 Result

NTT System S. A.
NTT Tytan 516I

SPECint2006 = 26.4

SPECint_base2006 = 23.6

CPU2006 license: 9013
Test sponsor: NTT System S. A.
Tested by: NTT System S. A.

Test date: Dec-2008
Hardware Availability: Dec-2008
Software Availability: Dec-2008

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	502	19.5	500	19.5	**501**	**19.5**	**397**	**24.6**	398	24.5	396	24.7
401.bzip2	**599**	**16.1**	599	16.1	601	16.1	559	17.3	**558**	**17.3**	557	17.3
403.gcc	416	19.4	**417**	**19.3**	417	19.3	**356**	**22.6**	356	22.6	357	22.6
429.mcf	290	31.5	288	31.6	**290**	**31.5**	**290**	**31.5**	291	31.4	289	31.5
445.gobmk	**552**	**19.0**	552	19.0	552	19.0	499	21.0	500	21.0	**500**	**21.0**
456.hmmer	**586**	**15.9**	587	15.9	586	15.9	389	24.0	**389**	**24.0**	389	24.0
458.sjeng	620	19.5	**619**	**19.5**	618	19.6	591	20.5	**591**	**20.5**	590	20.5
462.libquantum	**175**	**118**	174	119	176	118	**175**	**118**	174	119	176	118
464.h264ref	701	31.6	**706**	**31.3**	707	31.3	665	33.3	**665**	**33.3**	667	33.2
471.omnetpp	343	18.2	341	18.3	**341**	**18.3**	301	20.8	301	20.8	**301**	**20.8**
473.astar	478	14.7	479	14.7	**479**	**14.7**	409	17.2	412	17.0	**410**	**17.1**
483.xalancbmk	262	26.4	**262**	**26.3**	263	26.3	262	26.4	**262**	**26.3**	263	26.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch
```

C++ benchmarks:
```
-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/066/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/066/bin/intel64/icc`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -ansi-alias -opt-prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3`

429.mcf: `-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -O2 -ipo -no-prec-div -ansi-alias`

Continued on next page

Peak Optimization Flags (Continued)

456.hmmer: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.html
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.xml
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 22:46:14 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 January 2009.