SPEC® CINT2006 Result
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Dell Inc.

Dell Precision R5400 (Intel Xeon X5270, 3.50 GHz)

$SPECint®2006 =$ 28.8
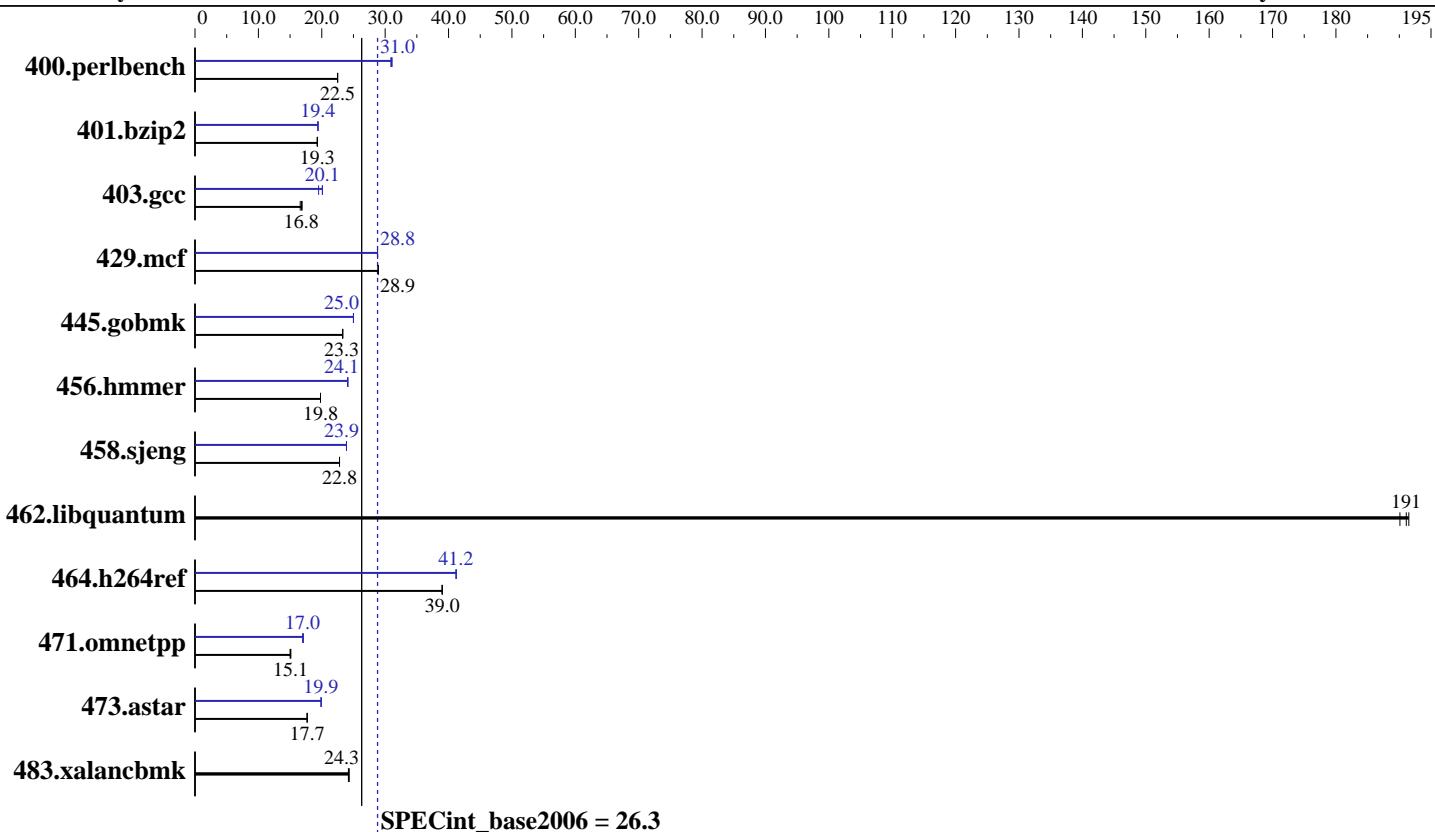
$SPECint\_base2006 =$ 26.3

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Dec-2008
Hardware Availability: Oct-2008
Software Availability: Nov-2008

400.perlbench 31.0 / 22.5
401.bzip2 19.4 / 19.3
403.gcc 20.1 / 16.8
429.mcf 28.8 / 28.9
445.gobmk 25.0 / 23.3
456.hmmer 24.1 / 19.8
458.sjeng 23.9 / 22.8
462.libquantum 191
464.h264ref 41.2 / 39.0
471.omnetpp 17.0 / 15.1
473.astar 19.9 / 17.7
483.xalancbmk 24.3

SPECint_base2006 = 26.3
SPECint2006 = 28.8

Hardware

CPU Name:	Intel Xeon X5270
CPU Characteristics:	1333 MHz Bus Speed
CPU MHz:	3500
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (4x4 GB DDR2-667 FB-DIMM, CL5)
Disk Subsystem:	1 x 320 GB SATA 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Business SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA-32, Version 11.0 Build 20080930 Package ID: w_cproc_p_11.0.061 Microsoft Visual Studio 2008 SP1
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.1

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	435	22.5	434	22.5	**435**	**22.5**	315	31.1	**315**	**31.0**	316	30.9
401.bzip2	**501**	**19.3**	501	19.3	501	19.3	497	19.4	497	19.4	**497**	**19.4**
403.gcc	477	16.9	486	16.6	**479**	**16.8**	413	19.5	401	20.1	**401**	**20.1**
429.mcf	316	28.9	**315**	**28.9**	315	28.9	317	28.8	**317**	**28.8**	317	28.8
445.gobmk	**451**	**23.3**	450	23.3	451	23.3	420	25.0	420	25.0	**420**	**25.0**
456.hmmer	**471**	**19.8**	472	19.8	471	19.8	388	24.1	**388**	**24.1**	388	24.1
458.sjeng	531	22.8	**530**	**22.8**	530	22.8	**506**	**23.9**	506	23.9	506	23.9
462.libquantum	**108**	**191**	108	192	109	190	**108**	**191**	108	192	109	190
464.h264ref	567	39.0	567	39.0	**567**	**39.0**	538	41.2	**538**	**41.2**	538	41.2
471.omnetpp	415	15.0	**415**	**15.1**	414	15.1	**367**	**17.0**	366	17.1	367	17.0
473.astar	397	17.7	397	17.7	**397**	**17.7**	353	19.9	**353**	**19.9**	353	19.9
483.xalancbmk	285	24.2	284	24.3	**284**	**24.3**	285	24.2	284	24.3	**284**	**24.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

    Binaries were built on Windows Vista Ultimate (64-bit)

    BIOS Settings
      Adjacent Cache Line Prefetch set to ON

Base Compiler Invocation

C benchmarks:
      `icl -Qvc9 -Qstd=c99`

C++ benchmarks:
      `icl -Qvc9`

Base Portability Flags

        403.gcc: `-DSPEC_CPU_WIN32`
    464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
    483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

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Test sponsor: Dell Inc.		**Hardware Availability:**	Oct-2008
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Base Optimization Flags

C benchmarks:
```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qpar-runtime-control -Qvec-guard-write /F512000000
```

C++ benchmarks:
```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx_features
/F512000000 shlw32m.lib           -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen(pass 1) -QxSSE4.1 -Qprof_use(pass 2) -Qipo -O3`
`-Qprec-div- -Qansi-alias -Qopt-prefetch /F512000000`
`shlw32m.lib              -link /FORCE:MULTIPLE`

401.bzip2: `-Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qprof_use(pass 2)`
`-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch`
`/F512000000`

403.gcc: `-Qprof_gen(pass 1) -QxSSE4.1 -Qprof_use(pass 2) -Qipo -O3`
`-Qprec-div- /F512000000`

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Peak Optimization Flags (Continued)

```
    429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
             /F512000000

  445.gobmk: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qprof_use(pass 2)
             -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

  456.hmmer: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
             /F512000000

   458.sjeng: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qprof_use(pass 2)
             -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: basepeak = yes

  464.h264ref: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qprof_use(pass 2)
             -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000
```

C++ benchmarks:

```
   471.omnetpp: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qprof_use(pass 2)
             -Qipo -O3 -Qprec-div- -Qansi-alias
             -Qopt-ra-region-strategy=block /F512000000 shlw32m.lib
                      -link /FORCE:MULTIPLE

    473.astar: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qprof_use(pass 2)
             -Qipo -O3 -Qprec-div- -Qansi-alias
             -Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib
                      -link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
      403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/dell.ic11.0.windows.flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/dell.ic11.0.windows.flags.xml

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