SPEC® CINT2006 Result
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Intel Corporation
Lenovo Thinkpad T60

SPECint®2006 =   12.9
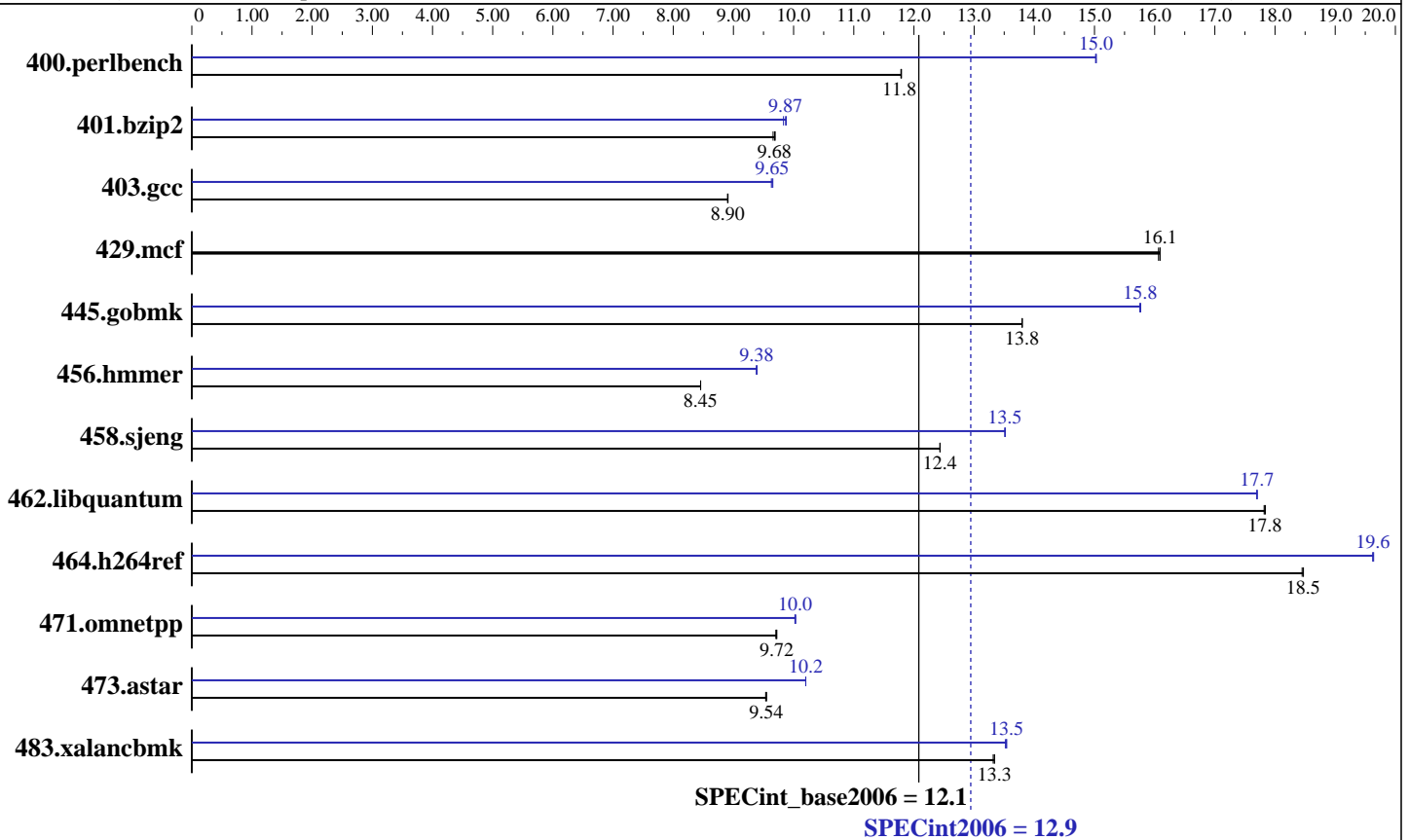
SPECint_base2006 =   12.1

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Aug-2008
Hardware Availability: Sep-2006
Software Availability: Nov-2007

Benchmark results:
- 400.perlbench: 15.0 / 11.8
- 401.bzip2: 9.87 / 9.68
- 403.gcc: 9.65 / 8.90
- 429.mcf: 16.1
- 445.gobmk: 15.8 / 13.8
- 456.hmmer: 9.38 / 8.45
- 458.sjeng: 13.5 / 12.4
- 462.libquantum: 17.7 / 17.8
- 464.h264ref: 19.6 / 18.5
- 471.omnetpp: 10.0 / 9.72
- 473.astar: 10.2 / 9.54
- 483.xalancbmk: 13.5 / 13.3

SPECint_base2006 = 12.1

SPECint2006 = 12.9

Hardware

CPU Name:	Intel Core Duo T2700
CPU Characteristics:	
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Micron PC2-5300 DDR2-667 CL5)
Disk Subsystem:	Hitachi Travelstar HTS721010G9SA00 SATA 100GB 7200RPM
Other Hardware:	None

Software

Operating System:	Windows XP Professional SP2
Compiler:	Intel C++ Compiler for IA32 version 10.1 Build 20070913 Package ID: w_cc_p_10.1.011 Microsoft Visual Studio 2005 SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	829	11.8	828	11.8	**829**	**11.8**	**650**	**15.0**	650	15.0	651	15.0
401.bzip2	999	9.66	995	9.69	**996**	**9.68**	977	9.88	**978**	**9.87**	981	9.83
403.gcc	904	8.90	903	8.91	**904**	**8.90**	834	9.66	**835**	**9.65**	836	9.63
429.mcf	**567**	**16.1**	567	16.1	568	16.1	**567**	**16.1**	567	16.1	568	16.1
445.gobmk	**760**	**13.8**	760	13.8	760	13.8	665	15.8	666	15.8	**666**	**15.8**
456.hmmer	1103	8.46	**1104**	**8.45**	1104	8.45	993	9.39	994	9.38	**994**	**9.38**
458.sjeng	973	12.4	973	12.4	**973**	**12.4**	895	13.5	896	13.5	**895**	**13.5**
462.libquantum	1163	17.8	**1162**	**17.8**	1161	17.8	**1171**	**17.7**	1171	17.7	1170	17.7
464.h264ref	1198	18.5	1199	18.5	**1198**	**18.5**	**1127**	**19.6**	1128	19.6	1127	19.6
471.omnetpp	643	9.72	**643**	**9.72**	644	9.70	624	10.0	623	10.0	**623**	**10.0**
473.astar	**735**	**9.54**	735	9.55	736	9.54	688	10.2	688	10.2	**688**	**10.2**
483.xalancbmk	**517**	**13.3**	517	13.3	518	13.3	**510**	**13.5**	510	13.5	510	13.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
   Binaries were built on Windows Vista32
   The following VS 2005 SP1 updates were applied: KB926601 and KB932232
```

Base Compiler Invocation

C benchmarks:
```
      icl -Qvc8 -Qc99
```

C++ benchmarks:
```
      icl -Qvc8
```

Base Portability Flags

```
        403.gcc: -DSPEC_CPU_WIN32
     464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
   483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Base Optimization Flags

C benchmarks:
```
     -QxP -Qipo -O3 -Qprec-div- -Qparallel -Qpar-runtime-control
     -Qvec-guard-write /F512000000 libguide40.lib
```

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Base Optimization Flags (Continued)

C++ benchmarks:
```
-QxP -Qipo -O3 -Qprec-div- -Qcxx_features /F512000000 shlw32m.lib
libguide40.lib          -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc8 -Qc99
```

C++ benchmarks:
```
icl -Qvc8
```

Peak Portability Flags

```
   403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
               -Qprec-div- -Qansi-alias -Qprefetch -Qparallel
               -Qpar-runtime-control /F512000000 shlw32m.lib
               libguide40.lib             -link /FORCE:MULTIPLE

   401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
              -Qprec-div- -Qprefetch /F512000000 libguide40.lib

     403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
              -Qprec-div- /F512000000 libguide40.lib

     429.mcf: basepeak = yes
```

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Peak Optimization Flags (Continued)

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -O2 -Qipo
-Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qunroll2 -Qansi-alias
-Qopt-multi-version-aggressive /F512000000 libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qunroll4 /F512000000 libguide40.lib

462.libquantum: -QxP -Qipo -O3 -Qprec-div- -Qunroll4 -Ob0 -Qprefetch
-Qopt-streaming-stores:always -Qparallel
-Qpar-runtime-control /F512000000 libguide40.lib

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qunroll2 -Qansi-alias /F512000000
libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block
-Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine
-Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qansi-alias -Qcxx_features /F512000000
shlw32m.lib libguide40.lib   -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-revC.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-revC.xml

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