



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint®2006 = 16.3

IBM BladeCenter HS12 (Intel Core 2 Duo E6405)

SPECint_base2006 = 14.1

CPU2006 license: 11

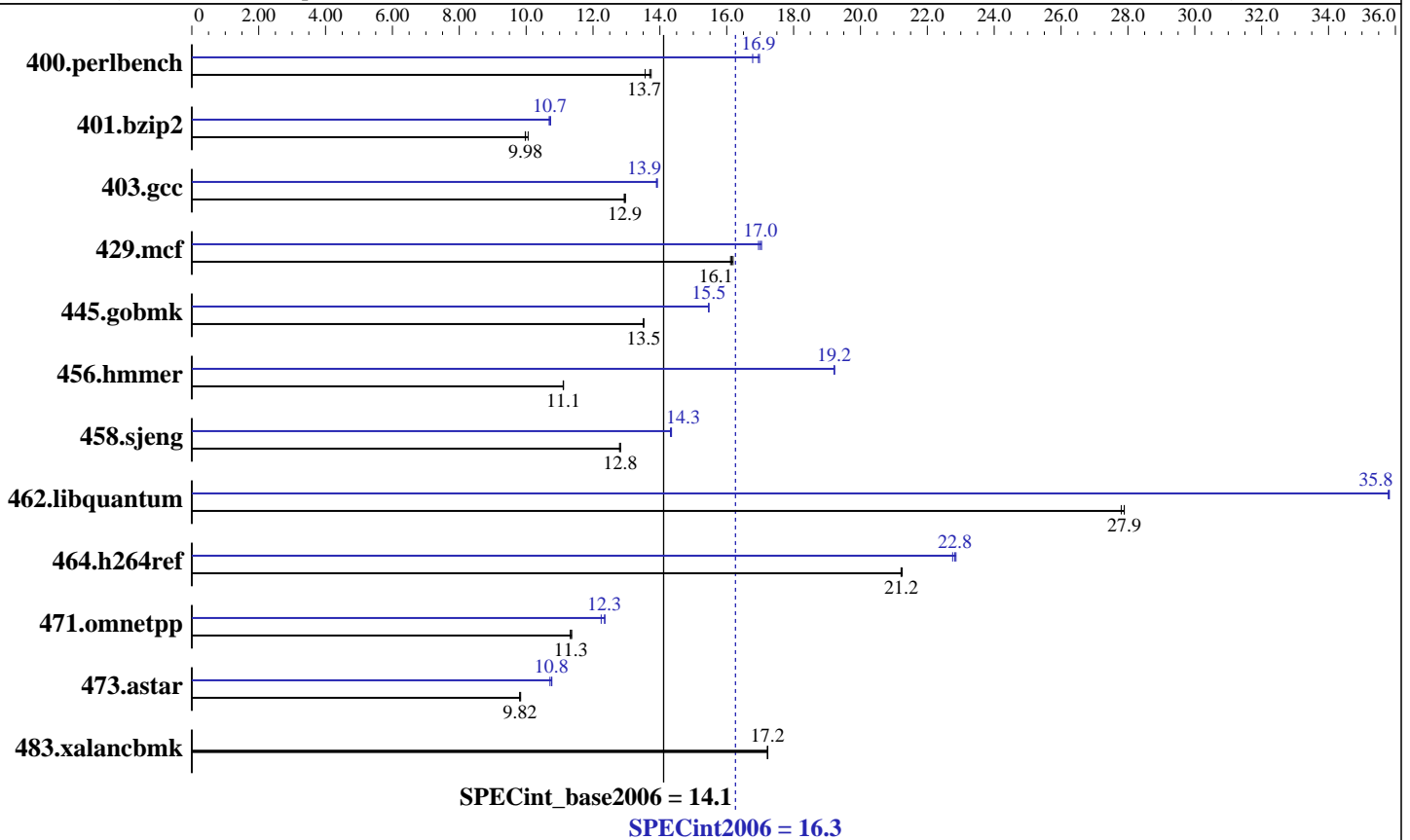
Test date: Apr-2008

Test sponsor: IBM Corporation

Hardware Availability: May-2008

Tested by: IBM Corporation

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Duo E6405
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 2133
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 2 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4 x 2 GB DDR2-5300 ECC)
 Disk Subsystem: 1 x 73 GB SAS, 10000 RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap 8.1 Binutils 2.17.50.0.15



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 16.3

IBM BladeCenter HS12 (Intel Core 2 Duo E6405)

SPECint_base2006 = 14.1

CPU2006 license: 11

Test date: Apr-2008

Test sponsor: IBM Corporation

Hardware Availability: May-2008

Tested by: IBM Corporation

Software Availability: Nov-2007

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	721	13.6	<u>713</u>	<u>13.7</u>	711	13.7	<u>576</u>	<u>16.9</u>	575	17.0	582	16.8
401.bzip2	968	9.97	959	10.1	<u>967</u>	<u>9.98</u>	<u>901</u>	<u>10.7</u>	899	10.7	903	10.7
403.gcc	621	13.0	622	12.9	<u>622</u>	<u>12.9</u>	578	13.9	579	13.9	<u>579</u>	<u>13.9</u>
429.mcf	566	16.1	<u>565</u>	<u>16.1</u>	563	16.2	535	17.0	<u>536</u>	<u>17.0</u>	538	17.0
445.gobmk	776	13.5	777	13.5	<u>776</u>	<u>13.5</u>	678	15.5	<u>678</u>	<u>15.5</u>	678	15.5
456.hmmmer	840	11.1	839	11.1	<u>840</u>	<u>11.1</u>	485	19.2	486	19.2	<u>486</u>	<u>19.2</u>
458.sjeng	944	12.8	<u>944</u>	<u>12.8</u>	946	12.8	845	14.3	843	14.3	<u>844</u>	<u>14.3</u>
462.libquantum	745	27.8	743	27.9	<u>743</u>	<u>27.9</u>	<u>579</u>	<u>35.8</u>	578	35.8	579	35.8
464.h264ref	1041	21.3	<u>1043</u>	<u>21.2</u>	1043	21.2	972	22.8	968	22.9	<u>970</u>	<u>22.8</u>
471.omnetpp	552	11.3	550	11.4	<u>551</u>	<u>11.3</u>	510	12.2	<u>506</u>	<u>12.3</u>	506	12.4
473.astar	716	9.81	<u>715</u>	<u>9.82</u>	714	9.84	655	10.7	<u>653</u>	<u>10.8</u>	652	10.8
483.xalancbmk	<u>401</u>	<u>17.2</u>	401	17.2	400	17.2	<u>401</u>	<u>17.2</u>	401	17.2	400	17.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode
Hardware Sector Prefetch Enabled and Adjacent Sector Prefetch Enabled
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to null

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 16.3

IBM BladeCenter HS12 (Intel Core 2 Duo E6405)

SPECint_base2006 = 14.1

CPU2006 license: 11

Test date: Apr-2008

Test sponsor: IBM Corporation

Hardware Availability: May-2008

Tested by: IBM Corporation

Software Availability: Nov-2007

Base Optimization Flags

C benchmarks:

-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 16.3

IBM BladeCenter HS12 (Intel Core 2 Duo E6405)

SPECint_base2006 = 14.1

CPU2006 license: 11

Test date: Apr-2008

Test sponsor: IBM Corporation

Hardware Availability: May-2008

Tested by: IBM Corporation

Software Availability: Nov-2007

Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 16.3

IBM BladeCenter HS12 (Intel Core 2 Duo E6405)

SPECint_base2006 = 14.1

CPU2006 license: 11

Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Apr-2008

Hardware Availability: May-2008

Software Availability: Nov-2007

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090714.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 17:12:54 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 13 May 2008.