



SPEC® CINT2006 Result

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Intel Corporation

Intel Desktop Board DQ35JO (Intel Core 2 Duo E8500)

SPECint®2006 = 24.6

SPECint_base2006 = 22.2

CPU2006 license: 13

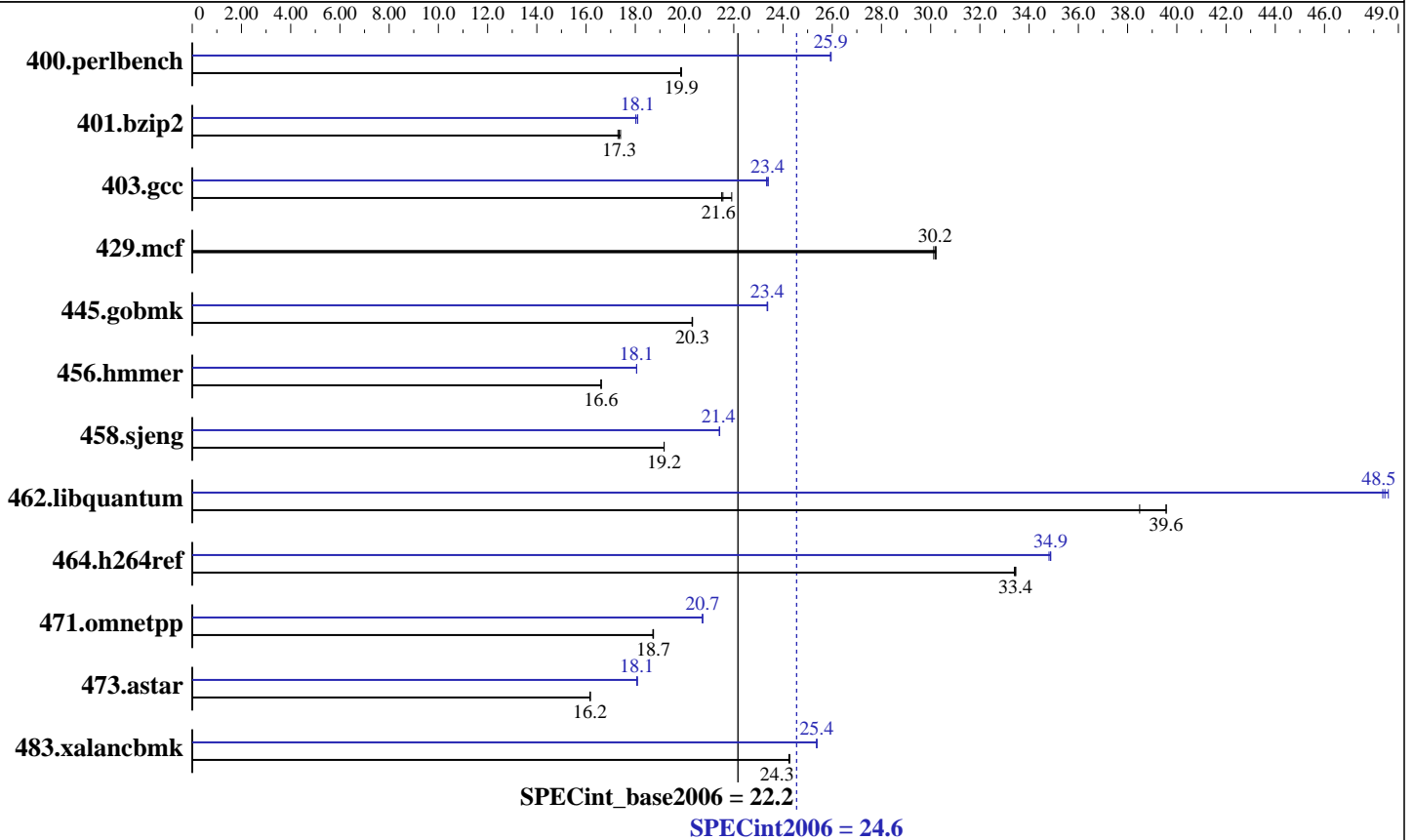
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Feb-2008

Hardware Availability: Feb-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Duo E8500
 CPU Characteristics: 3.16 GHz, 1333 FSB
 CPU MHz: 3167
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1GB Micron DDR2-800 CL5)
 Disk Subsystem: Seagate 320GB NCQ SATA, 16MB cache, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 10.1
 Build 20070913 Package ID: w_cc_p_10.1.011
 Microsoft Visual Studio 2005 SP1 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	492	19.8	492	19.9	492	19.9	377	25.9	377	25.9	376	26.0
401.bzip2	558	17.3	556	17.3	555	17.4	536	18.0	534	18.1	533	18.1
403.gcc	374	21.5	373	21.6	367	21.9	344	23.4	345	23.3	344	23.4
429.mcf	303	30.1	302	30.2	302	30.2	303	30.1	302	30.2	302	30.2
445.gobmk	517	20.3	516	20.3	516	20.3	449	23.4	449	23.4	449	23.4
456.hammer	562	16.6	562	16.6	561	16.6	517	18.1	517	18.1	517	18.1
458.sjeng	631	19.2	631	19.2	631	19.2	565	21.4	565	21.4	565	21.4
462.libquantum	538	38.5	524	39.6	524	39.6	426	48.6	428	48.4	428	48.5
464.h264ref	663	33.4	661	33.5	662	33.4	636	34.8	635	34.9	634	34.9
471.omnetpp	334	18.7	334	18.7	334	18.7	302	20.7	302	20.7	301	20.7
473.astar	434	16.2	434	16.2	434	16.2	389	18.1	389	18.1	388	18.1
483.xalancbmk	285	24.2	284	24.3	284	24.3	272	25.4	272	25.4	272	25.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 03/2008:

<http://www.intel.com/products/motherboard/DQ35JO/index.htm>

The system bus runs at 1333 MHz

System was configured with Asus EN8800GTX discrete graphics card

Binaries were built on Windows Vista Ultimate (32-bit)

The following VS 2005 SP1 updates were applied: KB926601 and KB932232

Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
```

```
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

```
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```



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Base Optimization Flags

C benchmarks:

```
-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000
libguide40.lib
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch -Qparallel -Qpar-runtime-control /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
/F512000000 libguide40.lib

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
libguide40.lib
```

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Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -Qopt-multi-version-aggressive /F512000000
libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
/F512000000 libguide40.lib

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch
-Qopt-streaming-stores:always -Qparallel
-Qpar-runtime-control /F512000000 libguide40.lib

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias /F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=block -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=routine -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-flags.xml>



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