SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

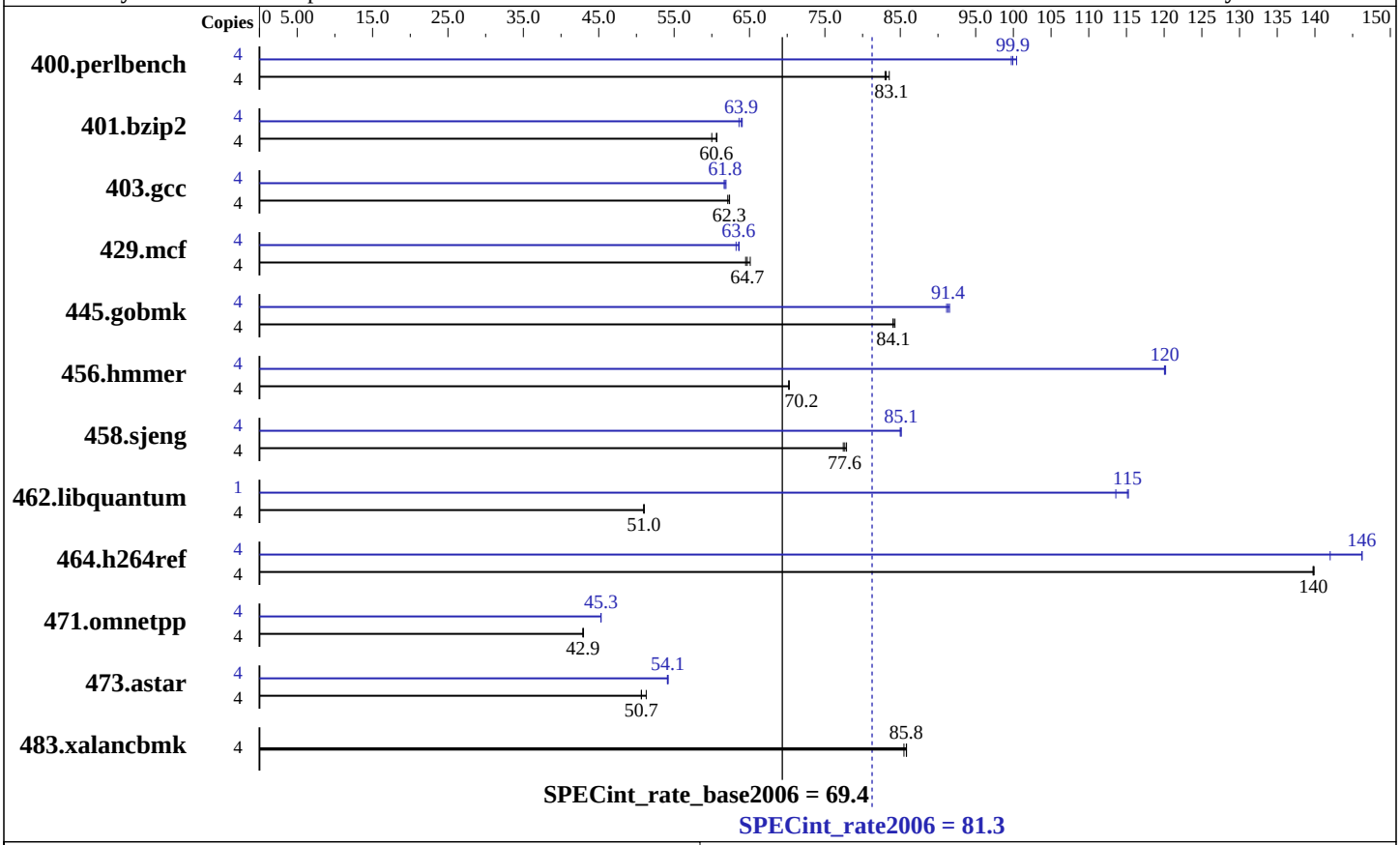
Express5800/120Lj
(Intel Xeon X5260)

SPECint®_rate2006 = 81.3

SPECint_rate_base2006 = 69.4

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Feb-2008
Hardware Availability: Feb-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X5260
CPU Characteristics:	3.33 GHz, 6 MB L2, 1333 MHz bus
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	12 GB (12x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	1x73.2 GB SAS, 15000RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Lj
(Intel Xeon X5260)

SPECint_rate2006 = 81.3

SPECint_rate_base2006 = 69.4

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Feb-2008
Hardware Availability: Feb-2008
Software Availability: Nov-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**470**	**83.1**	468	83.5	471	83.0	4	**391**	**99.9**	389	100	392	99.7
401.bzip2	4	**637**	**60.6**	636	60.7	643	60.0	4	607	63.6	603	64.0	**604**	**63.9**
403.gcc	4	**517**	**62.3**	516	62.4	518	62.1	4	522	61.6	**521**	**61.8**	520	61.9
429.mcf	4	566	64.5	**564**	**64.7**	560	65.1	4	573	63.6	**574**	**63.6**	577	63.2
445.gobmk	4	498	84.3	**499**	**84.1**	499	84.0	4	460	91.1	**459**	**91.4**	458	91.5
456.hmmer	4	**531**	**70.2**	531	70.3	532	70.2	4	311	120	**311**	**120**	310	120
458.sjeng	4	622	77.9	625	77.4	**623**	**77.6**	4	569	85.1	569	85.0	**569**	**85.1**
462.libquantum	4	**1625**	**51.0**	1626	51.0	1624	51.0	1	180	115	182	114	**180**	**115**
464.h264ref	4	633	140	633	140	**633**	**140**	4	**605**	**146**	623	142	605	146
471.omnetpp	4	583	42.9	**583**	**42.9**	582	43.0	4	552	45.3	**552**	**45.3**	552	45.3
473.astar	4	554	50.6	547	51.3	**554**	**50.7**	4	**519**	**54.1**	519	54.1	518	54.2
483.xalancbmk	4	**322**	**85.8**	323	85.5	322	85.8	4	**322**	**85.8**	323	85.5	322	85.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 OMP_NUM_THREADS set to number of cores
```

Platform Notes

```
Bios settings:
Intel SpeedStep Technology: Disabled
```

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2,
456.hmmer, for peak, are compiled in 64-bit mode

 The NEC Express5800/120Lj(Intel Xeon X5260) and
 the Bull NovaScale T860 E1(Intel Xeon X5260,3.33GHz) models are electronically equivalent.
 The results have been measured on a NEC Express5800/120Lj(Intel Xeon X5260) model.
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page

Peak Other Flags (Continued)

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090713.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090713.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 15:31:16 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 March 2008.