SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

T120Rc-1
(Intel Xeon X5460)

SPECint®_rate2006 =   134
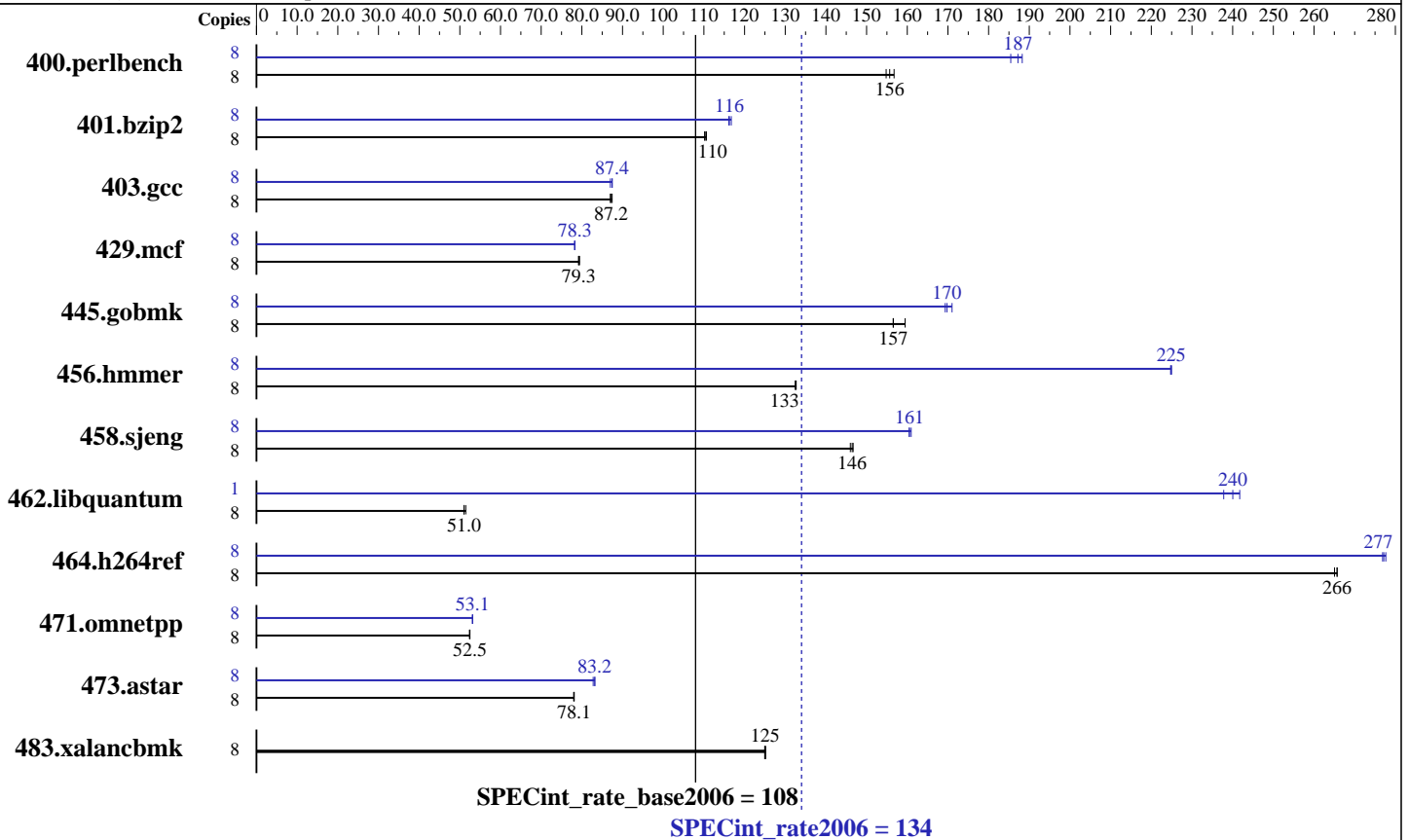
SPECint_rate_base2006 = 108

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Feb-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007

Copies		
400.perlbench	8	187
	8	156
401.bzip2	8	116
	8	110
403.gcc	8	87.4
	8	87.2
429.mcf	8	78.3
	8	79.3
445.gobmk	8	170
	8	157
456.hmmer	8	225
	8	133
458.sjeng	8	161
	8	146
462.libquantum	1	240
	8	51.0
464.h264ref	8	277
	8	266
471.omnetpp	8	53.1
	8	52.5
473.astar	8	83.2
	8	78.1
483.xalancbmk	8	125

SPECint_rate_base2006 = 108

SPECint_rate2006 = 134

Hardware
CPU Name: Intel Xeon X5460
CPU Characteristics: 3.16 GHz, 2x6 MB L2 shared, 1333 MHz bus
CPU MHz: 3167
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 12 GB (12x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1x250 GB SATAII, 7200RPM
Other Hardware: None

Software
Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel: Yes
File System: ext2
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: MicroQuill SmartHeap library 8.1
binutils-2.17.tar.gz, Version 2.17

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

T120Rc-1
(Intel Xeon X5460)

SPECint_rate2006 =   134

SPECint_rate_base2006 = 108

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Feb-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007

Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	499	157	505	155	**502**	**156**	8	421	185	415	188	**417**	**187**
401.bzip2	8	701	110	698	111	**700**	**110**	8	**664**	**116**	661	117	665	116
403.gcc	8	740	87.0	**738**	**87.2**	736	87.4	8	736	87.5	740	87.0	**737**	**87.4**
429.mcf	8	918	79.4	**921**	**79.3**	922	79.2	8	933	78.2	**932**	**78.3**	932	78.3
445.gobmk	8	526	159	**536**	**157**	536	157	8	491	171	496	169	**494**	**170**
456.hmmer	8	**563**	**133**	563	132	562	133	8	**332**	**225**	332	225	332	225
458.sjeng	8	663	146	660	147	**661**	**146**	8	603	160	601	161	**603**	**161**
462.libquantum	8	3249	51.0	**3247**	**51.0**	3221	51.5	1	87.1	238	85.7	242	**86.3**	**240**
464.h264ref	8	668	265	**666**	**266**	666	266	8	639	277	**639**	**277**	638	278
471.omnetpp	8	955	52.4	**953**	**52.5**	953	52.5	8	941	53.1	**941**	**53.1**	941	53.1
473.astar	8	719	78.1	720	78.0	**719**	**78.1**	8	675	83.2	**675**	**83.2**	678	82.8
483.xalancbmk	8	441	125	**441**	**125**	442	125	8	441	125	**441**	**125**	442	125

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores

Platform Notes

Bios settings:
Intel SpeedStep Technology: Disabled

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2,
456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

SPEC CINT2006 Result

NEC Corporation

T120Rc-1
(Intel Xeon X5460)

SPECint_rate2006 =   134

SPECint_rate_base2006 = 108

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Feb-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
    -fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
    -xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
    -L/opt/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
     403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
    icc
```

```
    401.bzip2: /opt/intel/cce/10.1.008/bin/icc
             -L/opt/intel/cce/10.1.008/lib
             -I/opt/intel/cce/10.1.008/include
```

```
    456.hmmer: /opt/intel/cce/10.1.008/bin/icc
             -L/opt/intel/cce/10.1.008/lib
             -I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:
```
    icpc
```

Peak Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    401.bzip2: -DSPEC_CPU_LP64
    456.hmmer: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LINUX
```

SPEC CINT2006 Result

NEC Corporation

SPECint_rate2006 =   134

T120Rc-1
(Intel Xeon X5460)

SPECint_rate_base2006 =   108

CPU2006 license: 9006	**Test date:** Feb-2008
Test sponsor: NEC Corporation	**Hardware Availability:** Jan-2008
Tested by: NEC Corporation	**Software Availability:** Nov-2007

Peak Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch`

401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap`

473.astar: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

NEC Corporation

T120Rc-1
(Intel Xeon X5460)

SPECint_rate2006 =   134

SPECint_rate_base2006 = 108

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Feb-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007

Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090713.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090713.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 15:33:47 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 March 2008.