SPEC® CINT2006 Result
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Supermicro Motherboard C2SBA+

SPECint®2006 = 21.9

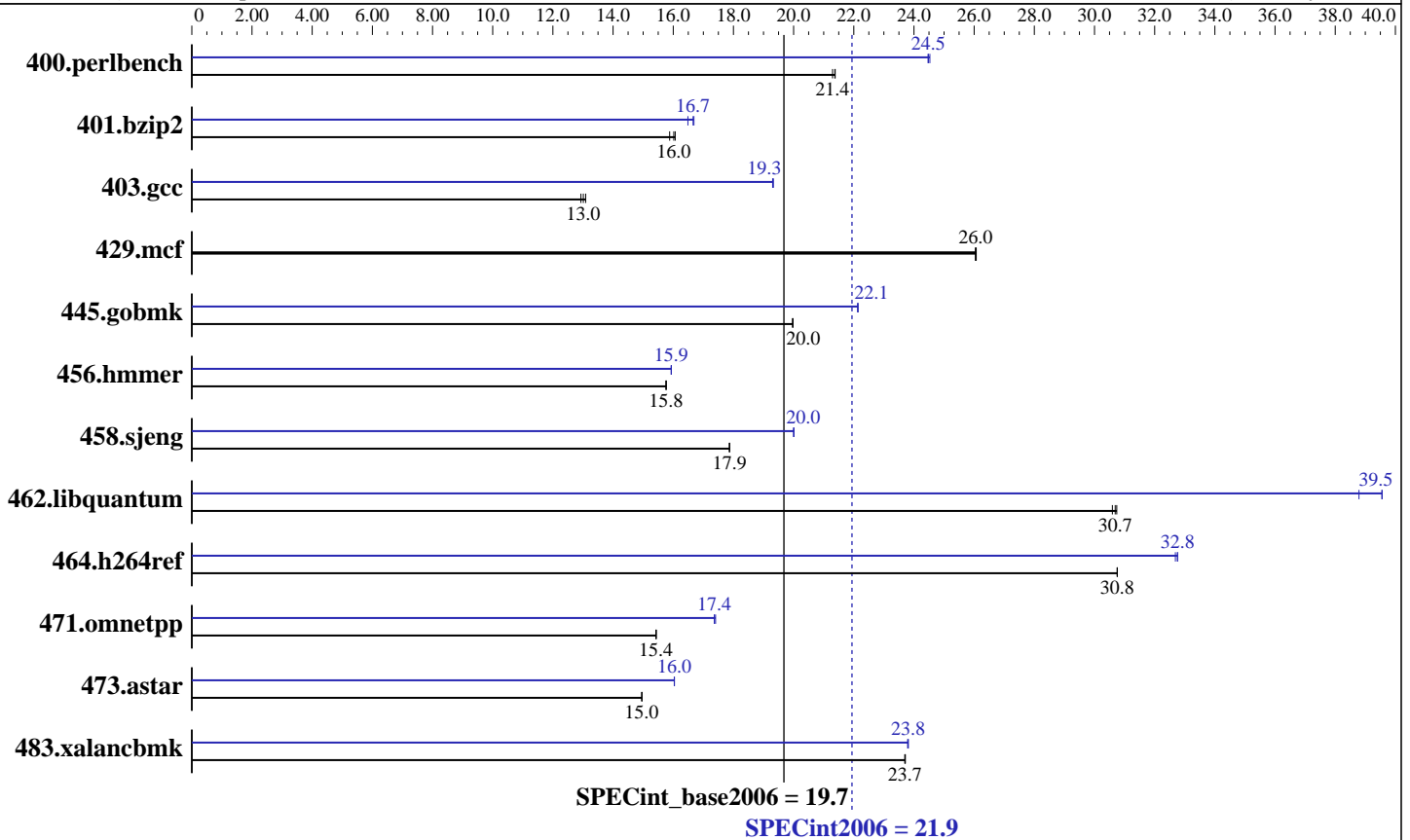SPECint_base2006 = 19.7

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Sep-2007
Hardware Availability: Jul-2007
Software Availability: May-2007

400.perlbench	24.5 / 21.4
401.bzip2	16.7 / 16.0
403.gcc	19.3 / 13.0
429.mcf	26.0
445.gobmk	22.1 / 20.0
456.hmmer	15.9 / 15.8
458.sjeng	20.0 / 17.9
462.libquantum	39.5 / 30.7
464.h264ref	32.8 / 30.8
471.omnetpp	17.4 / 15.4
473.astar	16.0 / 15.0
483.xalancbmk	23.8 / 23.7

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Hardware

CPU Name:	Intel Core 2 Duo E6850
CPU Characteristics:	3.00GHz, 1333MHz Bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4 X 2GB PC2-6400, CL5)
Disk Subsystem:	74GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Ultimate (32-bit)
Compiler:	Intel C++ Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.0 from http://www.microquill.com/

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	459	21.3	**457**	**21.4**	457	21.4	398	24.5	**399**	**24.5**	399	24.5
401.bzip2	608	15.9	601	16.1	**602**	**16.0**	585	16.5	**579**	**16.7**	578	16.7
403.gcc	623	12.9	**619**	**13.0**	615	13.1	417	19.3	417	19.3	**417**	**19.3**
429.mcf	**350**	**26.0**	350	26.1	350	26.0	**350**	**26.0**	350	26.1	350	26.0
445.gobmk	525	20.0	525	20.0	**525**	**20.0**	474	22.1	474	22.1	**474**	**22.1**
456.hmmer	**592**	**15.8**	592	15.8	592	15.8	586	15.9	**586**	**15.9**	586	15.9
458.sjeng	677	17.9	**677**	**17.9**	677	17.9	605	20.0	**605**	**20.0**	605	20.0
462.libquantum	677	30.6	**675**	**30.7**	674	30.7	**524**	**39.5**	524	39.6	534	38.8
464.h264ref	719	30.8	720	30.8	**719**	**30.8**	677	32.7	676	32.8	**676**	**32.8**
471.omnetpp	405	15.4	405	15.4	**405**	**15.4**	360	17.4	359	17.4	**360**	**17.4**
473.astar	**469**	**15.0**	469	15.0	469	15.0	438	16.0	**438**	**16.0**	438	16.0
483.xalancbmk	**291**	**23.7**	291	23.7	291	23.7	**290**	**23.8**	290	23.8	290	23.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with case CSE-733i-450
To ensure system stability, a 420W (minimum) ATX power supply [4-pin +12V AND (20 or 24-pin)]
is required.
Product description located at:
http://www.supermicro.com/products/motherboard/Core2Duo/G33/C2SBA+II.cfm
The system bus runs at 1333 MHz
BIOS Setting : Default
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

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Base Optimization Flags

C benchmarks:
```
    -fast /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
    -fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
    403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
    icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
    icl -Qvc7.1
```

Peak Portability Flags

```
    403.gcc: -DSPEC_CPU_WIN32
    464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
            -Qprefetch /F512000000 shlw32m.lib
                        -link /FORCE:MULTIPLE

    401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
            shlw32m.lib              -link /FORCE:MULTIPLE

    403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
                        -link /FORCE:MULTIPLE

    429.mcf: basepeak = yes
```

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Peak Optimization Flags (Continued)

```
445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
           -Qprec_div- -Qansi-alias /F512000000
                        -link /FORCE:MULTIPLE

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
           -Qansi-alias /F512000000 shlw32m.lib
                        -link /FORCE:MULTIPLE

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
           /F512000000 shlw32m.lib              -link /FORCE:MULTIPLE

462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
           -Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000
           shlw32m.lib              -link /FORCE:MULTIPLE

464.h264ref: Same as 456.hmmer

C++ benchmarks:
    -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
    -Qcxx_features /F512000000 shlw32m.lib
                   -link /FORCE:MULTIPLE
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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