SPEC® CINT2006 Result

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Itautec

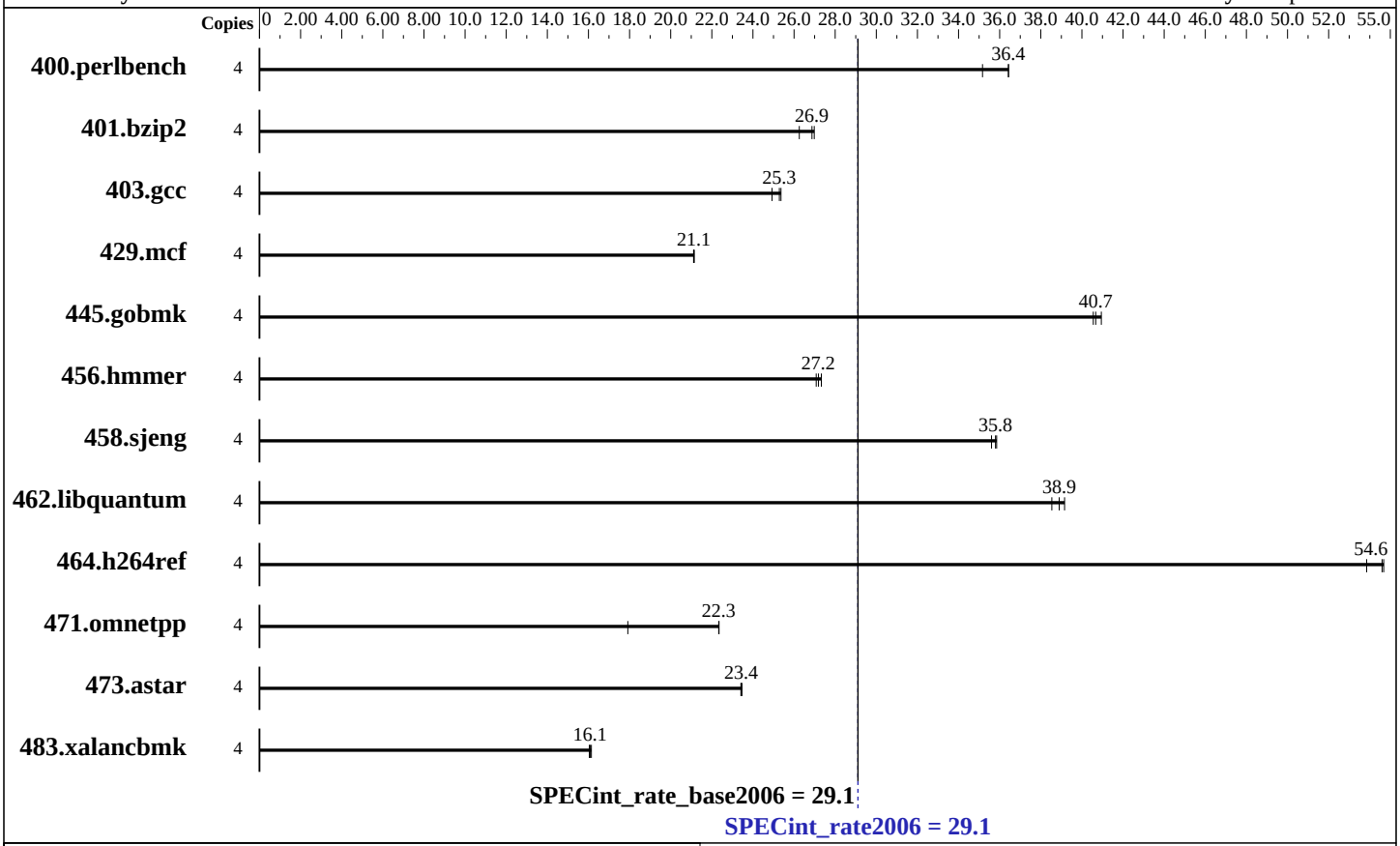
Servidor Itautec MR201 (AMD Opteron 2210)

SPECint®_rate2006 = 29.1

SPECint_rate_base2006 = 29.1

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Aug-2007
Hardware Availability: Oct-2007
Software Availability: Apr-2006



Hardware

CPU Name:	AMD Opteron 2210
CPU Characteristics:	
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (16x1GB, DDR2-533 CL5 ECC Dual Rank)
Disk Subsystem:	80 GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	SUSE SLES10 (for AMD64)
Compiler:	gcc , g++ & gfortran 4.1.0 (for AMD64)
Auto Parallel:	No
File System:	ReiserFS
System State:	runlevel 5
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other Software:	None

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	1111	35.2	1073	36.4	**1073**	**36.4**	4	1111	35.2	1073	36.4	**1073**	**36.4**
401.bzip2	4	1470	26.3	1431	27.0	**1436**	**26.9**	4	1470	26.3	1431	27.0	**1436**	**26.9**
403.gcc	4	1270	25.4	**1273**	**25.3**	1292	24.9	4	1270	25.4	**1273**	**25.3**	1292	24.9
429.mcf	4	1728	21.1	1725	21.1	**1727**	**21.1**	4	1728	21.1	1725	21.1	**1727**	**21.1**
445.gobmk	4	1035	40.6	**1032**	**40.7**	1025	40.9	4	1035	40.6	**1032**	**40.7**	1025	40.9
456.hmmer	4	1378	27.1	1366	27.3	**1373**	**27.2**	4	1378	27.1	1366	27.3	**1373**	**27.2**
458.sjeng	4	**1352**	**35.8**	1359	35.6	1350	35.9	4	**1352**	**35.8**	1359	35.6	1350	35.9
462.libquantum	4	2151	38.5	**2131**	**38.9**	2117	39.2	4	2151	38.5	**2131**	**38.9**	2117	39.2
464.h264ref	4	**1621**	**54.6**	1619	54.7	1644	53.9	4	**1621**	**54.6**	1619	54.7	1644	53.9
471.omnetpp	4	**1119**	**22.3**	1395	17.9	1118	22.4	4	**1119**	**22.3**	1395	17.9	1118	22.4
473.astar	4	1199	23.4	**1198**	**23.4**	1197	23.5	4	1199	23.4	**1198**	**23.4**	1197	23.5
483.xalancbmk	4	1720	16.0	**1716**	**16.1**	1710	16.1	4	1720	16.0	**1716**	**16.1**	1710	16.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
gcc

C++ benchmarks:
g++

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
`-O3 -fno-inline-functions -funroll-loops`

C++ benchmarks:
`-O3 -funroll-loops`

Peak Optimization Flags

C benchmarks:

400.perlbench: `basepeak = yes`

401.bzip2: `basepeak = yes`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `basepeak = yes`

456.hmmer: `basepeak = yes`

458.sjeng: `basepeak = yes`

462.libquantum: `basepeak = yes`

464.h264ref: `basepeak = yes`

C++ benchmarks:

471.omnetpp: `basepeak = yes`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.04.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.04.xml`

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For other inquiries, please contact webmaster@spec.org.

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