SPEC® CINT2006 Result
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ACTION S.A.

ACTINA SOLAR 672 X2 (Intel Xeon processor 5110, 1.60GHz)

SPECint®_rate2006 =   35.7

SPECint_rate_base2006 = 32.6
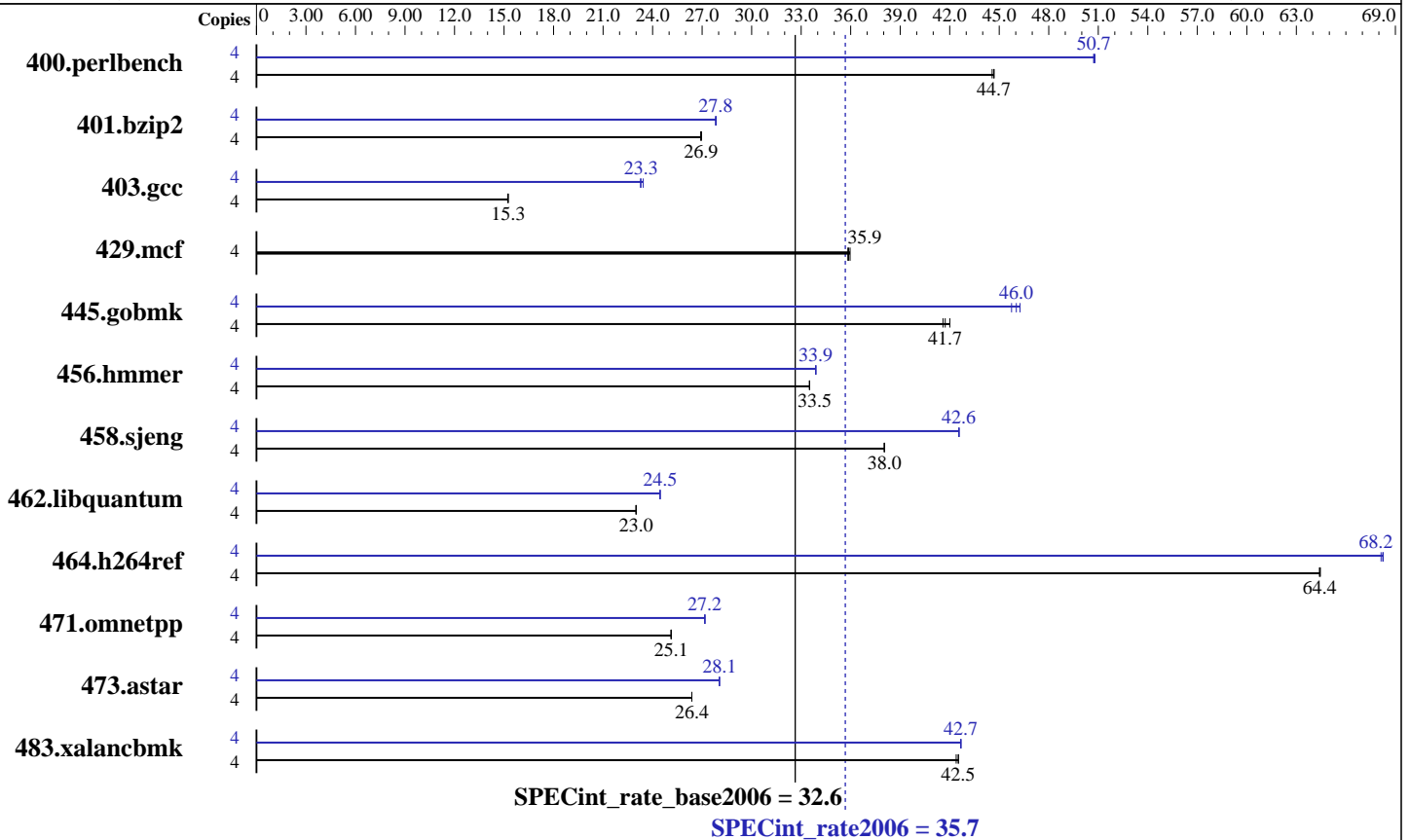
CPU2006 license: 3388

Test sponsor: ACTION S.A.

Tested by: ACTION S.A.

Test date: Aug-2007

Hardware Availability: Aug-2006

Software Availability: Jun-2007

Benchmark	Copies	Value
400.perlbench	4	50.7
	4	44.7
401.bzip2	4	27.8
	4	26.9
403.gcc	4	23.3
	4	15.3
429.mcf	4	35.9
445.gobmk	4	46.0
	4	41.7
456.hmmer	4	33.9
	4	33.5
458.sjeng	4	42.6
	4	38.0
462.libquantum	4	24.5
	4	23.0
464.h264ref	4	68.2
	4	64.4
471.omnetpp	4	27.2
	4	25.1
473.astar	4	28.1
	4	26.4
483.xalancbmk	4	42.7
	4	42.5

SPECint_rate_base2006 = 32.6

SPECint_rate2006 = 35.7

Hardware

CPU Name:	Intel Xeon 5110
CPU Characteristics:	1066 MHz system bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4 x 2 GB 667MHz CL5 DDR2 FB-DIMM SDRAM)
Disk Subsystem:	ST936701SS 36GB 10K SAS
Other Hardware:	None

Software

Operating System:	Windows 2003 Server Enterprise Edition Service Pack 1
Compiler:	Intel C++ Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.0

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	877	44.6	**875**	**44.7**	875	44.7	4	769	50.8	**770**	**50.7**	770	50.7
401.bzip2	4	**1433**	**26.9**	1432	27.0	1434	26.9	4	**1387**	**27.8**	1386	27.9	1388	27.8
403.gcc	4	2111	15.3	2116	15.2	**2111**	**15.3**	4	1374	23.4	**1382**	**23.3**	1384	23.3
429.mcf	4	**1017**	**35.9**	1014	36.0	1018	35.8	4	**1017**	**35.9**	1014	36.0	1018	35.8
445.gobmk	4	**1006**	**41.7**	999	42.0	1009	41.6	4	907	46.3	**912**	**46.0**	917	45.7
456.hmmer	4	1114	33.5	1114	33.5	**1114**	**33.5**	4	**1101**	**33.9**	1101	33.9	1101	33.9
458.sjeng	4	1273	38.0	1272	38.1	**1272**	**38.0**	4	**1137**	**42.6**	1137	42.6	1137	42.6
462.libquantum	4	3603	23.0	3604	23.0	**3604**	**23.0**	4	3389	24.5	3389	24.5	**3389**	**24.5**
464.h264ref	4	**1374**	**64.4**	1374	64.4	1373	64.5	4	**1297**	**68.2**	1299	68.1	1297	68.3
471.omnetpp	4	**995**	**25.1**	995	25.1	996	25.1	4	921	27.2	**920**	**27.2**	920	27.2
473.astar	4	1065	26.4	1065	26.4	**1065**	**26.4**	4	1001	28.1	1002	28.0	**1001**	**28.1**
483.xalancbmk	4	651	42.4	649	42.5	**649**	**42.5**	4	**647**	**42.7**	647	42.7	647	42.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
   Product description located as of 8/2007:
   http://www.actina.pl
   Binaries were built on Windows XP Professional SP2
   start command was used to bind processes to CPUs
```

Base Compiler Invocation

C benchmarks:
```
     icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
     icl -Qvc7.1
```

Base Portability Flags

```
        403.gcc: -DSPEC_CPU_WIN32
     464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

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Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
          -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
          -Qprefetch /F512000000 shlw32m.lib
                    -link /FORCE:MULTIPLE

   401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
          shlw32m.lib            -link /FORCE:MULTIPLE

    403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
                    -link /FORCE:MULTIPLE

    429.mcf: basepeak = yes
```

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Peak Optimization Flags (Continued)

```
245.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
           -Qprec_div- -Qansi-alias /F512000000
                      -link /FORCE:MULTIPLE

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
           -Qansi-alias /F512000000 shlw32m.lib
                      -link /FORCE:MULTIPLE

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
           /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE

462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
           -Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000
           shlw32m.lib             -link /FORCE:MULTIPLE

464.h264ref: Same as 456.hmmer
```

C++ benchmarks:
```
    -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
    -Qcxx_features /F512000000 shlw32m.lib
                     -link /FORCE:MULTIPLE
```

Peak Other Flags

C benchmarks:

```
    403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Dell-Intel-ic10-ia32.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Dell-Intel-ic10-ia32.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
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